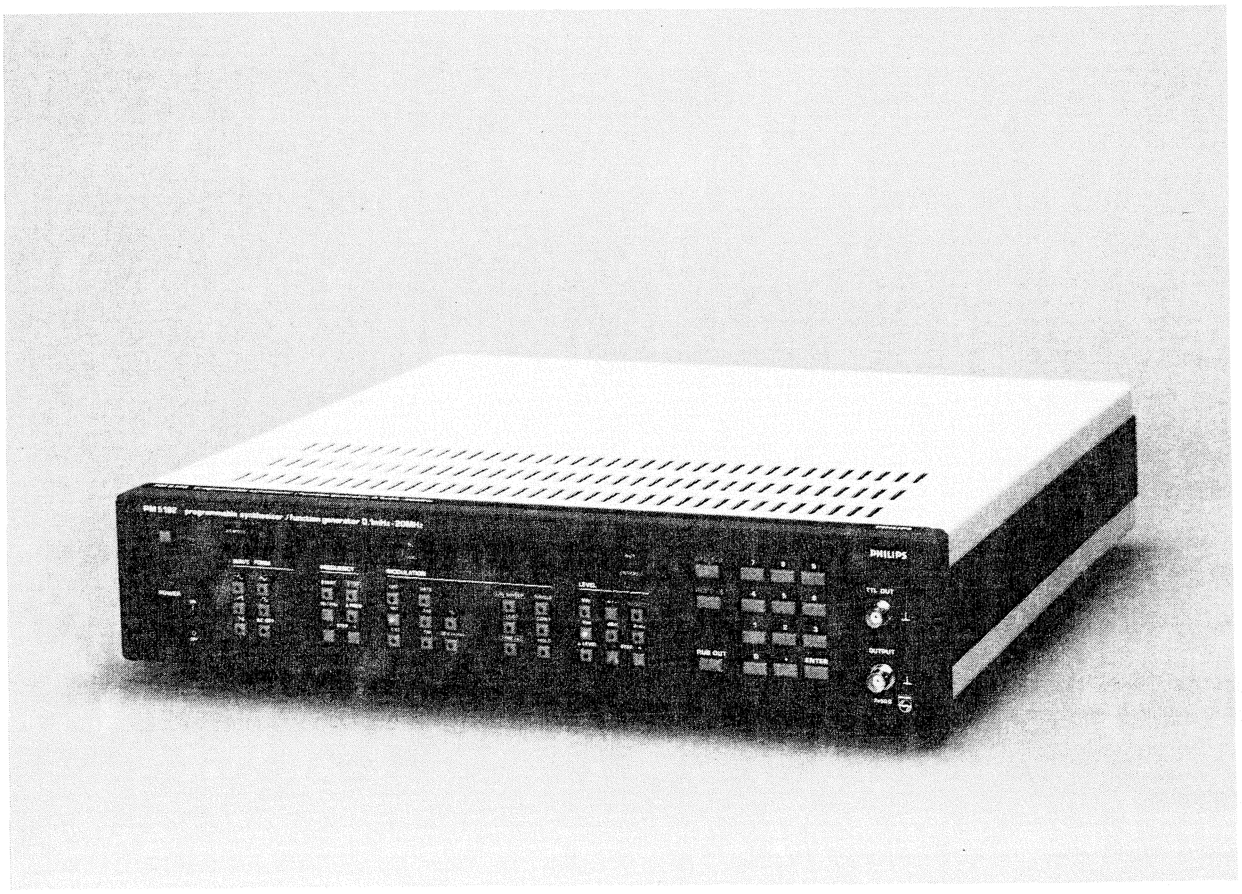
Programmable synthesizer / function generator 0.1 mHz – 20 MHz

PM 5192

9445 051 92001

Service manual

9499 455 00211
870801



PHILIPS

Please note

In correspondence concerning this instrument, please quote the type number and serial number as given on the type plate.

Bitte beachten

Bei Schriftwechsel über dieses Gerät wird gebeten, die Typennummer und die Gerätenummer anzugeben. Diese befinden sich auf dem Typenschild an der Rückseite des Gerätes.

Noter s. v. p.

Dans votre correspondance et dans vos réclamations se rapportant à cet appareil, veuillez toujours indiquer le numéro de type et le numéro de série qui sont marqués sur la plaquette de caractéristiques.

Important

As the instrument is an electrical apparatus, it may be operated only by trained personnel. Maintenance and repairs may also be carried out only by qualified personnel.

Wichtig

Da das Gerät ein elektrisches Betriebsmittel ist, darf die Bedienung nur durch eingewiesenes Personal erfolgen. Wartung und Reparatur dürfen nur von geschultem, fach- und sachkundigem Personal durchgeführt werden.

Important

Comme l'instrument est un équipement électrique, le service doit être assuré par du personnel qualifié. De même, l'entretien et les réparations sont à confier aux personnes suffisamment qualifiées.

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1. SAFETY INSTRUCTIONS

WARNING:
These servicing instructions are for use by qualified personnel only. To reduce the risk of electric shock, do not perform any servicing other then that specified in the Operating Instructions unless you are fully qualified to do so.

Read these pages carefully before installation and use of the instrument.

The following clauses contain information, cautions and warnings which must be followed to ensure safe operation and to retain the instrument in a safe condition.
Adjustment, maintenance and repair to the instrument shall be carried out only by qualified personnel.

1.1. SAFETY PRECAUTIONS

For the correct and safe use of this instrument it is essential that both operating and servicing personnel follow generally-accepted safety procedures in addition to the safety precautions specified in this manual. Specific warning and caution statements, where they apply, will be found throughout the manual. Where necessary, the warning and caution statements and/or symbols are marked on the apparatus.

1.2. CAUTION AND WARNING STATEMENTS

CAUTION:
Is used to indicate correct operating or maintenance procedures in order to prevent damage to or destruction of the equipment or other property.

WARNING:
Calls attention to a potential danger that requires correct procedures or practices in order to prevent personal injury.

1.3. SYMBOLS

⏚	Protective earth (grounding) terminal	(black symbol on yellow background or impressed, e. g. at the mains connector at the rear)

1.4. IMPAIRED SAFETY-PROTECTION

Whenever it is likely that safety-protection has been impaired, the instrument must be made inoperative and be secured against any unintended operation. The matter should then be referred to qualified technicians.
Safety protection is likely to be impaired if, for example, the instrument fails to perform the intended measurements or shows visible damage.

1.5. GENERAL CLAUSES

WARNING:
The opening of covers or removal of parts, except those to which access can be gaines by hand, is likely to expose live parts and accessible terminals which can be dangerous to live.

The instrument shall be disconnected from all voltage sources before it is opened.

Bear in mind that capacitors inside the instrument can hold their charge even if the instrument has been separated from all voltage sources.

WARNING:
Any interruption of the protective earth conductor inside or outside the instrument, or disconnection of the protective earth terminal, is likely to make the instrument dangerous.
Intentional interruption is prohibited.

Components which are important for the safety of the instrument may only be renewed by components obtained through your local Philips organisation (see also chapter 9.).

After repair and maintenance in the primary circuit, safety inspection and tests, as mentioned in chapter 9 have to be performed.

1.6. CONNECTIONS

The circuit earth potential is applied to the external contacts of the BNC sockets and is connected to the cabinet by means of parallel-connected capacitors. By this means hum loops are avoided and a clear HF earthing is obtained.

If the circuit earth potential in a measurement set-up is different from the protective earth potential, it must be noticed,

- that the BNC sockets can be touched and that it must not be live, see the safety regulations on the subject (VDE 0411),
- that all sockets marked with the sign ⊥ are internally interconnected.

2. MAINS VOLTAGE SETTING AND FUSES

The safety instructions in previous chapters must be followed.

PM 5192: On delivery from the factory the instrument is set to 220 V – AC.
PM 5192 M (USA): On delivery from the factory the instrument is set to 120 V – AC up to ser. no. LO-01690 or to 120 V – AC from ser. no. LO-02691 onwards.

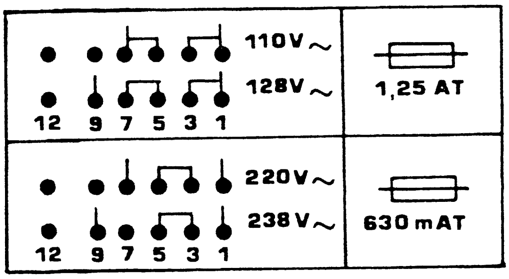
If the instrument is to be used on a different supply voltage the wiring must be altered; the main fuse should be replaced dependent on the mains voltage. The wiring for the fan must not be altered.

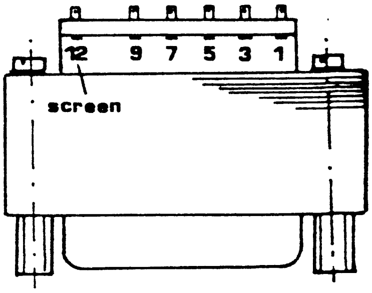
Proceed as follows:

- Loosen 2 cross-slotted screws at the rear side of the instrument (see also chapter 7.1.).
- Remove the top cover.
- Remove the isolating cover of the topside of the mains transformer, remove cable binder before.
- Alter the wiring of the mains transformer according to the connection diagram.
- Refit the isolating cover.
- If necessary, insert the advised fuse into the fuse holder instead of the fuse built-in. In this case change current label of the fuse holder.
- Change the mains voltage label at the rear of the instrument in accordance with the mains voltage selected.
  The labels for the mains voltage, current and the fuse are enclosed in a plastic bag.
- Close the instrument.

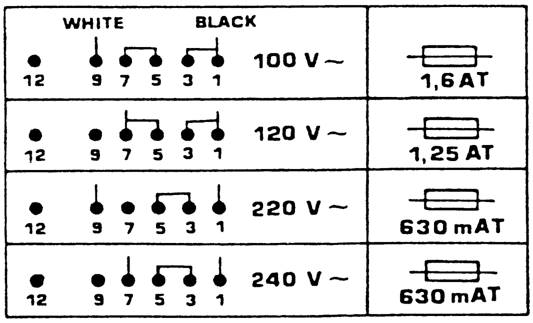
Connection diagram

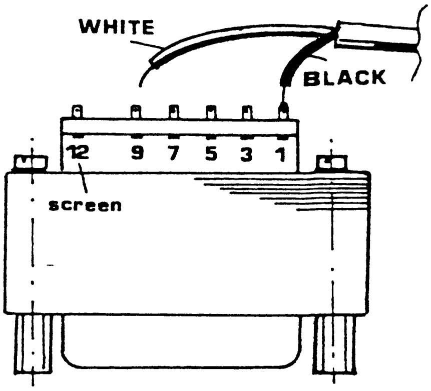
Up to series number LO-01690


110V~
128V~
12 9 7 5 3 1
1,25 AT
220V~
238V~
12 9 7 5 3 1
630 mAT


12 9 7 5 3 1
screen

From series number LO-02691 onwards:


WHITE
BLACK
100 V~
12 9 7 5 3 1
1,6AT
120 V~
12 9 7 5 3 1
1,25 AT
220 V~
12 9 7 5 3 1
630 mAT
240 V~
12 9 7 5 3 1
630 mAT


WHITE
BLACK
12 9 7 5 3 1
screen

3. OPERATING PRINCIPLE, Fig. 30 (Block diagram)

3.1. GENERAL OPERATING PRINCIPLE

The basic functional units, performing the generation, processing and conditioning of the generator output signals, are named

– DFS, Digital Frequency Synthesizer,	on unit 2
– PLL, Phase Locked Loop,	on unit 1
– MODULATOR	on unit 1
– PULSE GENERATOR	on unit 1
– AMPLIFIER	on unit 1

These functional units are under control of the CPU (Central Processing Unit), consisting of a microprocessor and its peripheral components on unit 2. Primary control data for the CPU is derived from the front-end KEYBOARD & DISPLAY on unit 3 or from an external controller via the IEEE/IEC bus interface. The output-signal parameters are displayed numerically on a 7-segment-LED display. Key LEDs are provided for operating mode indication. Subsequently a brief description of the over-all block diagram (fig. 2) of the generator is given.

3.2. DESCRIPTION OF THE BLOCK DIAGRAM

DFS

In the frequency range up to 2147 kHz the primary signals – sine, triangular, positive and negative sawtooth waves – are generated by direct digital signal synthesis.

Binary samples of the wave are created in the SIGNAL SYNTHESIZER section and converted to analogue voltages by a fast DAC at the clock rate fc. The output frequency fo is directly related to fc, according to

$$fo = 0.1 \cdot N \cdot 2^{-33} \cdot fc = N \cdot 10^{-4}\ \mathrm{Hz}$$

where N is the decimal equivalent of the binary frequency word, routed to the SIGNAL SYNTHESIZER from the CPU via U2–CONTROL BUS. fc is generated by an x-tal oscillator, the 8.59 MHz CLOCK. The AUTOMATIC SWITCH alternatively routes the external clock frequency to the SIGNAL SYNTHESIZER, if this is applied to the CLOCK INPUT. The DAC output signal is smoothed by the 3 MHz LPF, an anti-aliasing low-pass filter.

PLL

In the frequency range above 2147 kHz the primary sine wave is generated in the PLL. The PLL consists of a broad-band VCO, Voltage-Controlled Oscillator, – with a triangular-wave output signal fed to the SINE SHAPER – the FREQUENCY DIVIDER, the PHASE DETECTOR and the LOOP FILTER. By the PLL the PLL REF frequency – generated in the DFS – is multiplied by a factor of 4096 in FM mode and 16 otherwise. For fast phase-locking response the VCO is preset roughly by the DAC to the programmed frequency.

MODULATOR

By the VOLTAGE CONDITIONER the DFS sawtooth wave is halved in amplitude and shifted in dc, resulting in unipolar signals. The sine wave and the triangular wave are routed without change through the VOLTAGE CONDITIONER to die out SWITCH 1. In the frequency range above 2147 kHz the RF SINE wave is routed from the PLL to DIODE SWITCH 2 and to the AMPLITUDE MODULATOR or directly to the AMPLIFIER. Both diode switches are served by the SWITCH CONTROL, which evaluates the accurate control signal from the 2 MHz SWITCH control signal from the CPU and the GATE signal from the SWITCHING CIRCUITRY in the GATE mode of the generator.

In internal GATE, AM or FM mode the modulating signal is derived from the MODULATION OSCILLATOR output. The output sine wave is scaled in amplitude by the AMPLITUDE CONTROLLER to give the accurate AM or FM modulation depth. The modulating sine wave is fed to the AMPLITUDE MODULATOR in AM mode or to the PLL in FM mode through the SWITCHING CIRCUITRY. Alternatively – in the external modulation modes – the modulating signal is supplied from the generator MODULATION INPUT.

PULSE GENERATOR

The PULSE GENERATOR basically represents an electronical switching circuitry, creating a TTL signal and a square wave, each signal with a 50 % duty cycle. The instants of the positive and negative edges of these signals are determined by the zero-crossings of the reference input signal. In the frequency range up to 2147 kHz the DFS signal, e. g. a sine wave, fed to the ZERO CROSSING DETECTOR serves as reference. Above 2147 kHz the TTL output signal of the PLL, named RF TTL, directly determines the switching points.

By the CONTROL CIRCUITRY either the TTL output of the ZERO CROSSING DETECTOR or the RF TTL are routed to the switching output of the signal conditioners. The TTL OUTPUT STAGE and the SQUARE WAVE CONDITIONER are creating the TTL output voltage of the generator and the primary square wave with accurate amplitude and waveform.

AMPLIFIER

The vernier setting of the generator output amplitude is performed by the AMPLITUDE CONTROLLER. After amplification by the POWER AMPLIFIER the signal either directly or after 20 dB respectively 40 dB attenuation by the STEP ATTENUATOR is routed to the OUTPUT socket. The DC GENERATOR adds the programmed dc voltage.

CPU

An 8-bit microprocessor (8031) and a 10 MHz clock are the constituents of the PROCESSOR & CLOCK. The PROGRAM MEMORY is a 16 Kbyte EPROM. In an external data memory, the 256 byte RAM, the 10 storage register contents of the generator are stored. By the CONTROL BUS DRIVER the required load capability of the U1- and U2 CONTROL BUS serial data linie (SDA), and the clock line (SCL), is achieved. The device selecting strobe signals STR1 ... 15 – used for CPU components and latching-data – shift registers in the various functional units controlled by the CPU – are derived from 4 ports of the PROCESSOR by the STROBE DECODER.

By the DIRECT PORT LATCH two output port signals – 2 MHz SWITCH and PLL CNTL – are derived from three address/data bus lines of the CPU. The SWEEP VOLTAGE DAC is generating a voltage ramp during a frequency sweep. The PEN LIFT SWITCH serves for lifting the writing pen of an x-y plotter during frequency sweep fly-backs.

The IEEE/IEC bus interface of the generator consists of the IEC BUS CONTROLLER, the DEVICE ADDRESS LATCH & SHIFT REGISTER and the 3-STATE GATE & LATCH.

4. PERFORMANCE CHECK

4.1. GENERAL INFORMATION

WARNING:
Before switching on, ensure that the instrument has been installed in accordance with the instructions outlined in Section 2 of the Operating Manual: Installation instructions.

This procedure is intended to:

- check the instrument
- be used for incoming inspection to determine the acceptability of newly-purchased instruments and/or recently-recalibrated instruments.

ATTENTION:
The procedure does not check every detail of the instrument's calibration; rather, it is concerned primarily with those parts of the instrument which are essential to measurement accuracy and correct operation. Removing the instrument covers is not necessary to perform this procedure. All checks are made from the front panel.

If this test is started within a short period after switching on, bear in mind that steps may be out of specification, due to insufficient warming-up time. To avoid this situation, allow the specified warming-up time of 30 min.

4.2. POWER-ON SELFTEST

Immediately after power on a selftest routine is started with which PROM and RAM are tested. If an error is detected one of the following error messages appears:

ERR 1	PROM	checksum error
ERR 2	RAM	(processor) checksum error
ERR 3	RAM	checksum error; operation possible but memory centents is destroyed.

In case of no error in PROMs/RAMs all LEDs and all segments of the displays are then switched on for appr. 3s after the software version has been indicated in the 'LEVEL'-sector of the display. The instrument must then be in the on-state which is indicated by a zero in each of the display sections and the LEDs in the keys SINE, START, OFF and Vpp switched on.

4.3. GENERAL FUNCTIONAL TEST

The function of the synthesizer can now be checked with the help of the following examples:

amplitude = 5 Vpp; offset = 0 Vdc

terminate the output with 50 Ω and connect an oscilloscope.

Wave form	start	stop	modulation			defective unit in case of faulty function
	frequency		—mode	—frequency	-depth, -deviation sweep time,	
1) triangle	50 kHz	—	—	—	—	DFS
2) sine	500 kHz	—	AM	1 kHz	50 % depth	modulator
3) square	5 MHz	—	FM	1 kHz	100 kHz dev.	modulator, PLL pulse generator
4) pos. sawtooth	1 MHz	10 MHz	Sweep Lin.	—	5 sec. sweep time	CPU, DFS

If one of the functions doesn't work, the diagnostic program can be a help to distinguish whether the defect is in the unit in question or in the CPU with its C-bus drivers/decoders. By selecting TEST 4 (strobe test) it is possible to check the data communication lines and the decoders of the subunits.

In case that all functions are o. k. this test must be continued by checking the output signals:

TTL OUT: This output shows always a square wave voltage with TTL-level and signal-frequency.

INT CLOCK: This output contains the clock-signal of the internal digital frequency synthesizer with TTL-level and a frequency of 8.58993 MHz.

MODULATION: This output shows a sinewave signal with an amplitude of max. 1 Vrms depending on modulation depth/deviation and modulation frequency.

PEN LIFT: In the continuous sweep mode this output shows a sequence of pulses. The frequency corresponds to the sweep repetition rate, amplitude is 20 Vpp.

SWEEP: During continuous sweep operation this output shows a sawtooth voltage with an amplitude of 10 Vpp.

5. DIAGNOSTIC-PROGRAM PM 5192

This test program contains 5 submodules:

TEST 1: Display and LED test
TEST 2: Keyboard test
TEST 3: Storage register test
TEST 4: Strobe test (test of the internal interfaces)
TEST 5: Test for the IEEE/IEC-BUS interface

To activate this test program, press the key MODULATION OFF while power is switched on and keep it pressed for about 3 seconds.

The return to the main operating mode is only possible by switching power OFF and ON again.

When the test program is activated, the display shows "TEST x" where 'x' is a number from 1 to 5. This number changes continuously and slowly, and by pressing the key MODULATION OFF at the right moment, the respective test-submodule will be started.

To leave the test submodules, press the key MODULATION OFF for about 2 seconds.

TEST 1 : Display and LED test

Step 1: 7-segment-display

All display segments and LEDs are switched on for about 2 seconds.
After this the program starts to switch on one segment after the other for four display positions simultaneously. Finally, the decimal points of these four positions remain lit and the program starts to do the same with the next four display positions.
After the last four digits were tested, the program switches on all segments and LEDs and remain in this state until the key MODULATION OFF was pressed once again.

Step 2: LEDs

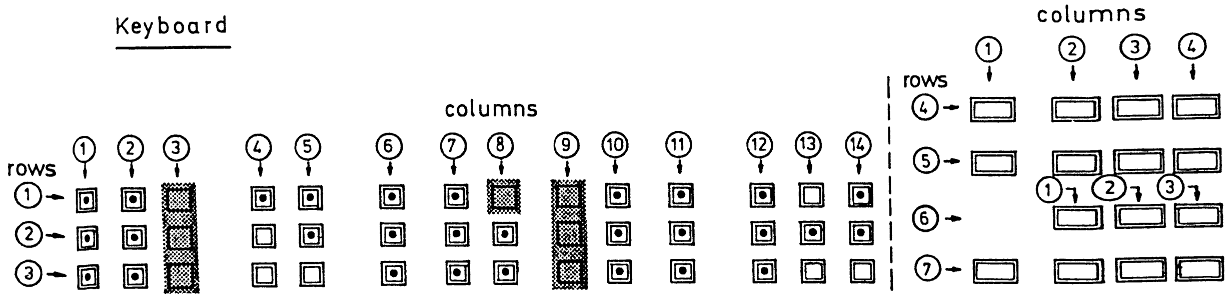
All LEDs will be switched on sequentially, one after the other, for approx. 0.5 seconds beginning with the uppermost left one (inside the key sine wave). When the last LED was switched on the indication "End" appears at the display until the key MODULATION OFF was pressed. Then the program returns to the test-menu.

TEST 2: Keyboard test

The display shows the indication: 1 – 01 – – – –

Now you must press the first key of the first row: 1 – 01 – – – –
(row 1) – (column 1)

Keyboard



When the right key was pressed, the display shows

1 – 01   1 1 1 1

for about 1 second and changes then to

1 – 02   – – – –

as a request to press the second key in the first row. In case of a failure, the display would show

Err 1 – 01   x – xx

where x – xx indicates the wrong code (row and column). This error indication will only be reset by pressing the requested key – in case of a hardware failure at the keyboard unit it would not be possible to get the right code and thus to reset the error message.

When the last key was pressed, the keyboard test is finished and the display indicates "End". To leave this diagnostic submodule and to get back to the test menue, the key MODULATION OFF must be pressed.

TEST 3 : Storage register test

Attention:
This memory test damages the register contents. When the instrument is switched on after the storage register test was executed, the display indicates "Err 3" which means that there are now no parameters in the storage register – the complete contents (parameters) are destroyed.

The display indicates

MEMO 1 –

and the program starts to write a test pattern into each location of memory chip 1, reads it again, and checks this value for correctness. When no failure was detected, the same will be done with a second pattern. In case that there is no failure, the display shows

MEMO 1 – 1

and in case of a failure

MEMO 1 – 0

Now the program waits until the key MODULATION OFF is pressed and starts then to check the memory chip 2 in the same way as described above. When this is terminated successfully, the display indicates

MEMO 2 – 1

or in case of a failure

MEMO 2 – 0

With MODULATION OFF the program returns to the test menu.

TEST 4 : Strobe test

The display indicates

STRO x

where x is a number from 6 to 15. This number changes continuously and slowly. By pressing the key "MODULATION OFF" at the right moment the required strobe line will be selected. The display shows then e.g.:

STRO 08 – 1

which means that the output lines of the shift registers controlled by strobe line 8 show a specific bit-pattern. If MODULATION OFF was pressed once for a short moment all output lines of the shift registers change their state. Now the display shows:

STRO 08 – 0

Each time the MODULATION OFF-key is pressed for a short moment, the states of these output lines will be inverted. If MODULATION OFF is pressed for longer than about 1 second, this subprogram will be left and the display shows again:

STRO x

If the key MODULATION OFF is pressed again for longer than about 1 second, the program will return to the test menu.

This strobe test serves fault finding in the internal C-bus system. Measuring points, positions of ICs and measuring values are given in the following tables.

By strobe lines controlled ICs show the following bit patterns during STROBE-test:

Bit pattern ICs HEF 4094								
Pin no.	4	5	6	7	14	13	12	11
Strobe 'x' – 0	0	1	0	1	0	1	0	1
Strobe 'x' – 1	1	0	1	0	1	0	1	0

Association of these ICs:

Strobe line	Controlled ICs (Pos. no.)	Location	
Strobe 7	307, 308, 309, 310, 311	"DFS",	unit 2
Strobe 8	302, 306, 312	"Output Amplifier",	unit 1
Strobe 9	302, 313, 316	"Modulator",	unit 1
Strobe 11	301	"PLL",	unit 1
Strobe 13	321	"SWEEP" CPU,	unit 2
Strobe 15	311	"IEC-bus, function and address" CPU,	unit 2

Bit pattern IC N74LS175		
Pin no.	2	7
Strobe 'x' – 0	1	0
Strobe 'x' – 1	0	1

Association of this IC

Strobe line	Controlled IC (Pos. no.)	Location
Strobe 12	313	"Direct port" CPU, unit 2

Bit pattern IC HEF 40373								
Pin no.	2	5	6	9	12	15	16	19
Strobe 'x' – 0	0	1	0	1	0	1	0	1
Strobe 'x' – 1	1	0	1	0	1	0	1	0

Association of this IC

Strobe line	Controlled IC (Pos. no.)	Location
Strobe 14	308	"IEC-bus out" CPU, unit 2

Strobe lines 6 and 10 are not used and have no control function.

TEST 5 : IEC-bus test

The display shows the indication

IEC BUS

Each character sent from the controller via the IEC (IEEE)-bus will be decoded and displayed with its hexadecimal code, e. g.

ASCII 'A' indication 41 H
ASCII '3' indication 33 H
etc.

The device address of the PM 5192 is fixed to 20.

With the key MODULATION OFF the program returns to the test menu.

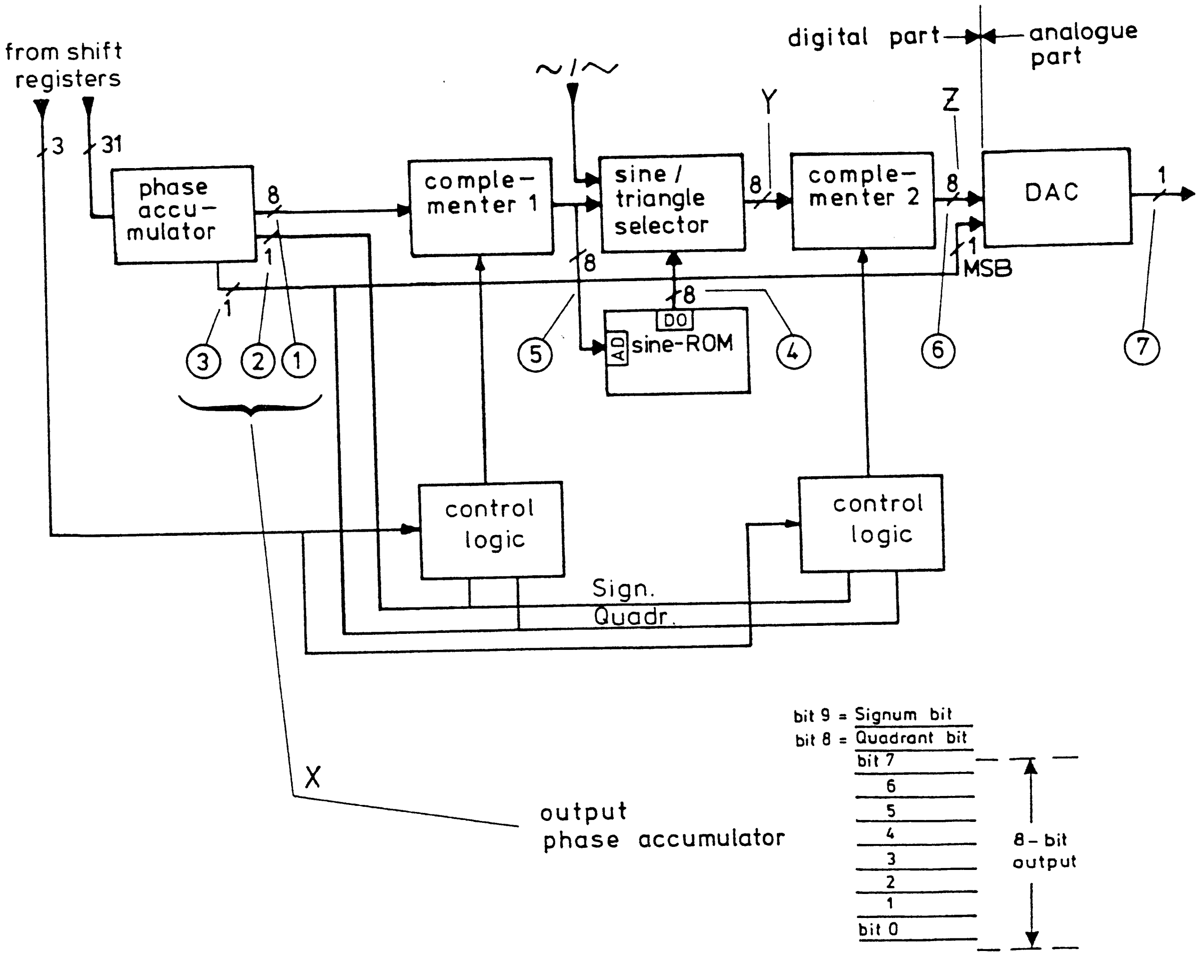
6. CIRCUIT DESCRIPTIONS

6.1. DIGITAL FREQUENCY SYNTHESIZER/U2

Signal Synthesizer

The primary signal of PM 5192 in the frequency range up to 2.147 MHz is generated in the digital frequency synthesizer (DFS). At the output of the digital section of the DFS the signal is presented as a sequence of 9 bit binary numbers. The digital samples of the signal are then converted to analogue voltages by means of a fast DAC.

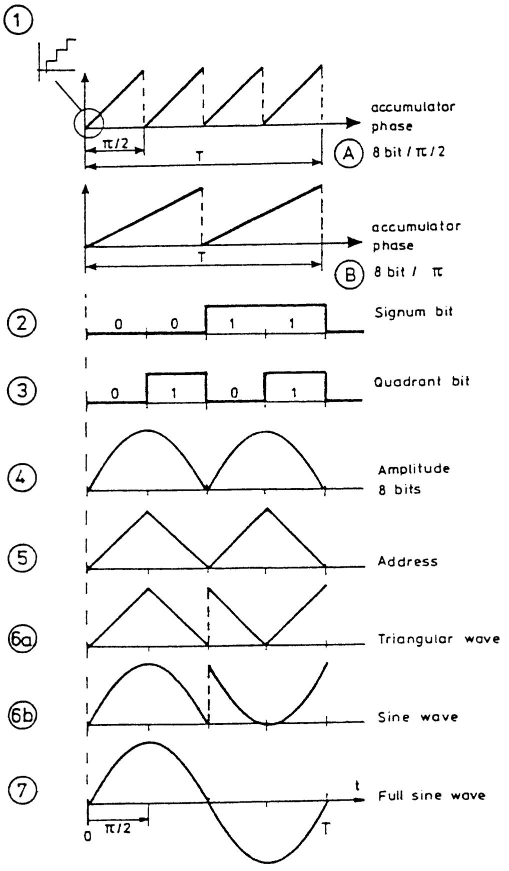
The frequency of the DFS is determined by a 40 bit frequency word which is sent to the shift registers 307 – 311 from the CPU. The bit parallel shift register outputs are connected with the phase accumulator inputs. The phase accumulator is clocked by 8.5899 MHz from the clock generator. With each clock pulse the 9 bit-phase-accumulator output is incremented by the value of the frequency word. The resulting sequence of binary numbers represents a periodic sawtooth wave. By intermittant one's complementing (complementer 1) the signal is converted to a triangular wave. The samples of this signal are used as addresses for reading out a sine table ROM. The output is representing sine wave values for the first quarter period (4). By intermittant one's complementing in complementer 2 this signal is converted to a full sine wave (7).


from shift registers
3
31
phase accu-mulator
8
1
1
③ ② ①
comple-menter 1
∿/⁄∿
sine / triangle selector
8
8
DO
AD
sine-ROM
⑤
④
digital part
analogue part
Y
Z
comple-menter 2
8
8
1
MSB
DAC
1
⑥
⑦
control logic
control logic
Sign.
Quadr.
X
output phase accumulator
bit 9 = Signum bit
bit 8 = Quadrant bit
bit 7
6
5
4
3
2
1
bit 0
8-bit output

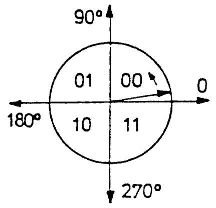
The phase accumulator is functionally divided into two parts. The upper part consists of the adders 312 – 319 and the D-registers 322 – 326 for the frequency range 1 mHz – 2.147 MHz. Frequency values for this range are sent in the 1 – 2 – 4 – 8 code. The lower part consists of the adders 320 and 321, the NOR gate 305 and the D-register 326 and covers the range 0.1 – 1 mHz. For this range the frequency values are applied in the Excess – 3 code.

The upper part of the phase accumulator generates a sequence of 33 bit binary numbers from 0 to $2^{33} - 1$. With each clock pulse the output is incremented by the value of the input frequency word. When reaching the upper limit the accumulator output is reset and starts again with incrementing. This results in a cyclic sequence of binary numbers which have a sawtooth wave form character. The frequency is $fg = 1/T = \delta \cdot tc \cdot 2^{-L}$ where δ is the value of the frequency word, fc the clock frequency (8.5899 MHz) and L the length of the phase accumulator (33 bit).

The upper 10 bit of the phase accumulator are used for subsequent signal processing. The samples of the lower 8 bit of them represent a sawtooth wave with the period T/4 (1 A). The upper two of the 10 bit accumulator output (signum bit (2), quadrant bit (3)) determine the quadrant in which the vector of the DFS output signal (7) is actually located. In complementer 1 (exclusiv – or gates 331, 332) the 8 bit output is inverted during the second and fourth quarter period. The resulting signal (5) is fed to the sine/triangle selector (ICs 336, 337) and parallel to the address lines of the sine-ROM in which the sine wave values for the first quarter period (0 – $\pi/2$) are deposited.



①
π/2
T
accumulator phase
Ⓐ 8 bit / π/2
T
accumulator phase
Ⓑ 8 bit / π
②
0 0 1 1
Signum bit
③
0 1 0 1
Quadrant bit
④
Amplitude 8 bits
⑤
Address
6a
Triangular wave
6b
Sine wave
⑦
t
Full sine wave
0 π/2
T



90°
01
00
0
180°
10
11
270°

Resulting signal phase

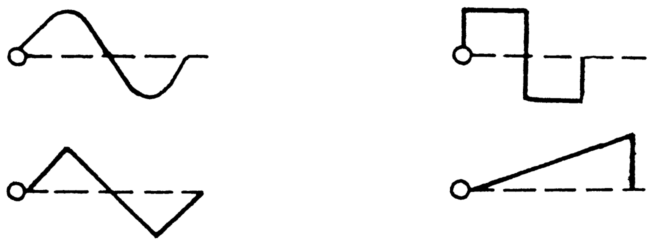
For generating the wave form triangle the output of complementer 1 is fed directly to the sine/triangle selector by-passing the sine ROM. Thus the sawtooth signal (5) applied to complementer 2 results in the wave form (6A).

For sine wave the output data of the sine ROM (4) is picked up by the sine/triangle selector and routed to complementer 2 resulting in wave form (6b). By adding the inverted signum bit (2) as the 9th bit, it results in wave form 7.

When generating positive or negative sawtooths the value of the binary frequency word at the phase accumulator input is halfed – thus the sawtooth period at the phase accumulator output is doubled. For positive ramps control signal "a" is low. Therefore bit 0 – 7 are routed through complementer 1 without inversion to buffer 333 and 334. After passing sine triangle selector 336/337, the buffer 338/339 and complementer 2 without inversion (b = 0) the signal is latched to the output by the D – FFs 342/343. The ninth bit at output 2/342 is directly derived from bit 8 of the phase accumulator through MUX 347 and the buffers 333 and 338 (= signal c).

For negative going ramps one difference is that control signal "a" is high. Therefore the signal (bit 0 – 7) is inverted by complementer 1. The second difference is that the ninth output bit (= c) is inverted by exclusive – or gate 327 (pins 8, 9, 10) at input pin 2 of MUX 347.

With 'low' at the reset input the phase accumulator output can be set to zero. After reset is switched back to 'high', the signal generation is started and performed in the way as described before. The starting conditions are shown in the picture below.

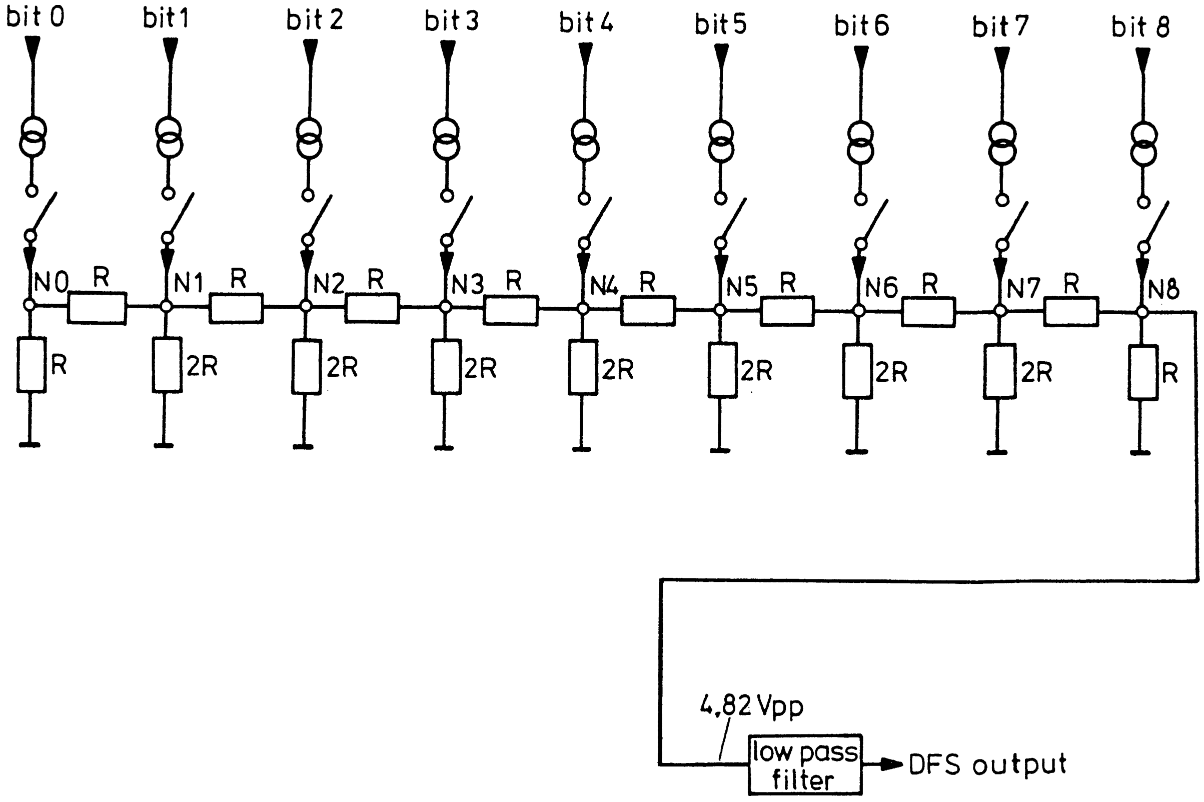


DAC

The binary signal coming from complementer 2 via buffers 342, 343 is converted to an analog voltage in the DAC.

At the DAC inputs the drivers 344, 345 are located which are controlling the current switching differential transistor stages 427 – 428, 424 – 425, . . . The DC-currents (each 5.85 mA = I_0) are generated by individual circuitries. The MSB (bit 8) – current source for instance includes transistor 427 and one OP of IC 346. The bit 7 – current source includes transistor 424 and the other OP of IC 346. The remaining current sources use also this second OP of IC 346. The individual currents – if switched on – are routed to the corresponding nodes N0 – N8 of the R – 2 R ladder shown below. The input impedance of each node is 2 R/3, where R is 619 ohm (2 R/3 = 412, 67 ohm). Therefore each current – if switched on – is building up the same voltage at the related node ($I_0 * 2\ R/3 = 2{,}41\ V$). The transformation to the output depends on the node position in the ladder. Generally from node n to node n+1 the voltage is divided by two. Thus for instance the bit 3 – node N3 voltage is transformed to the output node N8 by the factor $2^{-(8-3)} = 1/32$. The output voltage of the ladder at node N8 is filtered by the anti alias low pass filter 803, 804, 559 – 562. By this filter with a passband of about 3 MHz especially the spectral contents at both sides of the clock frequency (8.5899 MHz) are suppressed. The output signal is buffered with the transistors 430, 431.

For deglitching purposes of the DAC the current switching point of bit 8 can slightly be shifted with the trimpot 689 versus the current switching points of bits 0 – 7. The latter switching points are set with trimpot 676. By iterative calibration procedure with these both trimpots the sine wave distortion is minimized.

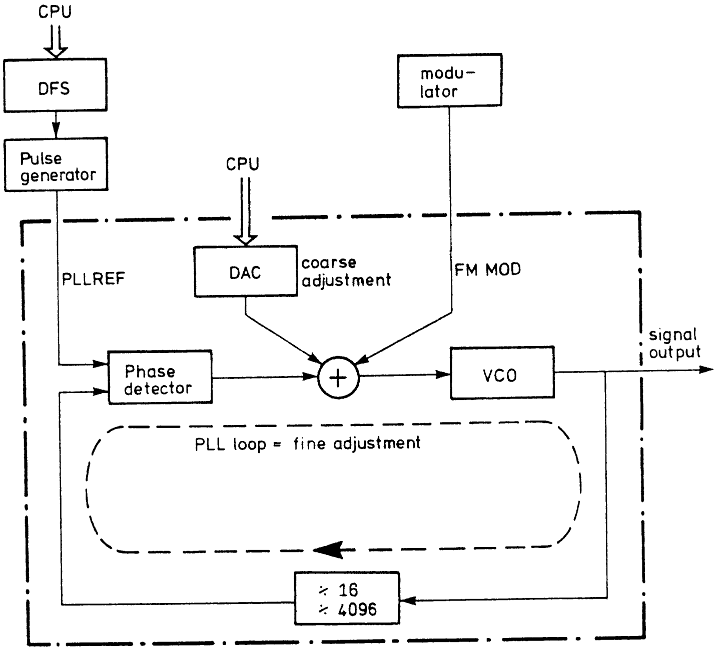

bit 0
bit1
bit 2
bit3
bit 4
bit5
bit6
bit7
bit 8
N0
N1
N2
N3
N4
N5
N6
N7
N8
R
2R
4,82 Vpp
low pass filter
DFS output

6.2. PLL / U1

In the frequency range above 2.147 MHz the primary signal is generated in the PLL. The PLL consists of a wide-band VCO, frequency divider, phase detector with loop filter and sine shaper.

DAC

Frequency coarse adjustment in the range above 2.147 MHz is achieved with the DAC which gets the information from the CPU via the C-bus, fine adjustment takes place via the PLL-loop by comparing the reference signal from the DFS with the VCO output divided by either 16 (= FM off) or 4096 (= FM on).



Phase Detector

The phase detector IC 309 compares the signals PLL REF and VCO output and generates a DC-signal which value depends on the phase difference between them.

The integration circuit following the phase detector consists of the integrated circuit 312, the 10 kΩ resistor 619 and the capacitors 516, 521 and 522. The purpose of the integrator is to filter spikes and short deviations in order to prevent disturbances of the PLL control loop. Time constant for FM off mode is 47 µs (619, 516), with FM on it is 0.5 s (619, 521, 522). Switching FM on and off is achieved with control signal PLL CNTL and switch 313/1, 2, 15. With FM off the capacitors 521, 522 are continuously charged with OP-amp 311 according to the level at input 3 of the integrator. This effects a short lock-in time when FM is switched on, because the capacitors 521, 522 are always charged to the level according to the momentary frequency. The output of the integrator is passed via solder switch S6 and resistor 608 to the current summing point of the VCO.

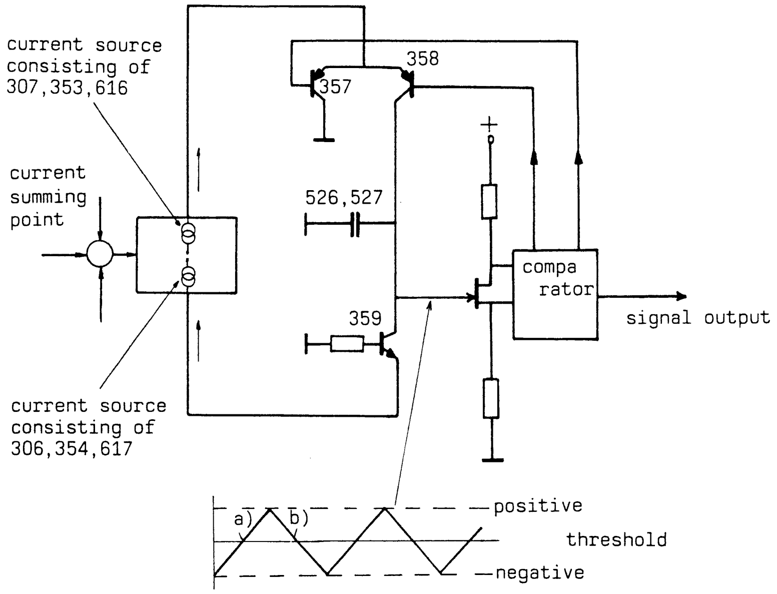
The DAC for frequency coarse adjustment consists of the shift register IC 301 and the digital-analog converting circuit IC 302. The serial information from the CPU is received from the shift register, formed to a parallel information and fed to the parallel inputs of IC 302. The output of this circuit is buffered with OP-amp 303 and then passed via resistor 607 to the current summing point.

The last signal which is fed to the current summing point is generated in the modulator and passed via resistor 606. This FM MOD signal effects an amplitude modulation of the integrator – and DAC output signals at the current summing point, whereby the influences of these three signals in the summing point are weighted by the different values of the resistors 606, 607, 608. Lower resistance values effect a higher current going into the summing point thus giving a higher influence for the VCO. The highest current is delivered from the DAC via 607 (= 10K) followed by the phase detector output via 608 (= 75K) and the modulation signal FM MOD via 606 (= 365K).

VCO

The VCO consists of the input buffer 304, a current mirror with two current sources 306, 307, 351, 353, 354 and 356 an amplifier 361, 362, 363, 380 and a comparator 364, 366, 367 and 368.

The two current sources generate the currents 'I' and '2I', whereby the value of 'I' depends on the current in the summing point, the relation between the two currents is exactly 1 : 2. The current '2I' is going via transistor 356 to the transistors 357, 358 which are controlled from the comparator output. During the rising ramp (a) transistor 358 is open and feeds the current '2I' to the capacitors and to the transistor 359 which draws the current 'I' from this point, thus the two capacitors 526, 527 are charged with the current 'I'. During the falling ramp (b) transistor 357 is open and leads the current of '2I' to ground. Because the second current source draws the current 'I' continuously from the two capacitors 526 and 527, they are now discharged until the negative threshold is reached.



The stage following the triangular wave generator is the sense amplifier consisting of the transistors 361, 362, 363 and 380. This amplifier has a very high input impedance (> 1 GΩ), a total gain of 1 and supplys the inputs of comparator and sine shaper. The signals at these inputs have precisely the same phase as the signal at the capacitors 526, 527 and an amplitude of 2 Vpp.

The comparator consists of the differential stage 364, 366 and the TTL switching circuit 367, 368. The two signals to be compared are fed to the bases of the transistors 364 and 366. The collectors of these transistors are controlling the bases of the transistors 367 and 368 thus generating a square wave signal which is connected to the base of transistor 366 via resistor 655. This effects the comparing of the positive ramp of the triangular wave with the positive half cycle of the square wave and of the negative ramp with the negative half cycle.

When the positive ramp of the triangular wave at the base of transistor 364 reaches the upper threshold determined by the level of the square wave at the base of transistor 366, the comparator switches and with this the collector of transistor 364 becomes more negative. This effects the switching of transistor 357 to the conducting state. Thus the current '2I' is drawn to ground, the capacitors 526 and 527 are discharged with the current 'I' and the triangular signal turns over to the negative ramp.

The RF TTL output is supplied from the TTL switching circuit in the VCO (367, 368) via decoupling transistor 379. This signal is then formed in the signal conditioner 369, 371 and fed to the output via the two buffers 314/1, 2, 3 and 314/11, 12, 13.

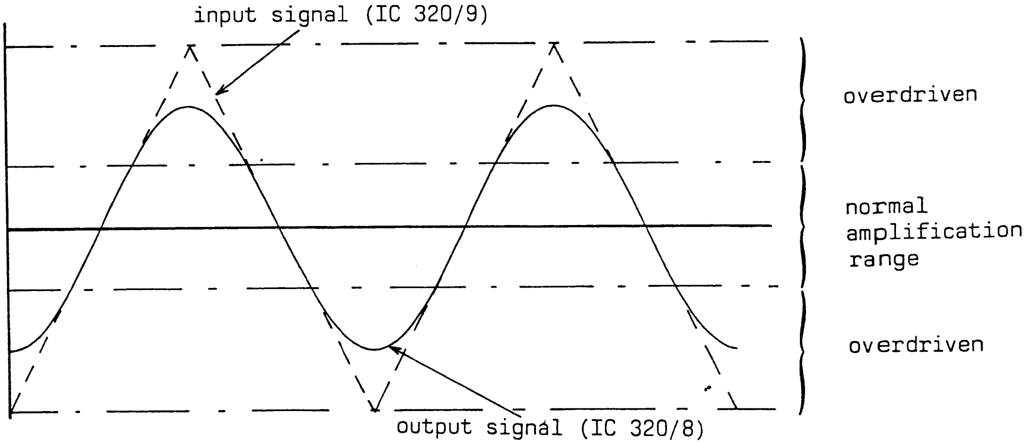
Frequency divider

The frequency divider of the PLL is also supplied from the TTL-output of the VCO (IC 314/3). This circuit serves the purpose to divide the VCO output frequency by the factor 16 for FM off and 4096 for FM on. Dividing takes place in four steps with the two dividing circuits 316 and 317, the divided value is available at pin 6 of IC 318 (a) and fed to input 1 of phase detector IC 309. Switching over from dividing factor 16 to 4096 is achieved via the gates 318/1, 2, 3 and 318/8, 9, 10 with the signal PLL CNTL (b) which is 'low' for FM on and 'high' for FM off.

Sine shaper

With the sine shaper-consisting of the transistor array 320 and the transistors 372, 373 the triangular wave from the VCO is formed to a sine wave and fed to the output RF-SINE via the sine wave amplifier (transistors 374 – 378).

The triangular wave is attenuated with the resistors 696 and 697 to 0.4 Vpp at the base of transistor 320/9, 10, 11. The base of the second transistor of this differential stage (320/6, 7, 8) is fixed to a constant level which is defined with potmeter 709 and resistors 707, 708. Because the operating point for this stage is adjusted such that the stage is overdriven with the tops of the triangular wave, the collectors (pins 11, 8) show a sine wave signal (see picture).



This principle of forming the triangular wave to a sine wave requires a stable and exactly defined operating point. Temperature drifts in the crystal of IC 320 effect a drift of the base-emitter voltages and with this a drift of the operating point which effects then a higher distortion of the sine wave signal at the output RF SINE.

In order to keep the crystal temperature of IC 320 constant, there is a temperature control circuit in the sine shaper which consists of the transistors 372, 373 and three transistors in IC 320. With potmeter 684 a base current is adjusted for transistor 320/12, 13, 14. The collector of this transistor controls the two parallel connected 'heating transistors' 320/1, 2, 3 and 320/3, 4, 5 – which produce a heating power of together appr. 0.3 W – via transistors 373 and 372.

In case of increasing crystal temperature in IC 320, the current through transistor 320/12, 13, 14 will also increase and reduce the heating power of transistors 320/1, 2, 3 and 320/3, 4, 5 via the transistors 373 and 372.

In this way there is a closed control loop which effects a crystal temperature of appr. 80 – 85° C remaining absolutely constant – this is essential for a proper sine wave forming.

The output signals of the differential amplifier 320/9, 10, 11 and 320/6, 7, 8 are fed to the bases of transistors 374 and 376. After amplification in this sine output amplifier the output amplitude is adjusted with potmeter 721 and passed to the modulator via output RF SINE.

6.3. MODULATOR/U1

Voltage Conditioner

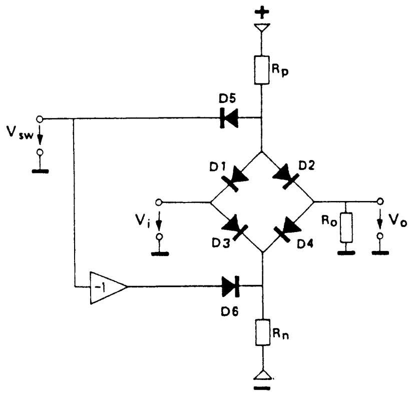
The DFS-signals (sine, triangle, rising sawtooth, falling sawtooth) are routed via the voltage conditioner to be conditioned to the final signals e. g. from the bipolar rising sawtooth to the unipolar positive sawtooth signal.

The resistance network of this voltage conditioner and it s analogue multiplexer (301) build up a voltage divider combination with selectable attenuation and dc-shifting. The path a1 (fig. b) is switched on at sinewave and triangular wave, path a2 at negative sawtooth and path a3 at haversine or positive sawtooth waveforms respectively (due to the inverting OUTPUT AMPLIFIER/U1 the polarities of the signals in the MODULATOR are invers with respect to the generator output signals).

The attenuations of a2 and a3 are double of that of a1, their positive or negative dc-shifting, respectively, comes to half the value of the resulting amplitudes of the signals. Both these facts result in unipolar sawtooth signals and in equal zero to peak amplitudes of all signals derivated from the DFS waveforms.

Diode Switches

The principle of operation of the DIODE SWITCHES 1 and 2 is shown in the figure below (fig. a)



V_i = input signal voltage
V_o = output signal voltage
V_d = diode foreward voltage
V_{sw} = switching voltage

fig. a

If V_{sw} is negative, D5 and D6 are open and the bridge diodes are closed. R_o pulls the output voltage to 0 volts.

If V_{sw} is positive, diodes D5 and D6 are closed, the diode bridge D1 . . . D4 is open, and the voltage relations are

$$V_{d1} + V_i = V_{d2} + V_o$$

$$\text{and} \quad V_i - V_{d3} = V_o - V_{d4}$$

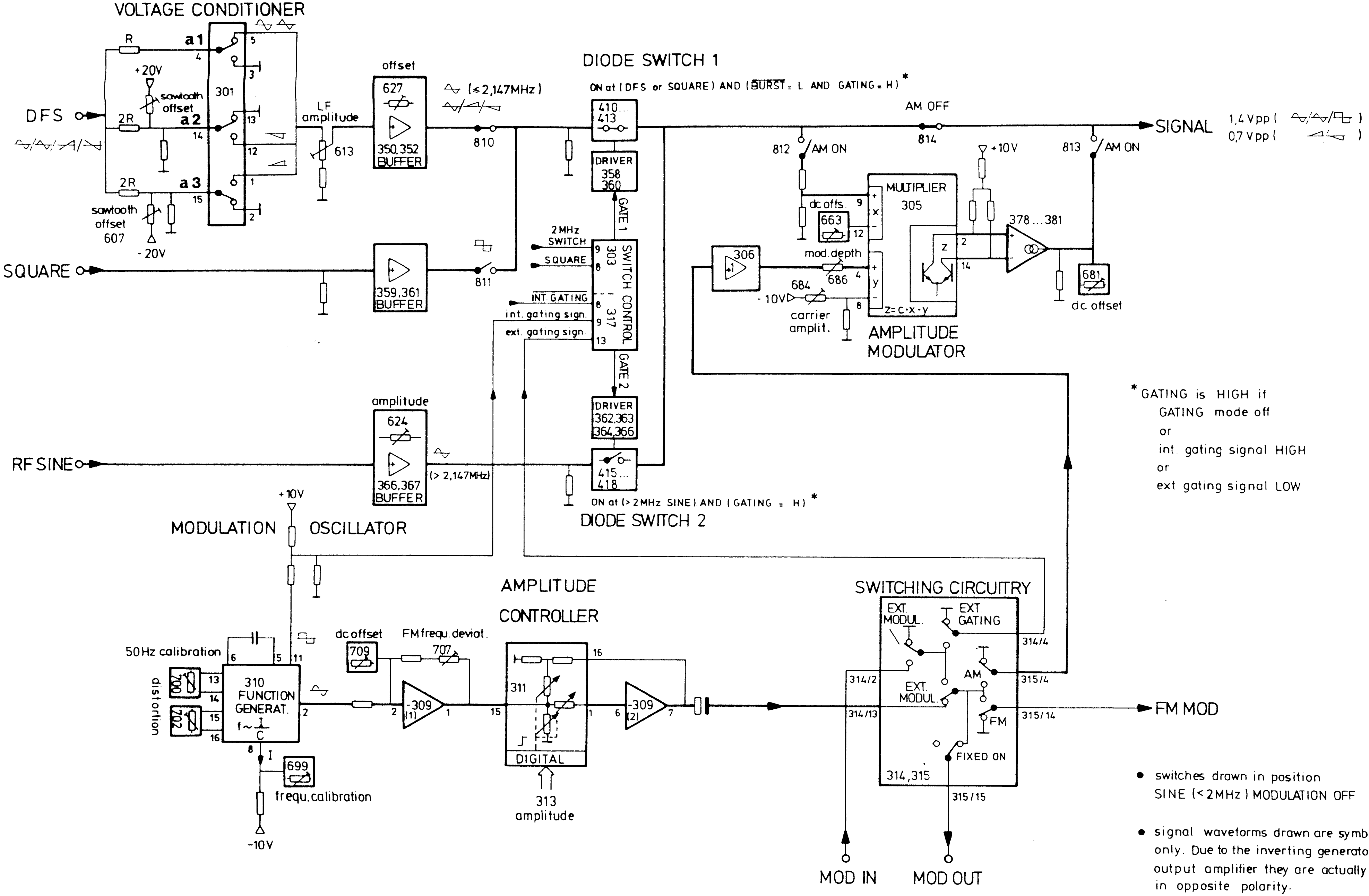
So the output voltage V_o is

$$V_o = V_i + V_{d1} - V_{d2} = V_i + V_{d4} - V_{d3}$$

$$\underline{V_o = V_i} \qquad (\text{if } V_{d1} = V_{d2} \text{ and } V_{d3} = V_{d4})$$

Differences of the diode voltages can cause an offset which is compensated at the general offset adjustment of the generator. The load resistance for the signal voltage source (V_i) is R_o shunted by R_p and R_n.

Besides the selection of the right signal paths the DIODE SWITCHES perform the signal keying at GATING mode. DIODE SWITCH 1 is driven by a balanced, non saturated differential driver (358, 360). DIODE SWITCH 2 is driven by two coupled transistor switches operating in opposite directions (362, 364 and 363, 366).


VOLTAGE CONDITIONER
DFS
SQUARE
RF SINE
R
2R
2R
a1
a2
a3
+20V
sawtooth offset
sawtooth offset 607
-20V
301
LF amplitude
613
offset
627
350,352 BUFFER
359,361 BUFFER
amplitude
624
366,367 BUFFER
(≤2,147MHz)
(> 2,147MHz)
810
811
DIODE SWITCH 1
ON at (DFS or SQUARE) AND ($\overline{\text{BURST}}$ = L AND GATING = H) *
410...413
DRIVER 358 360
GATE 1
2MHz SWITCH
SQUARE
INT. GATING
int. gating sign.
ext. gating sign.
SWITCH CONTROL 303 317
GATE 2
DRIVER 362,363 364,366
415...418
ON at (> 2MHz SINE) AND (GATING = H) *
DIODE SWITCH 2
AM OFF
814
812
AM ON
813
AM ON
SIGNAL
1,4 Vpp
0,7 Vpp
+10V
MULTIPLIER 305
dc offs.
663
mod.depth
686
684
-10V
carrier amplit.
306
z=c·x·y
378...381
681
dc offset
AMPLITUDE MODULATOR
MODULATION OSCILLATOR
+10V
50Hz calibration
700
702
distortion
310 FUNCTION GENERAT.
699
frequ.calibration
-10V
dc offset
709
FM frequ. deviat.
707
-309 (1)
AMPLITUDE CONTROLLER
311
DIGITAL
313 amplitude
-309 (2)
SWITCHING CIRCUITRY
EXT. MODUL.
EXT. GATING
AM
EXT. MODUL.
FM
FIXED ON
314,315
314/2
314/13
314/4
315/4
315/14
315/15
FM MOD
MOD IN
MOD OUT

* GATING is HIGH if
GATING mode off
or
int. gating signal HIGH
or
ext. gating signal LOW

- switches drawn in position SINE (< 2MHz) MODULATION OFF
- signal waveforms drawn are symbols only. Due to the inverting generator output amplifier they are actually in opposite polarity.

MODULATOR/U1 FUNCTIONAL DIAGRAM (fig.b)

Switch control

Both the DIODE SWITCHES are controled by logical control signals and the GATING signal, all them logically linked in the SWITCH CONTROL circuit, a combination of NOR-gates. The linkage of the control signals is explained by the equivalent logical equations below (+ = OR, x = AND):

$$\mathrm{GATE1} = (\mathrm{2\,MHzSWITCH} + \mathrm{SQUARE}) \times (\overline{\mathrm{INT\ GATE}} + \mathrm{int.\,gating\,sign.}) \times \overline{\mathrm{ext.\,gating\ sign.}}$$
$$\mathrm{GATE1} = \overline{\mathrm{2\,MHzSWITCH}} \times \overline{\mathrm{SQUARE}} \times (\overline{\mathrm{INT\ GATE}} + \mathrm{int.\,gating\,sign.}) \times \overline{\mathrm{ext.\,gating\ sign.}}$$

Represented as a combination of contacts:

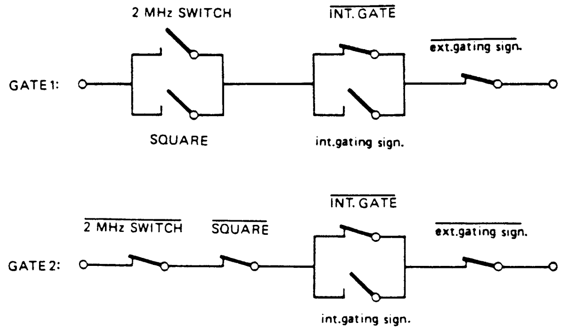


fig. c

Symbols:

2MHzSWITCH:	HIGH (ON) if frequencies < 2.147 MHz (control line)
SQUARE:	HIGH (ON) if SQUARE waveform selected (output pin 14 of shift register 302).
$\overline{\mathrm{INT\ GATE}}$:	HIGH (ON) if any other modulation mode is selected than INTERNAL GATING (output pin 7 of shift register 316).
int. gating sign.:	H and L (ON and OFF) change with the internal gating signal (square wave output of the modulation generator).
ext. gating sign.:	H and L (OFF and ON) change with the external gating signal. Kept LOW (ON) if external gating mode is switched off.
GATE1/2:	Driving signals for the DIODE SWITCHES 1 and 2 (= output signals of the SWITCH CONTROL circuit, see fig. b). HIGH: SWITCH ON, LOW: SWITCH OFF.

Amplitude Modulator

AM is performed by multiplication. The multiplier module (305) multiplies the carrier signal (any generator signal) by the internal or external modulation signal. The modulation signal is superposed by a dc-component (pin 8). The level of this dc-"offset" with respect to the ac input amplitudes (pin 9 and pin 4) determines the proportion between the amplitudes of carrier and both the sidebands. At 0 volts dc the carrier would be suppressed (balanced modulation).

The function of the complete AMPLITUDE MODULATOR circuit is represented by the following equation (for sinusoidal voltages):

$$u_{out} = k \times (E_c \cos\omega_c t \times (V_{dc} + E_m \cos\omega_m t))$$

Symbols: u_{out} = modulated output signal
$E_c \cos\omega_c t$ = carrier signal
$E_m \cos\omega_m t$ = modulation signal
$\left.\begin{matrix}\\ \\ \end{matrix}\right\}$ E = amplitude
V_{dc} = dc voltage (at pin 8)
k = scaling factor, resulting from the parameters of the complete AM-modulator circuitry

Evaluated:

$$
\begin{aligned}
u_{out} &= k \times (E_c V_{dc} \cos\omega_c t \times E_c E_m (\cos\omega_c t \times \cos\omega_m t)) \\
&= k \times E_c (\underbrace{V_{dc} \cos\omega_c t}_{\text{carrier}} + E_m \times 0.5 (\underbrace{\cos(\omega_c - \omega_m) t}_{\text{lower sideband}} + \underbrace{\cos(\omega_c + \omega_m) t}_{\text{upper sideband}}))
\end{aligned}
$$

The right relations between k, V and E, obtained by suitable dimensioning and proper calibration, result in

$$
\begin{aligned}
u_{out} &= 0.5 E_c (\cos\omega_c t + 0.5 m (\cos(\omega_c - \omega_m) t + \cos(\omega_c + \omega_m) t)) \\
&\qquad (m = E_m / E_{m\,max} = \text{depth of modulation})
\end{aligned}
$$

Modulation Oscillator

The main unit of the MODULATION OSCILLATOR is a function generator module (310). The current drawn from pin 8 of this IC and the capacitor connected to pins 5 and 6 determine the output frequency. Current sink (approx. 0.1 mA) and capacitor (33 nF) are dimensioned for a fixed frequency of 1 kHz.
The sinewave output signal is routed via a buffer amplifier (309 (1)), which gives also possibilities for amplitude and offset calibrations, to the AMPLITUDE CONTROLLER, a circuit consisting of a DAC and an operational amplifier (311, 309 (2)). The DAC is applied as controlable feedback resistor network for the operational amplifier.
The rectangular output signal of the function generator (pin 11) is routed to the SWITCH CONTROL circuit. It becomes effective as internal gating signal if the INTERNAL GATING mode is switched on.

Control of the Modulator Unit

Three 8-STAGE SHIFT-AND-STORE BUS REGISTERS, joint in cascade (302 ⟶ 316 ⟶ 313; see fig. 39), distribute the control messages of the CPU (SDA) – 32 bits serial, coming in via register module 302 – to the corresponding switches and control modules respectively. With strobe STR9 going HIGH that string of bits clocked in by the serial clock (SCL) is transfered to the outputs of the register modules in parallel (latched) and, thereby, to the switches, multiplexers, gates etc.
Register module 313 handsover the control bits for amplitude setting of the modulation oscillator. Register modules 302 and 316 hand over the control bits for selection of the different signal paths and modulation modes (see tables below):

4	5	6	7	14	13	12	11	
0	1	0	1	0	0	0	0	∿ or ⋀⋁
0	1	1	0	1	0	0	0	⎍
0	0	1	1	0	0	0	0	◢
1	1	1	1	0	0	0	0	◥

Shift register 302

4	5	6	7	14	13	12	11	
1	1	1	1	1	1			MODULATION OFF
0	0	1	1	1	1			internal AM
1	1	0	1	1	1			internal FM
1	1	1	0	1	1			internal GATING
0	0	1	1	1	0			external AM
1	1	0	1	1	0			external FM
1	1	1	1	0	0			external GATING

*) modulation frequency

Shift register 316

6.4. PULSE GENERATOR/U1

1. Square wave generator

According to fig. 30 (over-all block diagram) and fig. 36 (circuit diagram) the section of the PULSE GENERATOR, which generates the primary square wave, fed to the AMPLIFIER, comprises the following subsections:

- ZERO CROSSING DETECTOR, IC 301,
- CONTROL CIRCUITRY, IC 302, 303, 305, Transistor 351,
- SQUARE WAVE CONDITIONER, Transistors 352 . . . 355.

Zero crossing detector

By resistor 602, 605, 698 a slight hysteresis is implemented for accurate transitions without glitches at the zero crossings of the DFS input signal.

Control circuitry

Control signal states:

"2MHz SWITCH", IC 303-pin 1:	H, if frequency $\leqslant$ 2.147 MHz L, if frequency $>$ 2.147 MHz
"PULSE SEL":	L for rectangular waveform H else

Exclusive OR gate IC 302/1, 2, 3 is routing the ZERO CROSSING OUTPUT signal inverted, if a rectangular waveform is selected, to the MUX, IC 303. The MUX is controlled by the "2MHz SWITCH" control signal at pin 1.

Functional table of IC 303/SN 74S258N:

Pin 1	Pin 2,5	Pin 3,6	Pin 4,7
L	X	L	H
L	X	H	L
H	L	X	H
H	H	X	L

From this table and the "2 MHz SWITCH" definition results, that up to 2.147 MHz the signal from IC 302/pin 3 or above 2.147 MHz the "RF TTL" signal from the PLL is passed inversely through the MUX to IC 305/pin 1. There it is handed with "PULSE SEL" at pin 2. The resulting signal at pin 8 is fed to the SQUARE WAVE CONDITIONER.

The SQUARE WAVE CONDITIONER converts the TTL signal at IC 305/pin 8 to a square wave, accurately in shape and amplitude (about 2.6 Vpp). Trimmpots 624 and 627 are adjusted for accurate positive and negative amplitudes at the generator output.

3. TTL output voltage generation

The output voltage of IC 305/pin 11 is fed to the TTL VOLTAGE CONDITIONER stage with the transistors 356, 357, 358. The TTL OUTPUT signal is a continuous wave also in the generator GATE mode, this signal is not affected by the transistor 351 output.

6.5. AMPLIFIER / U1

As depicted in the over-all block diagram, fig. 30, the **AMPLIFIER** comprises the sub-sections **AMPLITUDE CONTROLLER, POWER AMPLIFIER, STEP ATTENUATOR** and **DC GENERATOR.**

Amplitude controller

Corresponding to fig. a, the input voltage is 1.4 Vpp. for sine, triangle and square waves, 0.7 Vpp for the other wave forms.

The ac currents fed into the emitters of 365 and 368, representing current sinks, are proportional to the total effective conductance between buffer output and current sinks. There are 2 x 7 conductances, namely y1, y2, . . ., y7 and y1', y2', . . ., y7' with binary weights 1, 2, 4, . . ., 64. Two additional conductances y8 and y8', weighted 83, complete the switched conductance ladder. Depending on the programmed voltage pp, some of the conductance pairs y, y' are connected by the switches S1 . . . S8 and S1' . . . S8' with the current sinks.

Example:
for 10 Vpp, switches S3, S6, S7 and S3', S6', S7' are closed. The effective conductance is then about y = 1/(134 ohm). If 20 Vpp is programmed. All switches excl. S2, S4 and S2', S4' are closed, resulting in y = 1/(67.2 ohm). The generator open loop output voltage is Vo = ayVi where a = 960 ohm in the 2.1 to 20 Vpp range.

Thus y0 = 20 Vpp, if y = 1/(67.2 ohm).

Resistor 632 is lowering the input impedance of the emitter current sinks, thus improving the amplitude-controller linearity.

In the real circuitry, see fig. 38, the switches are realized by the diodes 424 . . . 439. In the switch-off state, the diodes are biased reversely to about 1.3 V by transistors, e. g., diode 424 by transistor 333. Primary control is performed by shift register 302 via CMOS switches 314, 315.

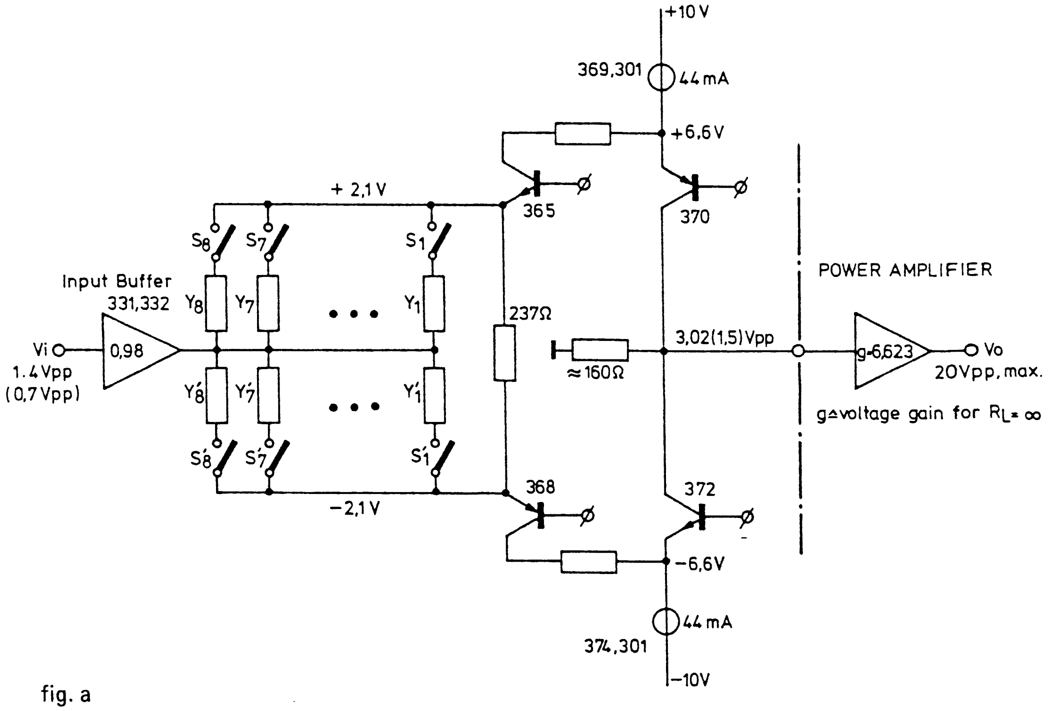

+10 V
369,301
44 mA
+6,6V
+ 2,1 V
365
370
S_8
S_7
S_1
Input Buffer
331,332
Y_8
Y_7
Y_1
237Ω
POWER AMPLIFIER
Vi
1.4Vpp
(0,7Vpp)
0,98
≈160Ω
3,02(1,5)Vpp
g-6,623
Vo
20Vpp, max.
Y_8'
Y_7'
Y_1'
g≙voltage gain for $R_L = \infty$
S_8'
S_7'
S_1'
368
372
-2,1V
-6,6V
44 mA
374,301
-10V

fig. a

Power amplifier

As depicted in fig. b, the **POWER AMPLIFIER** is a complementary collector-output type. At frequencies above some hundred Hz the ac input voltage is passed through the input buffer and C536, C 537 directly to the base of transistors 378 and 381, converted to ac currents by 378 . . . 383 and fed through 385, 386, 387 and 390, 391, 392 respectively to the internal 50 Ohm output resistor, made up by the resistors 781 . . . 786.

For high dc stability, the difference of the emitter dc currents (378, 379, 380 vs 381, 382, 383), which is about zero, is kept constant by an automatic control loop including OP 303 and transistor 377.

This loop is forcing i1 – i2 Vi in the frequency range from dc to some hundred Hz. Trimmer 700 is adjusted for flat amplitude response in the take-over range of the direct drive via C536 and C537.

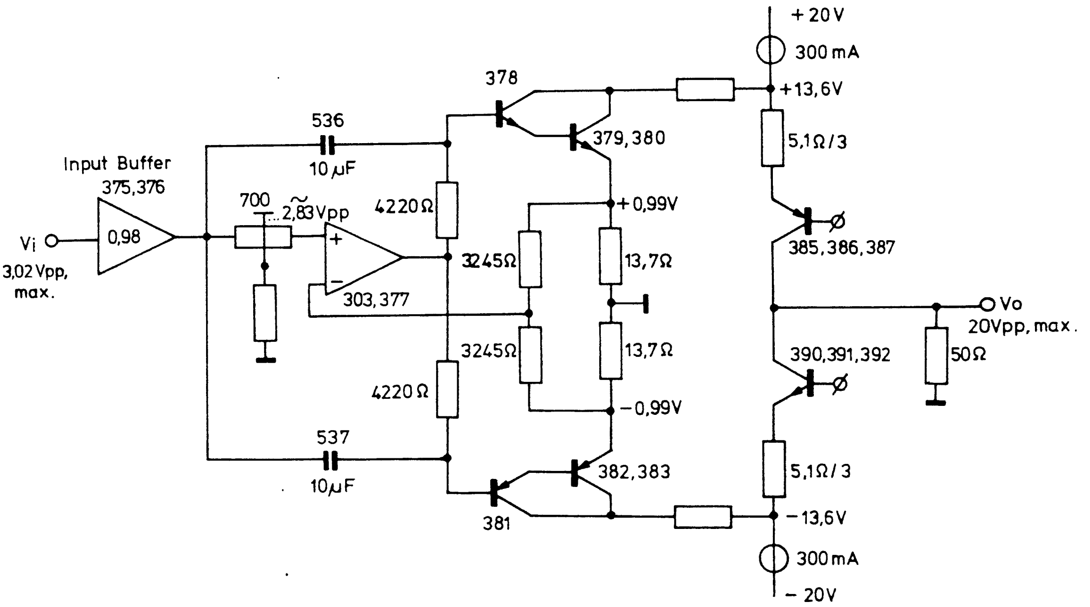

+20V
300mA
+13,6V
378
536
10µF
379,380
5,1Ω/3
Input Buffer
375,376
700
2,83Vpp
4220Ω
+0,99V
Vi
0,98
+
3245Ω
13,7Ω
385,386,387
3,02Vpp, max.
303,377
Vo
20Vpp, max.
3245Ω
13,7Ω
390,391,392
50Ω
4220Ω
-0,99V
537
10µF
382,383
5,1Ω/3
381
-13,6V
300mA
-20V

fig. b

DC generator

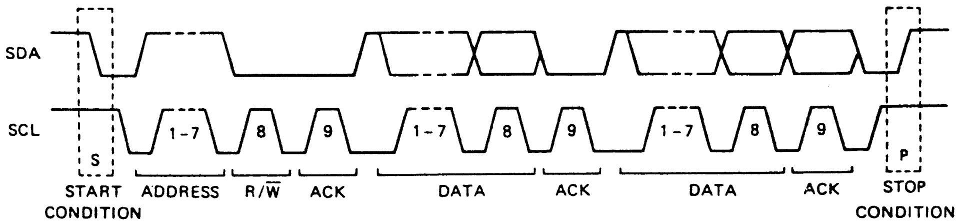
Primary control of the **DC GENERATOR** is performed by the DAC, Pos. 307. The DAC is set by shift register 306 to output currents I_0 from 0 to about 200 · $I_{ref}/255$ = 0.884 mA proportional to the decimal equivalents 0 . . . 200 of the shift register binary output. If the decimal equivalent is 100, i. e. DC = 0V, I_0 = 0.442 mA is compensated by the current through resistors 770, 771. 771 is adjusted for DC = 0V at the generator output. Hence, in this situation no current is drawn from the input (pin 6 of OP 308) of the dc generating circuitry. If the shift register output is 0 or 200, i. e. DC = – 10 V or + 10 V respectively, the asymmetry of the emitter voltages (transistors 394, 395 vs 397, 398 is U = $I_{0max} R/4$ = 5.5 V (I_0 = 0.884 mA, R = 24.9 kOhm; resistors 777 . . . 780), giving a dc voltage at the generator output of 4 · U · RL/r = 10 V absolute. (RL = 50 Ohm, r = 110 Ohm; resistors 868, 869 etc.). The corresponding output current of the DC GENERATOR is 4 · U/r = 200 mA.

6.6. CPU / U2

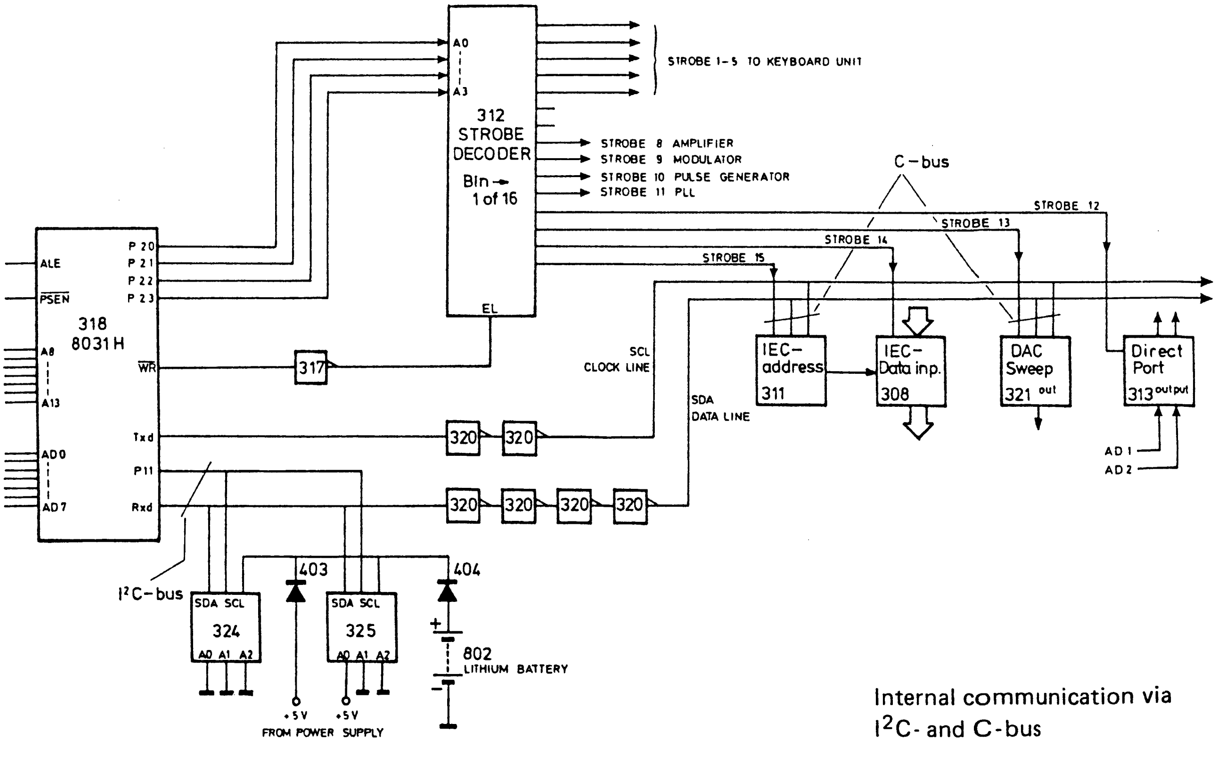
The CPU of the PM 5192 contains the μ-processor 8031 with all drivers, decoders, RAM- and PROM memory, clock generator, IEC-bus interface, strobe-decoder and the control outputs PEN LIFT and SWEEP OUT.

The multiplexed address/data bus (AD0 – AD7) of the processor 318 supplies the address inputs A0 – A7 of the PROM-memory 315 via the address latch 314 (74LS363), the inputs A8 – A13 are supplied directly from the processor (P20 – P25). Data from the PROM is fed from the outputs O_0 – O_7 via the lines AD0 – AD7 directly to the processor, this transfer is controlled with the output $\overline{\text{PSEN}}$ from the processor driving the input $\overline{\text{OE}}$ of the PROM.

The memory circuits 324 and 325 serve the storing of the parameter sets (store function). Communication with them takes place via the internal serial I^{2}C bus which consists of a data line SDA and a clock line SCL. The principle of the data transfer between processor and the RAM-memories is shown in the following diagram.

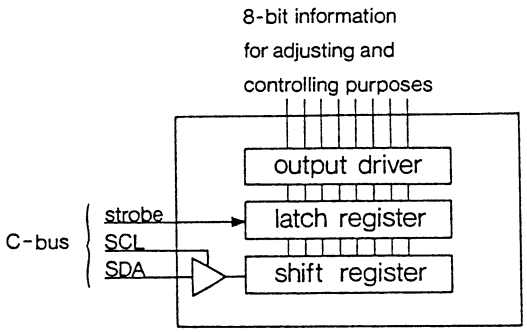

SDA
SCL
S
1-7
8
9
1-7
8
9
1-7
8
9
P
START CONDITION
ADDRESS
R/$\overline{\text{W}}$
ACK
DATA
ACK
DATA
ACK
STOP CONDITION

The line SDA and the line SCL for the C-bus (output T x d of the processor) servce the communication with the remaining units. The data information of the line SDA is clocked into the shift register of each unit, the according strobe signal following this data sequence latches the data information in the selected shift register and presents the transmitted information in parallel form at the output lines of this circuit. The required strobe signals are generated with the strobe-decoder 312 which is controlled again from the processor via the lines P20 – P23 and the signal $\overline{\text{WR}}$.


A0
A3
312 STROBE DECODER
Bin → 1 of 16
EL
STROBE 1-5 TO KEYBOARD UNIT
STROBE 8 AMPLIFIER
STROBE 9 MODULATOR
STROBE 10 PULSE GENERATOR
STROBE 11 PLL
C-bus
STROBE 12
STROBE 13
STROBE 14
STROBE 15
ALE
PSEN
A8
A13
AD0
AD7
P20
P21
P22
P23
318 8031H
WR
Txd
P11
Rxd
317
320
SCL CLOCK LINE
SDA DATA LINE
IEC-address 311
IEC-Data inp. 308
DAC Sweep 321 out
Direct Port 313 output
AD1
AD2
I²C-bus
SDA SCL
324
A0 A1 A2
SDA SCL
325
A0 A1 A2
403
404
802 LITHIUM BATTERY
+5V
+5V
FROM POWER SUPPLY

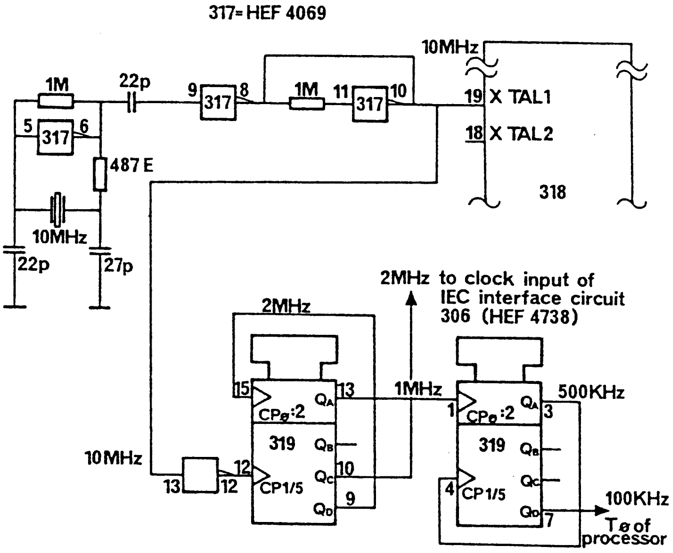
Internal communication via I^{2}C- and C-bus

Converting the serial information to a parallel one the parallel output presents the decoded commands.


8-bit information
for adjusting and
controlling purposes
output driver
latch register
shift register
C-bus
strobe
SCL
SDA

conversion from serial to parallel information

The clock signals for processor and IEC controller as well as a 100 kHz signal for the internal timer are generated in the 'clock generator' circuitry. This part of the CPU consists of the crystal 801, the inverter 317 and the decimal counter 319. The 10 MHz clock from the inverting buffer is fed to input XTAL 1 of the processor, furthermore divided by 5 with the decimal counter 319 and then fed to the IEC-controller 306. After further dividing by 20 the output 7 of the counter deliveres the 100 kHz signal T0 for the timer.

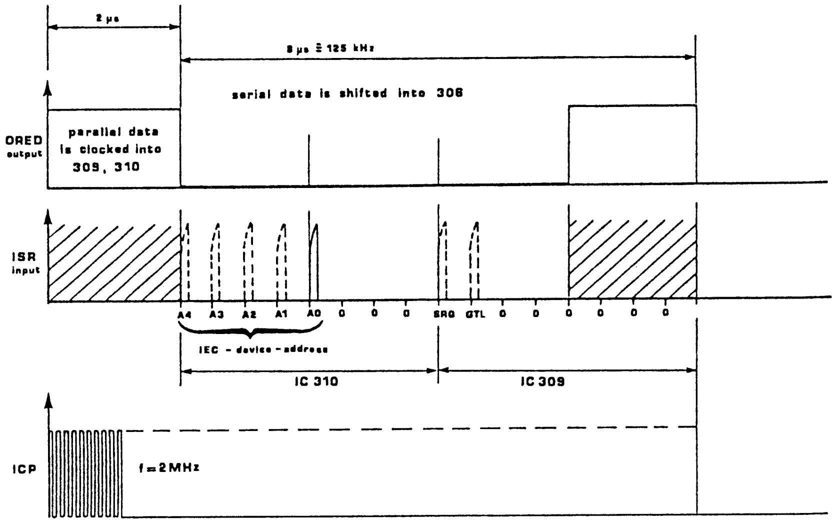

317= HEF 4069
1M
22p
9
317
8
1M
11
317
10
10MHz
19 X TAL1
18 X TAL2
318
5
317
6
487 E
10MHz
22p
27p
2MHz to clock input of IEC interface circuit 306 (HEF 4738)
2MHz
15
CPø:2
QA
13
1MHz
1
CPø:2
QA
3
500KHz
319
QB
319
QB
10MHz
13
12
12
CP1/5
QC
10
9
QD
4
CP1/5
QC
QD
7
100KHz
Tø of processor

IC 319=Decimal counter

clock generation

Remote control of the PM 5192 follows via the IEC-interface which consists of the controller 306, the bus drivers 301 – 304, the buffers 307, 308 and the shift registers 309 – 311. Input data (control commands) from the IEC-bus connector are fed to the data bus AD0 – AD7 via the bidirectional bus drivers 303 and 304 and buffer 307. Output data (learn- and identification mode) are accordingly fed to the IEC-bus via buffer 308 and the bus drivers 303 and 304. Data direction and enable of the buffers are controlled by the IEC-bus controller 306 via the output Ota (other talk address) and by the processor with the signal 'STROBE 14'.

The listener/talker addresses and the interface parameters necessary for the communication are sent to the IEC-controller in serial form. The microprocessor sends these informations via the C-bus with the help of the signal STR 15 to the shift register 311. The parallel outputs of this circuit supply again the parallel inputs of the shift registers 309 and 310 by means of which addresses and interface parameters are transferred to input 'ISR' (input shift register) of the IEC-bus controller. This transfer action is controlled from the controller circuit, the repetition rate is 125 kHz (= 2 MHz/16).



Each data string for the shift registers 309 and 310 contains the IEC device-address and one bit for SRQ on or off. The data string entered from the IEC controller 306 via input 'ISR' has a length of 11 bits, not required bits at the inputs of ICs 309/310 are fixed to ground. Input GTL of these circuits – pin 15 of IC 309 – is controlled directly from the key LOCAL at the front panel. Pressing this key causes the IEC controller 306 to switch back to the local mode.

The output SWEEP OUT contains a frequency-proportional voltage during the linear sweep and a log-frequency proportional voltage during the logarithmic sweep. This output voltage is generated by means of a shift register with a DAC controlled directly from the processor via the C-bus.

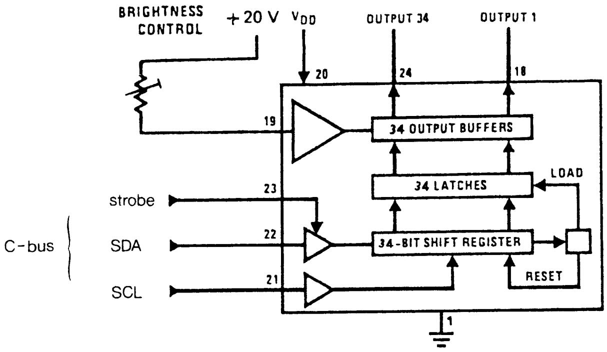
The digital information is transferred to the shift-register and presents the parallel information at the outputs when the strobe-signal 'Strobe 13' becomes true. The 8 output-lines of the shift-register 321 are fed directly to the input lines of the DAC 323 which forms the 8-bit digital information to an analog signal which is then – amplified by the operational amplifier 322 – fed to the SWEEP OUT connector.

The Direct Port consists of the latch circuit 313 which is used to latch informations from the lines AD1, AD2 and which is controlled with the signal STR 12. The output-signals '2 MHz-SWITCH' and 'PLL-CNTL' are used for controlling purposes in the units PLL/VCO and modulator.

6.7. KEYBOARD DISPLAY UNIT / U3

Unit 3 of the synthesizer PM 5192 contains LEDs keys and display elements with their concerning driver/decoder circuits. Data transfer from the CPU to the keyboard/display unit takes place via the C-bus (SDA, SCL, STROBE 1 – 5), input data from the keyboard is sent as a sequence of 12 pulses from the keyboard encoder 353 via the line SKC to the CPU. The key 'LOCAL' is directly led to the IEEE/IEC interface on unit 2 via line GTL.

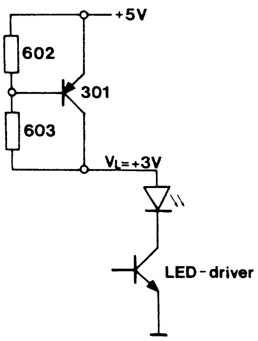
Display data are sent in 34 bit data blocks via the C-bus to the according part of the display unit, selection (= addressing) of this part is done with one of the strobe signals STROBE 1 – STROBE 5. During the data transfer from the CPU to the keyboard/display unit the according strobe line is set and a 34-bit data block is loaded into the shift register with the clock signal SCL. The last bit at the data input shifts the complete data set into the latch register and therewith to the display elements/LEDs via the buffer stage.



Each of the five strobe lines controls the data transfer to one of the display groups:

STROBE 1	display circuit 408 for	amplitude, Vdc, address and the LEDs in the keys 'Vrms' and 'ADDRESS'
STROBE 2	display circuit 407 for	modulation parameters and the LED in the key 'TIME(s)'
STROBE 3	display circuit 406 for	frequency display (right part) and the LEDs in the keys 'START' and'STOP'
STROBE 4	display circuit 405 for	frequency displays (left part)
STROBE 5	LED driver circuit 352	

Voltage supply for the LEDs and displays comes from the + 5 V which is reduced to + 3 V (= VL) by means of the transistor 301 and the resistors 602 and 603

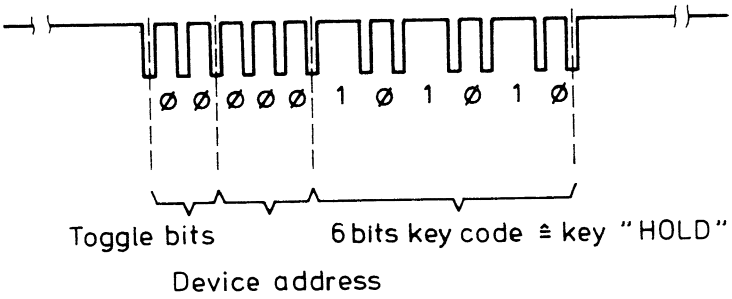


the transistor 301 is placed behind the front plate at a spacing piece beside the socket TTL OUT.

Brightness of the LEDs/displays is adjusted with a reference voltage at the inputs BC (= brightness control) of the driver circuits. These reference voltages are derived from the + 20 V supply by means of the resistors 604 – 608 feeding currents into the BC-inputs.

Input from the keyboard takes place with the help of the keyboard encoder IC 353 (= SAA 3007) which controls the 8 x 8 keyboard matrix and sends the keycode in serial form from the output REMO via line SKC to the CPU. During the rest condition the sense lines SEN0 – SEN6 are 'high', the drive lines of the matrix DRV0 – DRV6 are 'low', the last drive line is fixed to ground.

When a key is pressed the according sense line is forced to 'low', the internal logic of the encoder starts the scan of the matrix and transmits a sequence of 12 pulses whereby the distance between two pulses means binary "0" or "1".



Each time a key is pressed such a bit sequence must be measurable at line SKC (= pin 10 of the CIS connector). The toggle bits of this message are incremented by 1 each time when a key is pressed. Thus it is possible to distinguish between a key being pressed several times or once for a longer time.

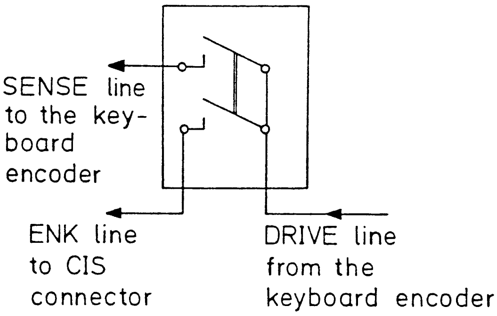
The device address is fixed to "000" and the last 6 bits show the following pattern by pressing the corresponding key

Key	Connection between	Key code
∿	DRV0 – SEN0	000000
◿	DRV0 – SEN1	001000
◹	DRV0 – SEN2	010000
⋀	DRV1 – SEN0	000001
⎍	DRV1 – SEN1	001001
AC OFF	DRV1 – SEN2	010001
START	DRV3 – SEN0	000011
Hz/kHz	DRV3 – SEN1	001011
–STEP	DRV3 – SEN2	010011
STOP	DRV4 – SEN0	000100
ΔFREQ	DRV4 – SEN1	001100
+STEP	DRV4 – SEN2	010100
OFF	DRV5 – SEN0	000101
INT	DRV5 – SEN1	001101
EXT	DRV5 – SEN2	010101
GATE	DRV6 – SEN0	000110
AM	DRV6 – SEN1	001110
FM	DRV6 – SEN2	010110
%	⊥ – SEN1	001111
DEV (kHz)	⊥ – SEN2	010111

Key	Connection between	Key code
LIN	DRV1 – SEN3	011001
LOG	DRV1 – SEN4	100001
TIME (s)	DRV1 – SEN5	101001
SINGLE	DRV2 – SEN3	011010
CONT	DRV2 – SEN4	100010
HOLD	DRV2 – SEN5	101010
Vdc	DRV3 – SEN3	011011
Vpp	DRV3 – SEN4	100011
ΔLEVEL	DRV3 – SEN5	101011
+/–	DRV4 – SEN3	011100
dBm	DRV4 – SEN4	100100
–STEP	DRV4 – SEN5	101100
ADDRESS	DRV5 – SEN3	011101
Vrms	DRV5 – SEN4	100101
+STEP	DRV5 – SEN5	101101
STO1-9	⊥ – SEN7	111111
RCL0-9	DRV6 – SEN7	111110
RUBOUT	DRV4 – SEN7	111100
0	DRV0 – SEN6	110000
1	DRV1 – SEN6	110001
2	DRV2 – SEN6	110010
3	DRV3 – SEN6	110011
4	DRV4 – SEN6	110100
5	DRV5 – SEN6	110101
6	DRV6 – SEN6	110110
7	⊥ – SEN6	110111
8	DRV0 – SEN7	111000
9	DRV1 – SEN7	111001
"•"	DRV2 – SEN7	111010
ENTER	DRV3 – SEN7	111011

The clock for the keyboard encoder is generated with the ceramic resonator 860. During the rest condition – i. e. no key is pressed – there is no signal at input 11 or 12 of the keyboard encoder. When any key – except LOCAL – is pressed, the clock supply will be activated and a signal with a frequency of 455 kHz and an amplitude of 4,5 Vpp can be measured at pin 11 or 12. By pressing a key only once for a short moment the clock will be switched on for approx. 170 ms., pressing a key for longer will keep the clock supply switched on as long as the key is pressed.

The line ENK (enable keyboard) at pin 4 of the CIS-connector has a special meaning for the keyboard input. During a running sweep it is not possible to press any key except MOD OFF, SINGLE, CONT or HOLD. These keys contain one more switch contact which is commonly connected to line ENK, only this line is supervised by the processor during a running sweep.


SENSE line to the keyboard encoder
ENK line to CIS connector
DRIVE line from the keyboard encoder

When operating one of these keys the line ENK and the according sense line are forced to 'Low' which effects the keyboard encoder to scan the matrix and to send the key code via the line SKC to the CPU. Only when the processor has recognized via ENK that one of the four keys was pressed, the normal keyboard input via line SKC is started.

7. ACCESS TO PARTS

7.1. TOP AND BOTTOM COVERS (DISMANTLING THE INSTRUMENT)

Before opening the instrument unplug mains connector, take note of chapter 1.5..

- Loosen the cross-slotted screws Ⓐ (Fig. 7-1) at the rear
- Pull top cover Ⓑ as shown in figure 7-1.
- The procedure to remove the bottom cover Ⓒ is the same as above.

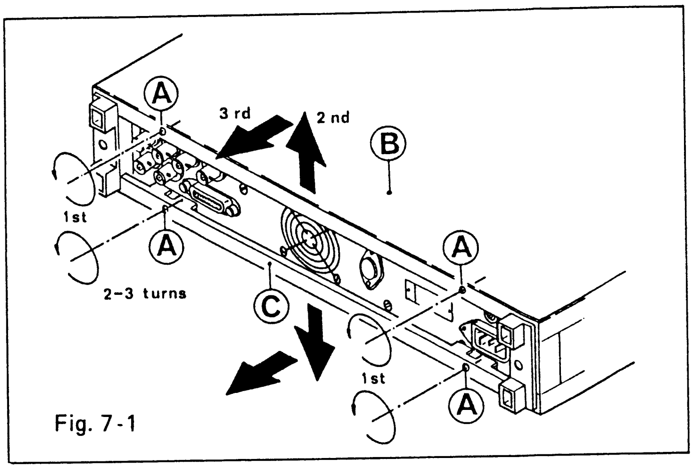


Fig. 7-1

7.2. FUSE, MAINS TRANSFORMER

For mains voltage setting and fuses and the assigned safety instructions see chapter 2.

7.3. UNIT 1 AND UNIT 2

For access to the upper side of unit 2 and the bottom side of unit 1 it is only necessary to remove the top cover respectively the bottom cover of the instrument (see chapter 7.1.).

To reach the upper side of unit 1 and the bottom side of unit 2 proceed as follows:

- Unplug the connector Ⓖ (Fig. 7-5) on the right hand side of unit 2.
- Remove 2 screws Ⓐ (Fig. 7-5) at the sides of the instrument.
- Lift the pcb as arrow Ⓒ shows in Fig. 7-5. (If it is heavy to move the pcb, loosen the screws at the pivot a little bit.).
- Fixing unit 2 in an upright position insert screw Ⓐ in position Ⓗ (Fig. 7-5).
- The other steps shown in fig. 7-5 are not necessary to reach unit 1 + 2.

7.4. FRONT-PANEL EDGING

- Remove covers (chapt. 7.1.).
- Lift the profile ornament Ⓐ (Fig. 7-2) with a small screw driver.
- Remove the screws Ⓑ (Fig. 7-2).
- Remove the edging Ⓒ (Fig. 7-2).
- For the bottom side the same procedure applies.

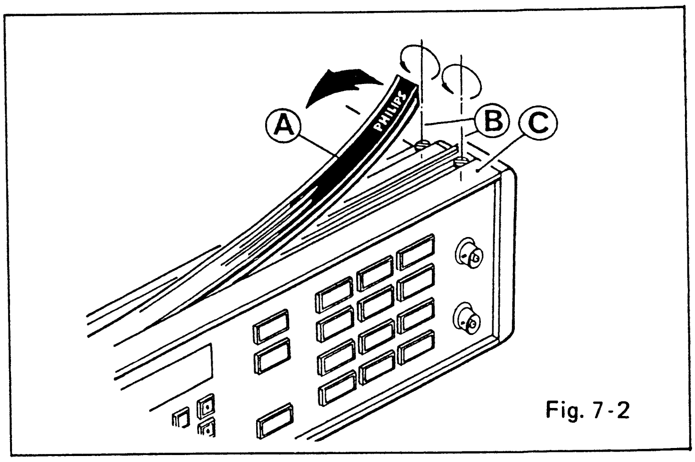


Fig. 7-2

- Remove screws Ⓑ (Fig. 7-3)
- Remove side pieces Ⓐ (Fig. 7-3)

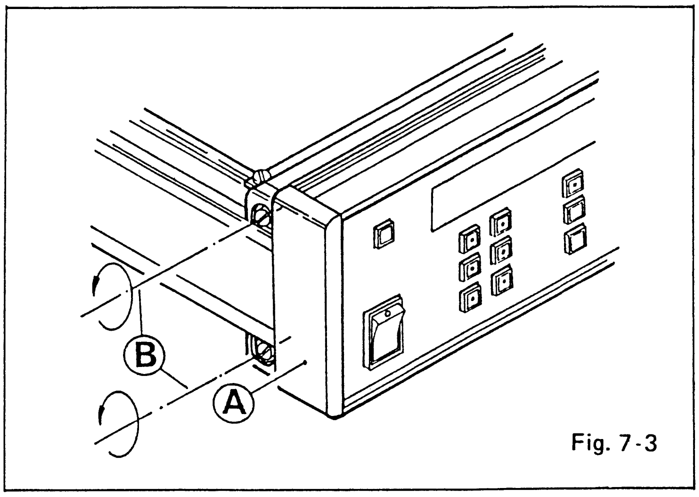


Fig. 7-3

7.5. TEXT PLATE

The text plate Ⓐ (Fig. 7-4) is fixed by three parts of doublesided adhesive tape Ⓑ (Fig. 7-4)

- To remove the text plate insert carefully a screwdriver near the tapes and move the screwdriver as shown in Fig. 7-4.
- Steps described in chapters 7.1 and 7.4. are necessary before.

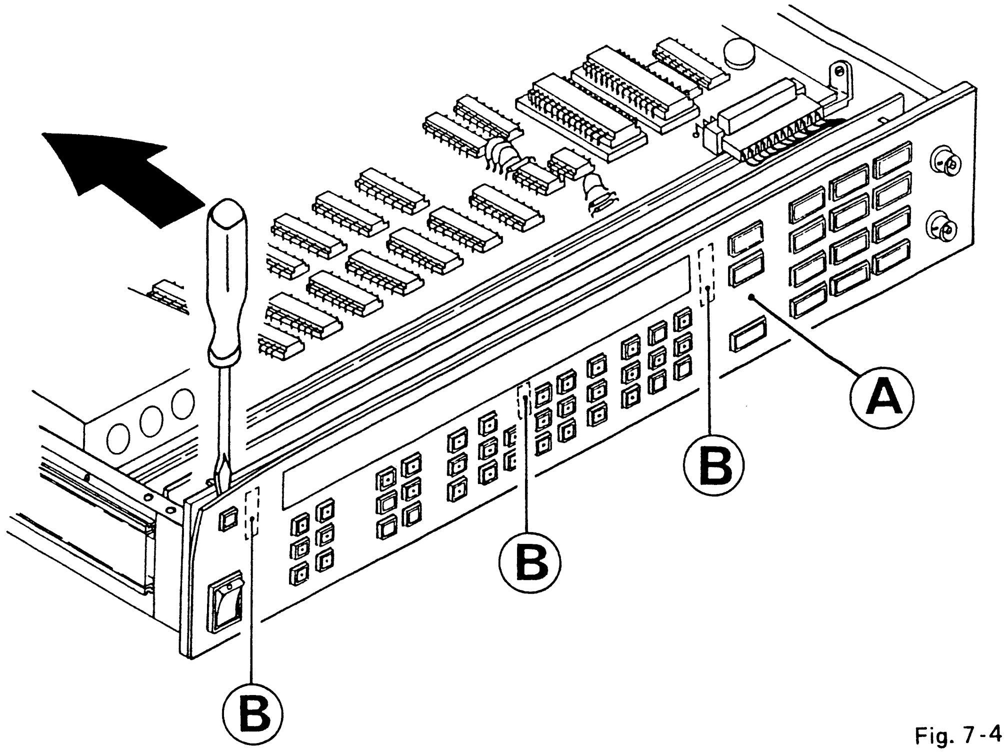


Fig. 7-4

7.6. UNIT 3 (KEYBOARD AND DISPLAY)

At first steps described in chapters 7.1., 7.4. and 7.5. must be done

- Unplug connector Ⓖ (Fig. 7-5)
- Loosen all screws Ⓕ (Fig. 7-5) at the front of the instrument
- Pull carefully frontplate Ⓔ (Fig. 7-5) forwards, take care of the wires of the BNC-connectors and the main switch
- Remove unit 3 Ⓓ (Fig. 7-5)
- The other steps shown in this figure are not necessary to reach unit 3.

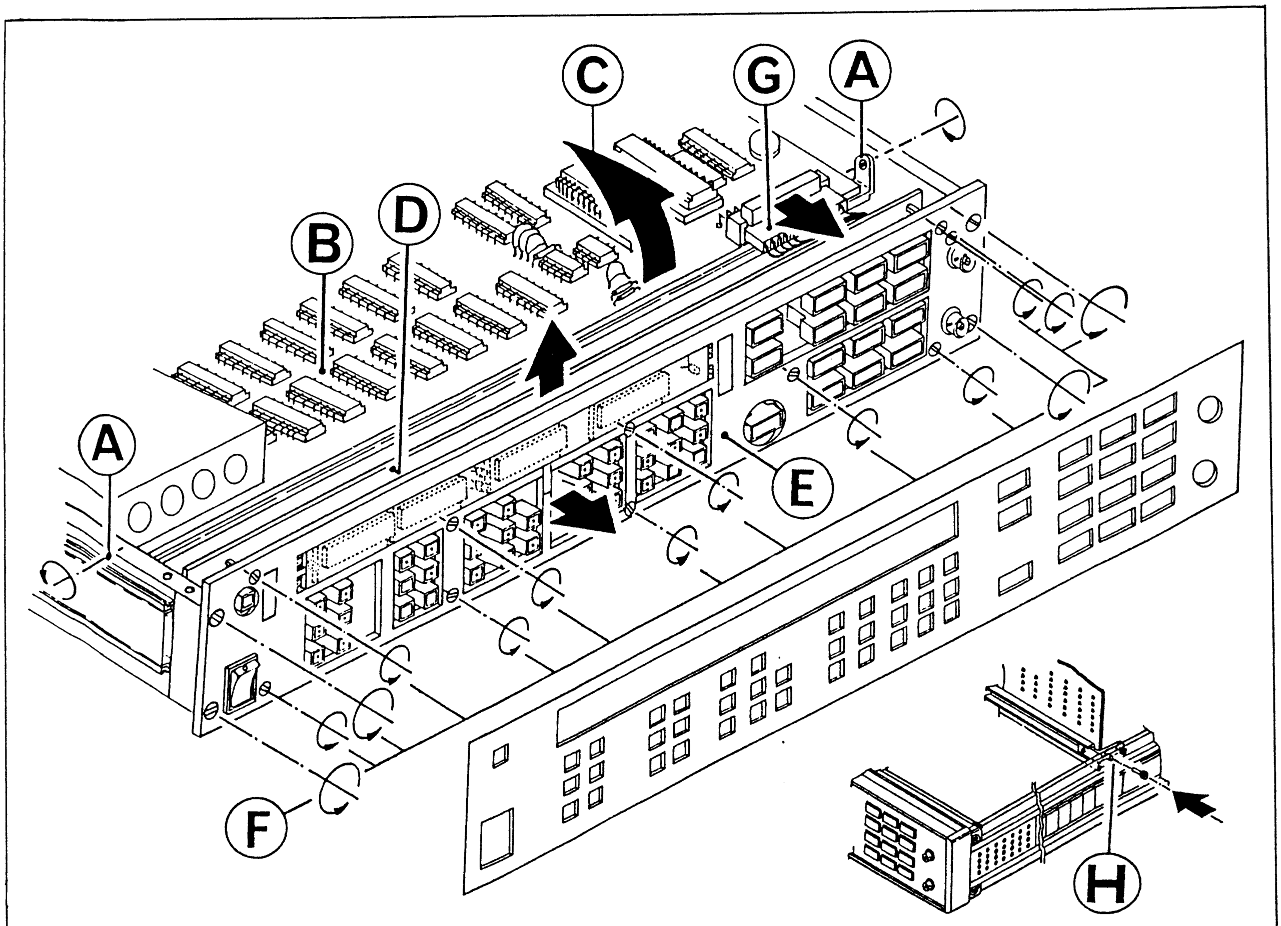


Fig. 7-5

7.7. CARRYING HANDLE

- Lift the carrying handle.
- Prise off carefully both plastic profile strips next to the handle in the similar way as the profile ornament of the front-panel edging shown in Figure 7-2.
- Loosen cross-slotted screws of the holder for handle.

7.8. HANDLE ASSEMBLY FOR RACK MOUNTING

- Remove top and bottom covers as described in chapter 7.1.
- Loosen screws Ⓑ (Fig. 7-6).
- Remove side piece Ⓒ
- Fit handle Ⓐ , refit screws Ⓑ
- For the right hand side the same procedure applies.
- Close the instrument

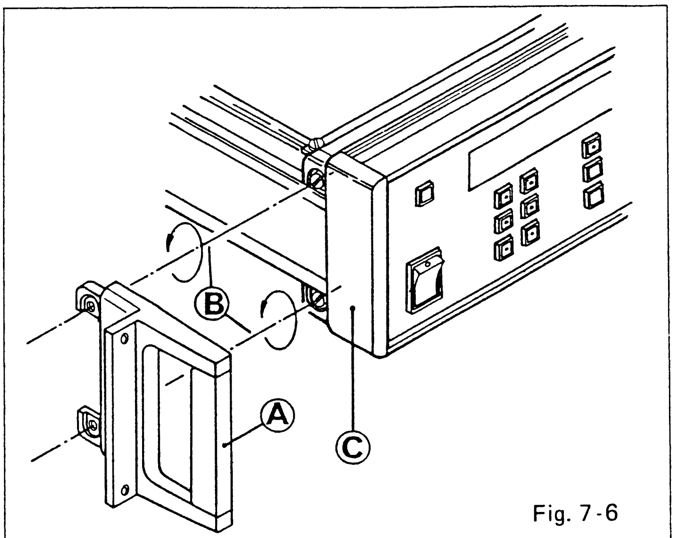
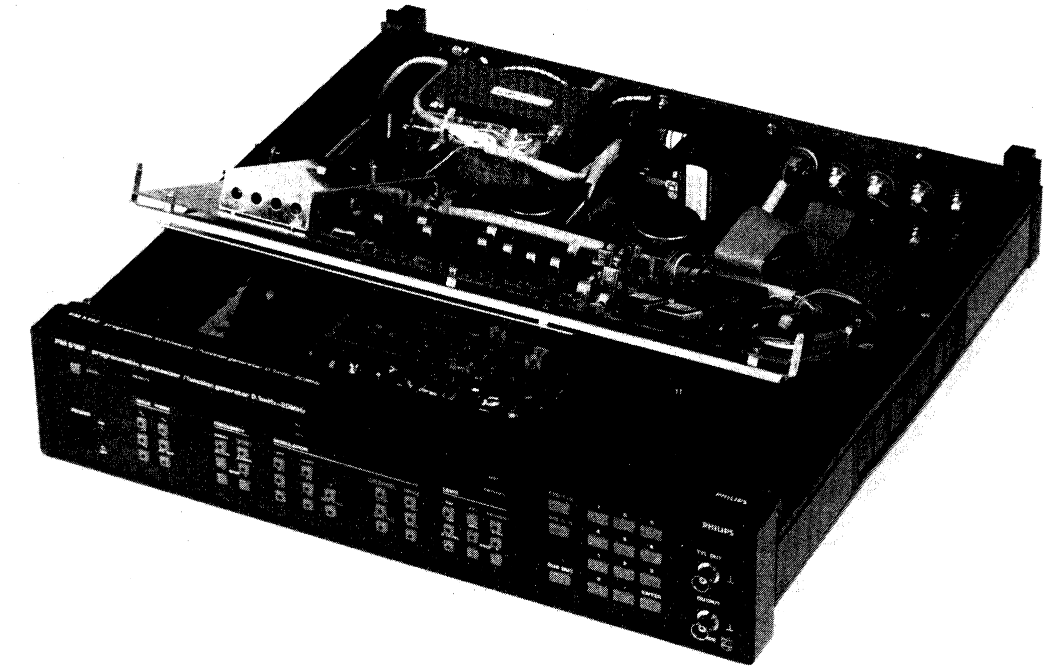


Fig. 7-6

7.9. VIEW INTO THE OPEN INSTRUMENT



PM 5192 without top cover

8. CHECKING AND ADJUSTING

8.1. GENERAL INFORMATION

The following information provides the complete check and adjustment procedure for the instrument. As various control functions are interdependent, a certain order of adjustment is often necessary. The procedure is, therefore, presented in a sequence which is best suited to this order, cross-reference being made to any circuit which may affect a particular adjustment.

Before any check, the instrument must attain its normal operating temperature.

- Warm-up time under average conditions is 30 minutes.
- Adjustments should be made after 2 hours
- Ambient temperature (23 ± 1)^{o} C
- Mains voltage, nominal values ± 10 %
- The cabinet must be closed.*
- Where possible, instrument performance should be checked before an adjustment is made.
- All limits and tolerances given in this chapter are calibration guides, and should not be interpreted as instrument specifications unless they are also published in chapter 1.2. of the Operating Manual.
- Tolerances are given for the instrument under test and do not include test equipment error.
- If not explicitely stated otherwise, the voltage potentials refer to the relevant contact measured against measuring earth.

8.2. RECOMMENDED TEST EQUIPMENT

The following instruments are necessary to provide check and adjustment of the PM 5192

- 50 Ω termination resistor PM 9581 (1 W)
- wide band oscilloscope (tr ⩽ 1 ns)
- DC-voltmeter: resolution ⩽ 100 µV e. g. PM 2528
- counter 20 MHz, intervall-measurements, 8 digits resolution, e. g. PM 6665
- spectrum analyzer e. g. Takeda Riken 4132
- rms voltmeter: resolution 1 mV, fmax = 3 MHz e. g. Fluke 8920 A; the connection cable together with the termination resistor must have an impedance of exactly 50 Ω
- distortion meter e. g. PM 6309
- power meter e. g. HP438A with probes HP8482A and HP8484A
- modulation meter e. g. Rhode + Schwarz FAM

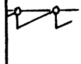
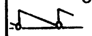
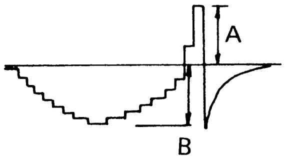
- service kit
  consisting of notch filter 100 kHz, low pass filter 5 kHz, adjustment covers and two adapter cables; to be ordered from SC Hamburg without service code number

* For adjustments special covers with holes for the adjusting elements are required. This parts are included in the service kit.

8.3. TABLE OF CHECKS AND ADJUSTMENTS

Step	Objective	c = check a = adjustment	operation parameters settings	adjusting elements	measured value, value to be adjusted	test point, output measuring instruments	open output/ 50Ω term.	comment
	POWER SUPPLY ADJUSTMENT							
1	+ 5 V supply	c	∿ /1kHz/10Vpp	–	5 ± 0.25 V	T1/6, DC-voltmeter (DVM)	–	
2	± 10V supply	c (a)	∿ /1kHz/10Vpp	612/615, power supply	± 10 ± 0.1 V	T1/1, 2, DC-voltmeter (DVM)	–	
3	± 20V supply	c (a)	∿ /1kHz/10Vpp	604/609, power supply	± 20 ± 0.2 V	T1/3, 4, DC-voltmeter (DVM)	–	
	DFS ADJUSTMENT							
4	Frequency adjustment	a	⎍ /1MHz/5Vpp	505, DFS unit 2	1 MHZ ± 0.3 Hz	OUTPUT connector, counter	50 Ohm	
5	DFS-glitches	a	∿ /100kHZ!/10Vpp see comment	676/689, DFS unit 2	minimum AC level	OUTPUT connector, notch filter	50 Ohm	the generator frequency must be exactly the same as the frequency of the notch filter
	DC-ADJUSTMENT							
6	DFS DC-offset	a	∿ /1kHz/10Vpp	693, DFS unit 2	0 ± 1 mV	T20 unit 1, DC-voltmeter	–	
7	DC-generator voltage	a	∿ /1kHz/0Vpp/+10VDC/AC OFF	771, amplifier unit 1	+ 9.9 V ± 5 mV	OUTPUT connector, DC-voltmeter	open	
8	DC-generator voltage	a	∿ /1kHz/0Vpp/0VDC/AC OFF	769, amplifier unit 1	0 ± 5 mV	OUTPUT connector, DC-voltmeter	open	
9	offset, amplifier preliminary stage	a	∿ /1kHz/10Vpp/jumper S13* open	809, amplifier unit 1	0 ± 1 mV	T14 amplifier, DC-voltmeter	–	
10	offset difference	a	∿ 1/kHz/3.2Vpp/jumper S13* open	874, amplifier unit 1	} Δ < 2 mV	OUTPUT, oscilloscope or DVM	50 Ohm	these AC-amplitudes must be switched alternatively and very fast (e. g. with an IEC-bus controller) in order to get a visible signal
11	offset difference		∿ /1kHz/6.4Vpp/jumper S13* open	874, amplifier unit 1				
12	offset	a	∿ /1kHz/3.2Vpp/jumper S13* open	666, amplifier unit 1	} Δ < 100 mV	OUTPUT, oscilloscope or DVM		
13	offset	a	∿ /1kHz/6.4Vpp/jumper S13* open	666, amplifier unit 1				
14	max. offset difference	a	∿ /1kHz/3Vpp . . . 20Vpp in steps of 1 V jumper S13* open	874, amplifier unit 1	Δ < 40 mV	OUTPUT, DVM	50 Ohm	
15	average output offset	a	"	666, amplifier unit 1	0 ± 10 mV	OUTPUT, DVM	50 Ohm	
16	output offset	c	"	–	0 ± 40 mV	OUTPUT connector, DC-voltmeter	open	If the offset value is outside the limit of ± 40 mV, repeat steps 10 and 11
17	output offset difference	a	∿ /1kHz/jumper S13 open, closed*/MOD OFF/5Vpp	627, modulator unit 1	Δ < 10 mV	OUTPUT connector, DC-voltmeter	open	

* jumper S[illegible] ≙ L11

Step	Objective	c = check a = adjustment	operation parameters settings	adjusting elements	measured value, value to be adjusted	test point, output measuring instruments	open output/ 50Ω term.	comment
	LF-AMPLITUDE, DISTORTION							
18	LF-amplitude	a	∿ /10 kHz/10 Vpp	613, modulator unit 1	1.767 ± 0.001 Vrms	OUTPUT connector, rms-voltmeter	50 Ohm	
19	LF-amplitude	c	∿ /10 kHz/1 Vpp	—	177 ± 3.5 mVrms	OUTPUT connector, rms-voltmeter	50 Ohm	
20	LF-amplitude	c	∿ /10 kHz/0.1 Vpp	—	17.7 ± 0.5 mVrms	OUTPUT connector, rms-voltmeter	50 Ohm	
21	amplitudes difference	a	∿ /100 Hz, 10 kHz/10 Vpp	700, amplifier unit 1	Δ U < 0.02 dB	OUTPUT connector, rms-voltmeter	50 Ohm	
22	distortion	c	∿ /1 kHz/17 Vpp, 20 Vpp	—	< 0.35 %, < 0.45 %	OUTPUT connector, distort.-meter	50 Ohm	
23	DFS-frequency response	c	∿ /10 kHz, 1.8 MHz/10 Vpp	—	± 0.3 dB	OUTPUT connector, rms-voltmeter	50 Ohm	if necessary 517 and/or 519 (modulator) may be altered
24	DFS-frequency response	c	∿ /10 kHz, 2.147 MHz/10 Vpp	—	± 0.3 dB	OUTPUT connector, rms-voltmeter	50 Ohm	
	SAWTOOTH ADJUSTMENT							
25	offset	a	◿/1 kHz/10 Vpp	607, modulator unit 1	 these points must be adjusted to 0 ± 70 mV	OUTPUT connector, oscilloscope	50 Ohm	
26	offset	a	◹/1 kHZ/10 Vpp	602, modulator unit 1		OUTPUT connector, oscilloscope	50 Ohm	
27	rms-value	c	◺ ◹ /1 kHz/10 Vpp	—	1.443 ± 0.025 Vrms	OUTPUT connector, rms-voltmeter	50 Ohm	
	PLL-ADJUSTMENT							
28	amplitude triangular wave	a	∿ /3 MHz/10 Vpp	636, PLL unit 1	12.3 ± 0.1 mVrms	T10 with 50 Ω termination, rms-voltmeter	–	
29	offset	c	∿ /3 MHz/10 Vpp	—	– 550 ± 150 mV	T10, DC-voltmeter	–	
30	2nd harmonic	a	∿ /20 MHz/10 Vpp	611, PLL unit 1	minimum, < – 31 dB	T10, spectrum analyser	–	
31	2nd harmonic	a	∿ /2.5 MHz/10 Vpp	614, PLL unit 1	minimum, < – 36 dB	T10, spectrum analyser	–	
32	PLL control voltage	a	∿ /2.5 – 20 MHz/lin. sweep cont/Ts = 0.05 s./10 Vpp	536 (526), PLL unit 1	I A I ≈ I B I < 3 V	T22, oscilloscope Set trimming capacitor 526 to minimum and use it only if there are problems to adjust the required value with only 536. The steps in the diagram above must be horizontal.	–	
33	DC-level, sine shaper – operating point	a	∿ /3 MHz/10 Vpp	684, PLL unit 1	700 ± 20 mV	T11 – T12, DC-voltmeter	–	DC-level between T11 and T12.

Step	Objective	c = check a = adjustment	operation parameters settings	adjusting elements	measured value, value to be adjusted	test point, output measuring instruments	open output/ 50Ωterm.	comment
34	2nd and 3rd harmonics	a	∿ /3 MHz/10 Vpp	706, 709, PLL unit 1	minimum, < – 40 dB	OUTPUT connector, spectrum analyser	50 Ohm	
35	2nd and 3rd harmonics	c	∿ /10 MHz/20 Vpp	—	minimum, < – 34 dB	OUTPUT connector, spectrum analyser	50 Ohm	
36	2nd and 3rd harmonics	c	∿ /10, 15, 20 MHz/20 Vpp	—	minimum, < – 32 dB	OUTPUT connector, spectrum analyser	50 Ohm	
37	output level difference	a	∿ /100 kHz, 3 MHz/10 Vpp	721, PLL unit 1	Δ < 0.03 dB	OUTPUT connector, rms-voltmeter	50 Ohm	
38	DC-difference	a	∿ /3 kHz, 3 MHz/5 Vpp	624, modulator unit 1	Δ < 10 mV	OUTPUT connector, DC-voltmeter	open	
39	output level difference	a	∿ /3 MHz, 20 MHz/10 Vpp	503, modulator unit 1	0 ± 0.3 dB	OUTPUT connector, power meter	50 Ohm	if the output level is too low, change capacitor 501 in the modulator to 27 pF; if the level is too high, increase resistor 714 PLL.
40	output level difference	c	∿ /3 MHz, 20 MHz/10 Vpp	—	+ 0.1 ± 0.3 dB	OUTPUT connector, power meter	50 Ohm	
41	DC-level between T6 and T7	a	∿ /5 MHz/10 Vpp	621, PLL unit 1	< 0.2 mV	T6, T7, DC-voltmeter	–	
	SQUARE WAVE ADJUSTMENT							
42	positive half cycle	a	⎍ /1 kHz/10 Vpp	603/pulse generator unit 1	Tx + 2 μs.	OUTPUT connector, Intervall counter	50 Ohm	Tx = half cycle
43	positive half cycle: T	c (a)	⎍ /1 kHz/10 Vpp	—	500 μs ⩽ T ⩽ 504 μs	OUTPUT connector, Intervall counter	50 Ohm	if < 500 μs adjust with 603 to 500 μs; if > 504 μs check step 6 – DC adjustment
44	positive amplitude	a	⎍ /0.05 Hz/10 Vpp	627/pulse generator unit 1	+ 4.95 ± 0.02 V	OUTPUT connector, DC voltmeter	open	
45	negative amplitude	a	⎍ /0.05 Hz/10 Vpp	624/pulse generator unit 1	– 4.95 ± 0.02 V	OUTPUT connector, DC voltmeter	open	
46	rms-value	c	⎍ /10 kHz/10 Vpp	—	2.5 ± 0.02 Vrms	OUTPUT connector, rms-voltmeter	50 Ohm	if this value is not correct, repeat 42 and 43
47	signalform	a	⎍ /10 MHz/10 Vpp	505/modulator unit 1 (513/output amplif.)	adjust minimum rise-time without overshoot	OUTPUT connector, wide band oscilloscope (tr ⩽ 1 nsec.)	50 Ohm	if 513 must be adjusted, the steps 39 and 40 PLL adjustment) are to be repeated
48	duty cycle	c (a)	⎍ /20 MHz/20 Vpp	(614, pulse generator unit 1)	50 ± 5 %	OUTPUT connector, wide band oscilloscope (tr ⩽ 1 nsec.)	50 Ohm	use potmeter 614 only in case of exceeded limits and adjust it until the limit is reached.

Step	Objective	c = check a = adjustment	operation parameters settings	adjusting elements	measured value, value to be adjusted	test point, output measuring instruments	open output/ 50Ωterm.	comment
49	rise-/falltime	c	⎍ /10 MHz/10 Vpp	—	< 10.5 nsec.	OUTPUT connector, wide band oscilloscope (tr ⩽ 1nsec.)	50 Ohm	
50	aberrations (overshoot etc.)	c	⎍ /1 kHz/10 Vpp	—	< 2 %	OUTPUT connector, wide band oscilloscope (tr ⩽ 1nsec.)	50 Ohm	aberrations related to the amplitude 0 – p.
	AM- AND FM-ADJUSTMENTS							
51	modulation frequency	a	∿ /10 kHz/AM INT/50 %/20 Vpp	699/modulator unit 1	1000 ± 0.1 Hz	MOD-OUT connector, counter	–	
52	distortion	a	∿ /10 kHz/AM INT/50 %/10 Vpp	700, 702/modulator unit	minimum distortion (< 0.5 %)	MOD-OUT connector, distortion meter	–	
53	DC-offset	a	∿ /10 kHz/AM INT/50 %/10 Vpp	709/modulator unit 1	+ 100 ± 50 mV	T21, DC-voltmeter	–	
54	residual FM	a	∿ /5 MHz/FM EXT, MOD OFF/10 Vpp	620/PLL unit 1	minimum (< 500 Hz)	OUTPUT connector, modulation meter	50 Ohm	LF-band width 10 Hz – 20 kHz
55	O-p frequency deviation (average)	a	∿ /5, 10, 15, 20 MHz/FM INT/ 200 kHz DEV/10 Vpp	707/modulator unit 1	200 ± 3 kHz	OUTPUT connector, modulation meter	50 Ohm	± 3 kHz is the average failure over the whole range of 5 – 35 MHz, single failures can exceed these limits.
56	AC-amplitude rms	a	∿ /1 kHz/AM INT/0 %/20 Vpp	684/modulator unit 1	1.767 ± 0.01 Vrms	OUTPUT connector, rms-voltmeter	50 Ohm	
57	modulation depth AM	a	∿ /1 MHz/AM INT/50 %/20 Vpp	686/modulator unit 1	50 ± 0.2 %	OUTPUT connector, modulation meter	50 Ohm	LF bandwidth 10 Hz – 20 kHz
58	LF-suppression	a	∿ /1 MHz/AM INT/50 %/20 Vpp	663/modulator unit 1	minimum level	OUTPUT connector, rms-voltmeter with low pass filter	50 Ohm	
59	DC-difference	a	∿ /5 kHz/AM INT/MOD OFF/ 0 %/10 Vpp, 20 Vpp	681/modulator unit 1	< 1 ± 10 mV	OUTPUT connector, DC-voltmeter	open	
60	level difference	a	∿ /1 MHz, 20 MHz/AM INT/ 0 %/20 Vpp	511/modulator unit 1	–0.2 ± 1 dB	OUTPUT connector, power meter	50 Ohm	

Step	Objective	c = check a = adjustment	operation parameters settings	adjusting elements	measured value, value to be adjusted	test point, output measuring instruments	open output/ 50Ωterm.	comment
	MISCELLANEOUS							
61	frequency response	a	∿ /3 – 20 MHz/sweep lin cont/ Ts = 5 sec/0.001 Vpp	805/amplifier unit 1	0 ± 2 dB	OUTPUT connector, spectrum analyser	50 Ohm	start adjustment at full left position of 805 (bottom view) and turn it until frequency response is within ± 5 dB
62	frequency, level pp	c	∿ /1 kHz/10 Vpp	—	8.589935 MHz ± 3 Hz level > 3.5 Vpp	INT CLOCK connector, counter, oscilloscope with 1 : 10 atten.	–	e. g. PM 6654 with 1 : 10 attenuator
63	frequency	c	∿ /1 MHz/10 Vpp	—	1.164153 MHz ± 1 Hz	OUTPUT connector, counter	50 Ohm	connect external clock signal of 10 MHz ± 10 Hz to EXT CLOCK INPUT
64	frequency, level pp	c	∿ /10 MHz/10 Vpp	—	10 MHz ± 3 Hz level > 3.5 Vpp	TTL out connector, counter, oscilloscope	–	
65	modulation depth	c	∿ /10 MHz/AM EXT/10 Vpp	—	(50 ± 3.5) %	OUTPUT connector, modulation meter	50 Ohm	connect external signal - sine wave, 1 kHz, 1.4 Vpp to MOD INPUT (with 50 Ω)
66	gate function	c	∿ /1 MHz/GATE INT/10 Vpp	—	check of the gate function	OUTPUT connector, oscilloscope	50 Ohm	
67	Mod out amplitude	c	∿ /1 MHz/GATE INT/10 Vpp	—	sine wave 2.8 Vpp	MOD OUT connector, oscilloscope	–	
68	Pen lift amplitude	c	∿ /fstart = 1 kHz/ fstop = 10 kHz/TS = 10 ms/ lin sweep cont/10 Vpp	—	square wave signal > 16 Vpp	PEN LIFT connector, oscilloscope	–	
69	Sweep out amplitude	c	∿ /fstart = 1 kHz/ fstop = 10 kHz/TS = 100 ms/ lin sweep cont/10 Vpp	—	sawtooth signal 10 Vpp	SWEEP OUT connector, oscilloscope	–	

9. SAFETY INSPECTION AND TESTS AFTER REPAIR AND MAINTENANCE IN THE PRIMARY CIRCUIT

9.1. GENERAL DIRECTIVES

- Take care that creepage distances and clearances have not been reduced
- Before soldering, wires:
- should be bent through the holes of solder tags, or wrapped round the tag in the form of an open U, or, wiring ridigity shall be maintained by cable clamps or cable lacing.
- Replace all insulating guards and -plates.

9.2. SAFETY COMPONENTS

Components in the primary circuit may only be renewed by components selected by Philips, see also chapter 10.

9.3. CHECKING THE PROTECTIVE EARTH CONNECTION

The correct connection and condition is checked by visual control and by measuring the resistance between the protective-lead connection at the plug and the cabinet/frame. The resistance shall not be more than 0.5 Ω. During measurement the mains cable should be moved. Resistance variations indicate a defect.

9.4. CHECKING THE INSULATION RESISTANCE

Measure the insulation resistance at U = 500 Vdc between the mains connections and the protective lead connections. For this purpose set the mains switch to ON. The insulation resistance shall not be less than 2 MΩ.

Note:
2 MΩ is a minimum requirement at 40^{0} C and 95 % relative humidity. Under normal conditions the insulation resistance should be much higher (10 to 20 MΩ).

9.5. TEST AFTER REPAIR AND MAINTENANCE

This part of the checking — and adjusting procedure represents the final check of the PM 5192. Bottom and top cover of the cabinet must be closed and the instrument must be warmed up for at least 2 hours. The check contains measurements of DC-levels, amplitudes and frequencies on following their specifications and furthermore some tests of modulation functions. The sequence of the measurements is free selectable.

9.5.1. Frequency Measurements

Objective	Frequency setting	maximum tolerance	connector
basic frequency	1 MHz	±0.8 Hz	OUTPUT
modulation frequency (1 kHz fixed)		±20 Hz	MOD OUT

9.5.2. **Checking of the square wave and pulse signals** (terminate output with 50 Ω)

Setting	Connector	Measuring Instrument
/10 MHz/10 Vpp	OUTPUT	wide band oscilloscope (tr ⩽ 1nsec)

9.5.3. **Modulation Measurements** (terminate OUTPUT with 50 Ω)

Modulation	Carrier Frequency	Object	Measured tolerance
OFF	4.05 MHz	residual FM rms B_{lf} = 10 Hz – 20 kHz	< 500 Hz
"	8,589 MHz		
"	19 MHz		
FM EXT	4,05 MHz		
"	8,589 MHz		
"	19 MHz		
FM INT	5 MHz	frequency deviation setting = 200 kHz (f_{mod} = 1 kHz)	± 30 kHz
"	10 MHz		
"	15 MHz		
AM INT	5 MHz	mod. depth setting 10 %	± 3 %
"	5 MHz	–"– 50 %	± 3 %
"	5 MHz	–"– 90 %	± 4 %

9.5.4. **DC-measurements**

modulation off, frequency = 1 kHz

Waveform	Setting		DC-output open circuit	Tolerance of DC-output open circuit
	AC	DC		
AC OFF	0	0	0	± 30 mV
"	0	–10	–10	± 180 mV
"	0	– 5	– 5	± 105 mV
"	0	5	5	± 105 mV
"	0	10	10	± 180 mV
∿	3,4 . . . , 20 Vpp	0	0	± 100 mV_{max}
	5, 10, 15, 20 Vpp	0	0	± 150 mV
∿ 1 KHz/3 MHz	10 Vpp	0	0	± 100 mV, $\Delta U_{dc} < 25$ mV
AM, frequency = 1 kHz				
∿	10 Vpp	0	0	± 150 mV

9.5.5. **AC-Measurements** (terminate OUTPUT with 50 Ω)

Waveform	Frequency	Modulation	Setting Vpp	Tolerance
∿	1 kHz	OFF	3.1	
″	″	″	3.2	± 1.5 %
″	″	″	6.3	
″	″	″	6.4	
″	″	″	12.7	
″	″	″	12.8	
″	″	″	20.0	
″	″	″	0.30	± 2.0 %
″	″	″	1.00	″
″	″	″	2.00	″
″	″	″	0.003	± 12.5 %
″	″	″	0.010	± 5.5 %
″	″	″	0.020	± 4.0 %
″	″	″	0.050	± 3.1 %
″	″	″	0.100	± 2.8 %
″	″	″	0.200	± 2.65 %
⋀⋁	″	″	10.0	± 1.5 %
⊓⊔	″	″	10.0	± 2.0 %
⟋|	″	″	10.0	± 2.5 %
⟍|	″	″	10.0	± 2.5 %
∿	1 kHz	AM INT 0 %	20.0	± 2.0 %
″	10 kHz	″	20.0	± 2.0 %
″	20 MHz	OFF	20.0	± 4.0 %
″	200 kHz	″	10.0	± 1.5 %
″	1.8 MHz	″	10.0	± 2.5 %
″	2.146 MHz	″	10.0	+ 0 / − 3.0 %
″	20 MHz	″	10.0	± 4.0 %
″	2.146 MHz	″	1.00	± 3.5 %
″	20 MHz	″	1.00	± 6.0 %
″	2.146 MHz	AM INT 0 %	20.0	± 5.0 %
″	20 MHz	″ 0 %	20.0	± 6.0 %
″	2.146 MHz	OFF	0.100	± 5.0 %
″	20 MHz	″	0.100	+ 4.0 / − 8.5 %
″	2.146 MHz	″	0.010	+ 4.0 / − 10.0 %
″	20 MHz	″	0.010	+ 4.0 / − 12.0 %

10. SPARE PARTS

10.1. GENERAL

The synthesizer/function generator PM 5192 is repaired on single component level. No complete boards and modules are available at Concern Service Eindhoven.
Loaded PROMs must be ordered directly via Philips Supply Center Hamburg (please note software version).
In case of difficult faults central repair facility of the complete instrument is possible on special request via repair procedure at Supply Center Hamburg.
Conversion of an existing instrument to a different version is not foreseen.

Standard Parts
Electrical and mechanical parts replacement can be obtained through your local Philips organisation or representative. However, many of the standard electronic components can be obtained from other local suppliers. Before purchasing or ordering replacement parts, check the parts list for value, tolerance, rating and description.

NOTE: Physical size and shape of a component may affect instrument performance, particularly at high frequencies. Always use direct-replacement components, unless it is known that a substitute will not degrade instrument performance.

Special Parts
In addition to the standard electronic components, some special components are used:
- Components, manufactured or selected by Philips to meet specific performance requirements.
- Components which are important for the safety of the instrument marked with 'S' in the parts list.

ATTENTION: Both type of components may only be replaced by components obtained through your local Philips organisation.

10.2. STATIC SENSITIVE COMPONENTS

This instrument contains electrical components that are suspectible to damage from static discharge. Servicing static-sensitive assemblies or components should be performed only at a static-free work station by qualified service personnel.

10.3. HANDLING MOS DEVICES

Though all our MOS integrated circuits incorporate protection against electrostatic discharges, they can nevertheless be damaged by accidental over-voltages. In storing and handling them, the following precautions are recommended.

CAUTION: Testing or handling and mounting call for special attention to personal safety. Personnel handling MOS devices should normally be connected to ground via a resistor.

10.4. SOLDERING TECHNIQUES

Working method:
- Carefully unsolder one after the other the soldering tags of the semi-conductor.
- Remove all superfluous soldering material. Use a sucking iron or sucking litze wire.
- Check that the tags of the replacement part are clean and pre-tinned on the soldering places.
- Locate the replacement semi-conductor exactly on its place, and solder each tag to the relevant printed conductor on the circuit board.

NOTE: Bear in mind that the maximum permissible soldering time is 10 seconds during which the temperature of the tags must not exceed 250° C. The use of solder with a low melting point is therefore recommended.
Take care not to damage the plastic encapsulation of the semi-conductor (softening point of the plastic is 150° C).

ATTENTION: When you are soldering inside the instrument it is essential to use a low-voltage soldering iron, the tip of which must be earthed to ground of the instrument.

Suitable soldering irons should have temperature control and different types of nozzles (pin point tips), e. g. Weller Magnastat WTCP or WECP, Ersa TC 70/24 V.

If a higher wattage-rating soldering iron is used on the etched circuit boards excessive heat can cause the etched circuit wiring to seperate from the board base material.
In general use short-time heating with high tip temperature at a small point, avoid long time heating.

10.5. PARTS LIST PM 5192

10.5.1. Mechanical parts

Cabinet

Item	Quantity	Order number	Description
A	1	5322 447 91368	Top cover
B	1	5322 447 91369	Bottom cover
C	4	5322 462 40756	Plastic foot
D	4	5322 462 44434	Rubber foot, adhesive
E	4	5322 492 64745	Locking clip
F	1	5322 401 10867	Tilting support
G	1	5322 447 91373	Rear panel
H	2	5322 462 40761	Rear bumper
I	1	5322 447 91372	Front panel
J	1	5322 447 91371	Window for display
K	1	5322 456 90277	Text plate PM 5192
L	1	5322 447 90502	Front plate edging (upper)
M	1	5322 466 92117	Front plate edging (lower)
N	1	5322 460 60433	Profile ornament
O	1	5322 460 60452	Profile ornament with text
P	2	5322 447 90501	Side piece
P	2	5322 263 70186	Handle assembly (rack), not shown
Q	1	5322 460 60432	Profile orn. long, perf. (left)
R	1	5322 460 60434	Profile orn. short (right)
R	1	5322 460 60431	Profile orn. short, perf. (right)
S	1	5322 498 50176	Rubber handle
T	1	5322 462 40759	Steel insert

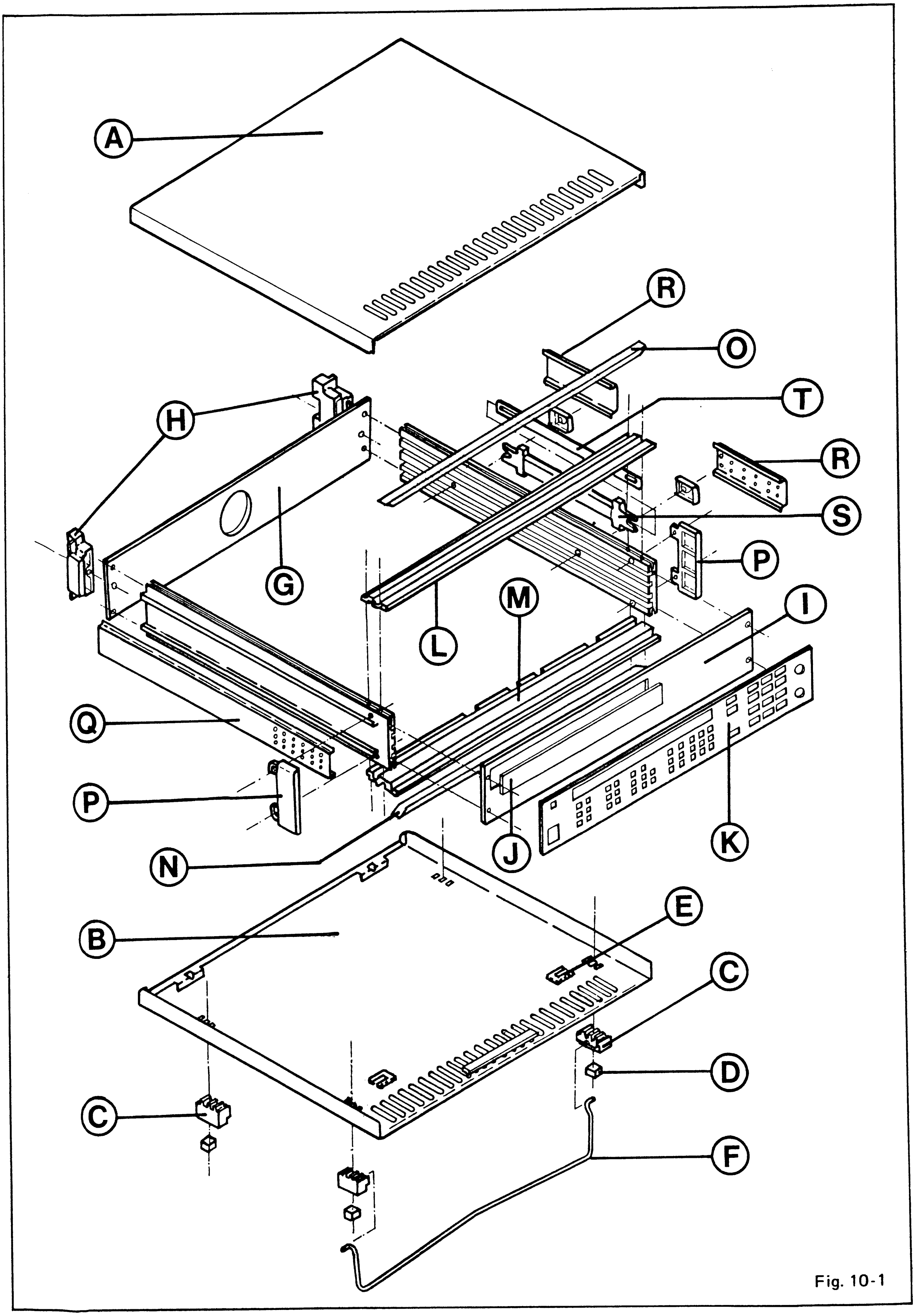

A
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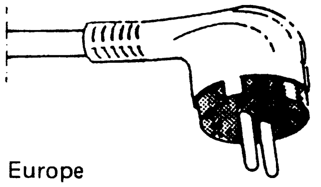
Fig. 10-1

10.5.2. Miscellaneaous, parts not on units

Item	Fig.	Quantity	Order number	Description
880	33 (31)	1	5322 276 12029 *S	Mains switch
881, 882	33 (31)	2	5322 267 10004	BNC connector, front
862-867	33 (32)	6	5322 267 10173	BNC connector, rear
	33 (32)	1	5322 321 22352	IEEE connector wired
870	33 (32)	1	5322 267 30416 *S	Mains socket with filter
	10-2	1	5322 321 10388 *S	Mains cable (Europe)
	10-3	1	5322 321 20816 *S	Mains cable (USA)
	10-4	1	5322 321 10123 *S	Mains cable (U. K.)
	32	1	5322 267 10173 *S	Fuse holder
869	33 (32)	1	4822 253 30018 *S	Fuse 630 mAT
869	33 (32)	1	4822 253 30022 *S	Fuse 1.25 AT
869	33 (32)	1	4822 253 30024 *S	Fuse 1.6 AT
868	33	1	5322 361 10451 *S	Fan
751	33	1	5322 146 21241 *S	Transformer
	31	15	5322 414 60037	Knob, large 12.5 x 6.5
	31	7	5322 414 60036	Knob, small 6.5 x 6.5
	31	29	5322 414 60038	Knob, small with LED
881, 882	33	2	5322 116 21068	Varistor (BNC front)
852-857	33	6	5322 116 21137	Varistor (BNC rear)
850-858	33	9	5322 526 14034	Damping bead (BNC connector)
	32	1	5322 462 44172	Cap for IC (rear panel)

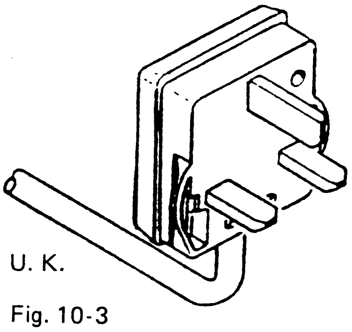
*S = Safety component, see chapter 10.1.

Mains cables



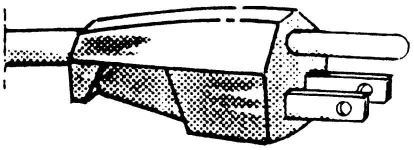
Europe

Fig. 10-2



U. K.

Fig. 10-3



U S A

Fig. 10-4

10.5.3. Electrical Parts

Some parts are listed in chapter 10.5.2.

All metal film resistors not listed are of type MR 25 ± 1 % 0.4 W (ordering code see end of this list).

*1 Please order loaded PROM directly via Philips Supply Center Hamburg (note software version).

UNIT 1, POWER SUPPLY

INTEGRATED CIRCUITS / UNIT 1, POWER SUPPLY

Pos. no.	Description		Ordering code
301, 303	Integr. circuit	Regulator 78GCU1	5322 209 85565
302, 304	Integr. circuit	Regulator 79GCU1	5322 209 86349
305	Integr. circuit	Regulator LM223K	5322 209 71639 (rear wall)

TRANSISTORS / UNIT 1, POWER SUPPLY

Pos. no.	Description		Ordering code
351	Transistor	BD204	5322 130 44324
352	Transistor	BD203	5322 130 44325
360	Transistor	BC558B	4822 130 44197

DIODES / UNIT 1, POWER SUPPLY

Pos. no.	Description		Ordering code
401	Rectifier	SKB2/08L5A	5322 130 32031
402-405	Diode, reference	BZX92	5322 130 34397
417	Diode, reference	BZX75C1V4	4822 130 34047
420, 421	Rectifier	BY260–200	4822 130 32145 (rear wall)

CAPACITORS / UNIT 1, POWER SUPPLY

Pos. no.	Description				Ordering code
501, 503	Cap. foil	220 nF	20 %	100 V	4822 121 40232
502, 504	Cap. electrolyt.	10 000 µF		40 V	5322 124 41278
505, 506	Cap. solid alu.	1 µF		25 V	4822 124 20944
508-510	Cap. foil	220 nF	20 %	100 V	4822 121 40232
511-513	Cap. electrolyt.	4 700 µF		25 V	5322 124 21459
512	Cap foil	220 nF	20 %	100 V	4822 121 40232
514, 515	Cap. solid alu.	1 µF		25 V	4822 124 20944
516	Cap. foil	220 nF	20 %	100 V	4822 121 40232
517	Cap. electrolyt.	22 000 µF		16 V	5322 124 70435
518	Cap. solid alu.	1 µF		25 V	4822 124 20944
519	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
520	Cap. solid alu.	2.2 µF		25 V	4822 124 21255
521-523	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
524, 525	Cap. solid alu.	2.2 µF		40 V	5322 124 14014
526	Cap. electrolyt.	47 µF		63 V	4822 124 40433
527	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
528	Cap. solid alu.	22 µF		10 V	4822 124 20943

RESISTORS / UNIT 1, POWER SUPPLY

Pos. no.	Description				Ordering code
602	Res. metal film	5.6 Ω	5 %	2.5 W	4822 116 52165
604, 609	Potm. trimmer	220 Ω	CERMET	0.1 W	4822 100 10359
607	Res. metal film	5.6 Ω	5 %	2.5 W	4822 116 52165
612	Potm. trimmer	470 Ω	CERMET	0.1 W	5322 101 14047
615	Potm. trimmer	220 Ω	CERMET	0.1 W	4822 100 10359

COIL / UNIT 1, POWER SUPPLY

Pos. no.	Description	Ordering code
800	Coil	5322 158 10271

UNIT 1, AMPLIFIER

INTEGRATED CIRCUITS / UNIT 1, AMPL.

Pos. no.	Description		Ordering code
301	Integr. circuit	MC1558U	5322 209 71645
302	Integr. circuit	HEF4094BP	5322 209 10421
303	Integr. circuit	SE538N	5322 209 71641
304, 305	Integr. circuit	TL071IP	5322 209 71643
306	Integr. circuit	HEF4094BP	5322 209 10421
307	Integr. circuit	DAC-08EN	5322 209 11254
308	Integr. circuit	TL072IP	5322 209 71646
312	Integr. circuit	HEF4094BP	5322 209 10421
313	Integr. circuit	7406N-00	5322 209 86327
314, 315	Integr. circuit	HEF4066BP	5322 209 10357

TRANSISTORS / UNIT 1, AMPL.

Pos. no.	Description		Ordering code
320, 321	Transistor	BC548B	4822 130 40937
330, 332	Transistor	BFW16A	5322 130 44015
331	Transistor	2N4035	5322 130 44201
333	Transistor	BF450	4822 130 44237
334, 335	Transistor	BC558B	4822 130 44197
336	Transistor	BF240	4822 130 40902
337	Transistor	BF450	4822 130 44237
338	Transistor	BC548B	4822 130 40937
339	Transistor	BC558B	4822 130 44197
340	Transistor	BF240	4822 130 40902
341	Transistor	BF450	4822 130 44237
343	Transistor	BC558B	4822 130 44197
344	Transistor	BF240	4822 130 40902
345	Transistor	BF450	4822 130 44237
347	Transistor	BC558B	4822 130 44197
348	Transistor	BF240	4822 130 40902
349	Transistor	BF450	4822 130 44237
351	Transistor	BC558B	4822 130 44197
352	Transistor	BF240	4822 130 40902
353	Transistor	BF450	4822 130 44237

Pos. no.	Description		Ordering code
355	Transistor	BC558B	4822 130 44197
356	Transistor	BF240	4822 130 40902
357	Transistor	BF450	4822 130 44237
359	Transistor	BC558B	4822 130 44197
360	Transistor	BF240	4822 130 40902
361	Transistor	BF450	4822 130 44237
363	Transistor	BC558B	4822 130 44197
364	Transistor	BF240	4822 130 40902
365	Transistor	BSX20	4822 130 41705
366	Transistor	BC558B	4822 130 44197
367	Transistor	BC548B	4822 130 40937
368	Transistor	2N2894A	5322 130 44127
369	Transistor	BC558C	5322 130 60068
370	Transistor	2N4035	5322 130 44201
371	Transistor	BC548B	4822 130 40937
372	Transistor	BSX20	4822 130 41705
373	Transistor	BC558B	4822 130 44197
374	Transistor	BC548C	4822 130 44196
375	Transistor	2N4035	5322 130 44201
376	Transistor	BFW16A	5322 130 44015
377	Transistor	BC548B	4822 130 40937
378	Transistor	BSX20	4822 130 41705
379, 380	Transistor	BFW16A	5322 130 44015
381	Transistor	2N2894A	5322 130 44127
382, 383	Transistor	2N5583	5322 130 44033
384	Transistor	BSS61	5322 130 44714
385-387	Transistor	2N5583	5322 130 44033
388	Transistor	2N2905A	5322 130 40468
389	Transistor	2N2219A	5322 130 44034
390-392	Transistor	BFW16A	5322 130 44015
393	Transistor	BSS52	5322 130 44579
394, 395	Transistor	2N2905A	5322 130 40468
396	Transistor	BF450	4822 130 44237
397, 398	Transistor	2N2219A	5322 130 44034
399	Transistor	BF240	4822 130 40902

DIODES / UNIT 1, AMPL.

403, 404	Diode, ref.	BZV46C1V5	5322 130 34865
405, 406	Diode	BAW62	4822 130 30613
409, 411	Diode, ref.	BZV46C1V5	5322 130 34865
416, 417	Diode, ref.	BZX79C6V8	4822 130 34278
419, 420	Diode, ref.	BZX79C5V1	4822 130 34233
421, 422	Diode, ref.	BZX79C6V2	4822 130 34167
423	Diode, ref.	BZX90	5322 130 34397
424-439	Diode	BA482 (selected)	5322 130 80265
440, 441	Diode, ref.	BZV46C1V5	5322 130 34865
442	Diode. ref.	BZX90	5322 130 34397

CAPACITORS / UNIT 1, AMPL.

Pos. no.	Description				Ordering code
501	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
502	Cap. ceramic	1 nF	10 %	100 V	5322 122 32331
503	Cap. ceramic	2.2 pF	0.25 %	100 V	4822 122 31036
504, 506	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
505, 507	Cap. electrolyt.	220 µF		16 V	4822 124 40196
508-511	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
512	Cap. ceramic	39 pF	2 %	100 V	4822 122 31069
513	Cap. trimmer	5.5-65 pF		100 V	5322 125 54025
514	Cap. solid alu.	10 µF		16 V	4822 124 21314
515	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
516	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
517-520	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
521	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
522	Cap. solid alu.	10 µF		16 V	4822 124 21314
523	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
525	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
526	Cap. ceramic	100 pF	2 %	100 V	4822 122 31316
528-531	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
532, 533	Cap. ceramic	470 pF	2 %	63 V	4822 122 32062
534	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
536, 537	Cap. solid alu.	10 µF		16 V	4822 124 21314
538	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
539	Cap. ceramic	150 pF	2 %	100 V	4822 122 31413
540, 542	Cap. ceramic	68 pF	2 %	100 V	4822 122 31349
544	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
545	Cap. solid alu.	1 µF		25 V	4822 124 20944
547	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
548	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
549, 550	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
551, 553	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
554	Cap. solid alu.	1 µF		25 V	4822 124 20944
555	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
557	Cap. ceramic	15 pF	2 %	100 V	4822 122 31823
560, 561	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
562-565	Cap. ceramic	10 nF	+ 20/– 90 %	40 V	4822 122 30043
566-574	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
577, 578	Cap. ceramic	220 pF	2 %	100 V	5322 122 34047
581, 582	Cap. ceramic	470 nF	20 %	100 V	5322 122 33078
583	Cap. ceramic	1 nF	10 %	100 V	4822 122 30027

Pos. no.	Description				Ordering code
RESISTORS / UNIT 1, AMPL.					
609, 610	Res. metal film	312 Ω	0.1 %	0.25 W	5322 116 80206
611, 612	Res. metal film	412 Ω	0.1 %	0.25 W	5322 116 80211
613, 614	Res. metal film	825 Ω	0.1 %	0.25 W	5322 116 80213
644, 653	Res. network	8 x 4.7 kΩ	5 %	0.125 W	5322 116 90132
666	Potm. trimmer	100 Ω	carb.	0.1 W	4822 100 10075
700	Potm. trimmer	2.2 kΩ	carb.	0.1 W	4822 100 10029
702	Res. metal film	130 Ω	5 %	1.6 W	5322 116 55509
769	Potm. trimmer	1 kΩ	carb.	0.1 W	4822 100 10037
771	Potm. trimmer	4.7 kΩ	carb.	0.1 W	4822 100 10036
787	Res. metal film	100 Ω	0.1 %	0.25 W	5322 116 80208
788	Res. metal film	150 Ω	0.1 %	0.25 W	5322 116 80209
789	Res. metal film	261 Ω	0.1 %	0.25 W	5322 116 80205
792, 793	Res. metal film	100 Ω	0.1 %	0.25 W	5322 116 80208
794	Res. metal film	150 Ω	0.1 %	0.25 W	5322 116 80209
795	Res. metal film	261 Ω	0.1 %	0.25 W	5322 116 80205
805	Potm. trimmer	2.2 kΩ	carb.	0.1 W	4822 100 10029
809	Potm. trimmer	10 kΩ	carb.	0.1 W	4822 100 10035
825-828	Res. metal film	39 Ω	5 %	0.5 W	4822 116 52193
874	Potm. trimmer	470 Ω	carb.	0.1 W	4822 100 10038
COILS / UNIT 1, AMPL.					
831, 832	Wide band choke				5322 158 10271
833, 835	Choke	220 μH			5322 157 53012
838	Wide band choke				5322 157 53015
873	Choke	220 μH			5322 157 53012
RELAIS / UNIT 1, AMPL.					
841-848	Reed relais	5 V			5322 280 20281

UNIT 1, MODULATOR

INTEGRATED CIRCUITS / UNIT 1, MOD.

Pos. no.	Description		Ordering code
301	Integr. circuit	HEF4053BP	5322 209 10576
302	Integr. circuit	HEF4094BP	5322 209 10421
303	Integr. circuit	N74LS02N	5322 209 85312
304	Integr. circuit	N74LS27N	5322 209 85561
305	Integr. circuit	MC1495L	5322 209 71638
306	Integr. circuit	NE538N	5322 209 81343
309	Integr. circuit	TL072ACP−00	5322 209 71644
310	Integr. circuit	XR−2206CP	5322 209 86453
311	Integr. circuit	AD7523JN	5322 209 70195
313, 316	Integr. circuit	HEF4094BP	5322 209 10421
314, 315	Integr. circuit	HEF4053BP	5322 209 10576
317	Integr. circuit	HEF4001BP	4822 209 10246

TRANSISTORS / UNIT 1, MOD.

Pos. no.	Description		Ordering code
350	Transistor	BC558B	4822 130 44197
352-355	Transistor	BC548B	4822 130 40937
358	Transistor	BSX20	4822 130 41705
359	Transistor	2N2894A	5322 130 44127
360, 361	Transistor	BSX20	4822 130 41705
362	Transistor	BC548B	4822 130 40937
363	Transistor	BC558B	4822 130 44197
364, 365	Transistor	2N2894A	5322 130 44127
366, 367	Transistor	BSX20	4822 130 41705
378, 379	Transistor	BF450	4822 130 44237
380, 381	Transistor	BF240	4822 130 40902
382	Transistor	BC548B	4822 130 40937
383	Transistor	BC558B	4822 130 44197

Pos. no.	Description				Ordering code
DIODES / UNIT 1, MOD.					
401, 402	Diode, ref.	BZX79B4V7			4822 130 34174
403-408	Diode, ref.	BZX79B6V8			4822 130 34278
410-418	Diode	BA481			5322 130 32239
421	Diode, ref.	BZX79C8V2			4822 130 34382
422, 423	Diode, ref.	BZX79C6V8			4822 130 34278
426	Diode	BAW62			4822 130 30613
427, 428	Diode, ref.	BZX79B4V7			4822 130 34174
429, 430	Diode, ref.	BZX79B4V3			4822 130 31554
431	Diode, ref.	BZX79B10			4822 130 34297
432, 433	Diode	BA481			5322 130 32239
CAPACITORS / UNIT 1, MOD.					
503	Cap. trimmer	2-22 pF		100 V	4822 125 50045
504	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
505, 511	Cap. trimmer	2-22 pF		100 V	4822 125 50045
513	Cap. ceramic	120 pF	2 %	100 V	4822 122 31685
514-516	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
517	Cap. ceramic	270 pF	2 %	100 V	4822 122 30107
519	Cap. ceramic	220 pF	2 %	100 V	5322 122 34047
521	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
522	Cap. solid alu.	15 μF		16 V	4822 124 20977
523	Cap. solid alu.	1 μF		25 V	4822 124 20944
528	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
529	Cap. solid alu.	15 μF		16 V	4822 124 20977
532	Cap. foil	33 nF	1 %	63 V	5322 121 54111
534	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
535	Cap. trimmer	5.5-65 pF		100 V	5322 125 54025
536	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
537	Cap. electrolyt.	47 μF		25 V	4822 124 22027
540-544	Cap. ceramic	100 nF	10 %	100 V	5322 122 32941
545-546	Cap. ceramic	100 pF	2 %	100 V	4822 122 31316
547	Cap. ceramic	22 pF	2 %	100 V	5322 122 32143
548-549	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
550	Cap. ceramic	2.2 nF	10 %	100 V	4822 122 30114

Pos. no.	Description				Ordering code
RESISTORS / UNIT 1, MOD.					
601	Res. metal film	1 kΩ	0.1 %	0.25 W	5322 116 52384
602, 607	Potm. trimmer	47 kΩ	CERMET	0.5 W	5322 101 10509
604, 606	Res. metal film	2 kΩ	0.1 %	0.25 W	5322 116 51812
609, 611	Res. metal film	2 kΩ	0.1 %	0.25 W	5322 116 51812
613	Potm. trimmer	470 Ω	CERMET	0.5 W	5322 101 14047
624, 627	Potm. trimmer	100 Ω	CERMET	0.5 W	5322 101 14011
663, 681	Potm. trimmer	10 kΩ	carb.	0.1 W	4822 100 10035
684	Potm. trimmer	2.2 kΩ	carb.	0.1 W	4822 100 10029
686	Potm. trimmer	470 Ω	carb.	0.1 W	4822 100 10038
696	Potm. trimmer	1 kΩ	CERMET	0.5 W	4822 100 10254
699	Potm. trimmer	10 kΩ	carb.	0.1 W	4822 100 10035
700	Potm. trimmer	220 Ω	carb.	0.1 W	4822 100 10019
702, 709	Potm. trimmer	22 kΩ	carb.	0.1 W	4822 100 10051
707	Potm. trimmer	1 kΩ	carb.	0.1 W	4822 100 10037
714	Res. N. T. C.	4.7 kΩ	10 %	0.25 W	5322 116 30215
COILS / UNIT 1, MOD.					
801	Choke	0.33 μH			5322 157 53013
802	Choke	220 μH			5322 157 53012
RELAIS / UNIT 1, MOD.					
810-812	Reed relais	5 V			5322 280 20281

UNIT 1, PULSE GENERATOR

INTEGRATED CIRCUITS / UNIT 1, PULSE GEN.

Pos. no.	Description		Ordering code
301	Integr. circuit	NE521N	5322 209 14441
302	Integr. circuit	N74LS86N	5322 209 84997
303	Integr. circuit	N74S258N	5322 209 85674
305	Integr. circuit	N74S00N	5322 209 84167

TRANSISTORS / UNIT 1, PULSE GEN.

Pos. no.	Description		Ordering code
351	Transistor	BC548B	4822 130 40937
352, 354	Transistor	2N2894A	5322 130 44127
353, 355	Transistor	BSX20	4822 130 41705
356, 357	Transistor	2N2894A	5322 130 44127
358	Transistor	2N5583	5322 130 44033

DIODES / UNIT 1, PULSE GEN.

Pos. no.	Description		Ordering code
401, 402	Diode, ref.	BZX79B4V7	4822 130 34174
403	Diode, ref.	BZV46C2V0	4822 130 31248
404	Diode, ref.	BZX79B6V2	4822 130 34167
405-408	Diode	BA481	5322 130 32239
409, 410	Diode, ref.	BZX75C2V8	4822 130 34048
411	Diode, ref.	BZX79B4V7	4822 130 34174
412, 413	Diode	BA481	5322 130 32239
414	Diode, ref.	BZX79B3V3	5322 130 31504
428	Diode	BAW62	4822 130 30613

CAPACITORS / UNIT 1, PULSE GEN.

Pos. no.	Description				Ordering code
501, 502	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
503	Cap. ceramic	220 pF	2 %	100 V	5322 122 34047
504, 505	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
506, 507	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
508, 509	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
510, 511	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
512-517	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
518, 519	Cap. solid alu.	10 μF		16 V	4822 124 21314
520	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
541	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414

RESISTORS / UNIT 1, PULSE GEN.

Pos. no.	Description				Ordering code
603	Potm. trimmer	100 kΩ	carb.	0.1 W	4822 100 10052
614	Potm. trimmer	4.7 kΩ	carb.	0.1 W	4822 100 10036
624, 627	Potm. trimmer	4.7 kΩ	carb.	0.1 W	4822 100 10036
650	Res. metal film	150 Ω	5 %	1.6 W	4822 116 51142

COILS / UNIT 1, PULSE GEN.

Pos. no.	Description		Ordering code
806	Choke	22 μH	5322 157 50317

UNIT 1, PLL / VCO

INTEGRATED CIRCUITS / UNIT 1, PLL / VCO

Pos. no.	Description		Ordering code
301	Integr. circuit	HEF4094BP	5322 209 10421
302	Integr. circuit	DAC–08EN	5322 209 11254
303, 312	Integr. circuit	LF356N	5322 209 86451
304, 307	Integr. circuit	LF256H	5322 209 71642
306	Integr. circuit	TL072IP	5322 209 71646
309	Integr. circuit	MC4044P	5322 209 85821
311	Integr. circuit	MC1456P1	5322 209 71647
313	Integr. circuit	HEF4053BP	5322 209 10576
314	Integr. circuit	N74S00N	5322 209 84167
316	Integr. circuit	SN74197N	5322 209 84516
317	Integr. circuit	N74LS393N	4822 209 80447
318	Integr. circuit	N74LS00N	5322 209 84823
320	Integr. circuit	MC3346P (CA3086)	5322 209 84111

TRANSISTORS / UNIT 1, PLL / VCO

Pos. no.	Description		Ordering code
351, 354	Transistor	BC548B	4822 130 40937
352, 353	Transistor	BC558B	4822 130 44197
356	Transistor	BF450	4822 130 44237
357, 358	Transistor	2N4035	5322 130 44201
359	Transistor	BF240	4822 130 40902
361	Transistor	BSV79	5322 130 44017
362	Transistor	BFX89	5322 130 40542
363	Transistor	BFW16A	5322 130 44015
364, 366	Transistor	BFX89	5322 130 40542
367, 368	Transistor	2N2894A	5322 130 44127
369, 371	Transistor	PH2369	4822 130 41594
372	Transistor	BC548B	4822 130 40937
373	Transistor	BC558B	4822 130 44197
374, 376	Transistor	2N4035	5322 130 44201
375, 377	Transistor	BF240	4822 130 40902
378	Transistor	2N4035	5322 130 44201
379	Transistor	BF450	4822 130 44237
380	Transistor	BF240	4822 130 40902
381	Transistor	BC558B	4822 130 44197
382	Integr. circuit	Regul. UA7815UC	4822 209 80808

DIODES / UNIT 1, PLL / VCO

Pos. no.	Description		Ordering code
401, 402	Diode, ref.	BZX79C4V7	4822 130 34174
403	Diode, ref.	BZX91	5322 130 34397
404-407	Diode, ref.	BZX79C4V7	4822 130 34174
408	Diode, ref.	BZX79C4V3	4822 130 31554
409, 411	Diode, ref.	BZX79C5V1	4822 130 34233
412	Diode, ref.	BZX91	5322 130 34397
413, 417	Diode, ref.	BZX79C4V7	4822 130 34174
414, 420	Diode	BAW62	4822 130 30613
416	Diode, ref.	BZX79C6V2	4822 130 34167
418	Diode, ref.	BZV46C1V5	5322 130 34865
419	Diode, ref.	BZX79B3V3	5322 130 31504

CAPACITORS / UNIT 1, PLL / VCO

Pos. no.	Description				Ordering code
502	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
503	Cap. solid alu.	10 µF		16 V	4822 124 21314
504-506	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
507, 508	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
509	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
511	Cap. ceramic	220 pF	2 %	100 V	5322 122 34047
512	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
513, 514	Cap. tantal	47 µF		6.3 V	4822 124 10197
516	Cap. foil	4.7 nF	5 %	63 V	4822 121 50539
517	Cap. ceramic	470 pF	2 %	63 V	4822 122 32062
518, 519	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
521, 522	Cap. electrolyt.	100 µF		25 V	4822 124 40207
523, 524	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
526	Cap. trimmer	2-10 pF		100 V	4822 125 50062
527	Cap. ceramic	56 pF	2 %	100 V	4822 122 32027
529	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
533, 547	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
536	Cap. trimmer	2-22 pF		100 V	4822 125 50045
537	Cap. ceramic	68 pF	2 %	100 V	4822 122 31349
539	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
541	Cap. ceramic	470 pF	2 %	63 V	4822 122 32062
542	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
544	Cap. ceramic	2.2 nF	10 %	100 V	4822 122 30114
548-552	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
556	Cap. tantal	47 µF		6.3 V	5322 124 10197
557, 566	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941
558	Cap. solid alu.	10 µF		16 V	4822 124 21314
559-562	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
563	Cap. ceramic	27 pF	2 %	100 V	4822 122 30045
564-565	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
568, 569	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
570	Cap. ceramic	220 pF	2 %	100 V	5322 122 34047
571	Cap. ceramic	100 nF	10 %	20 V	5322 122 30108
572	Cap. ceramic	10 nF	+ 50/– 20 %	100 V	4822 122 31414
573, 574	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941

RESISTORS / UNIT 1, PLL / VCO

611	Potm. trimmer	470 Ω	carb.	0.1 W	4822 100 10038
614, 621	Potm. trimmer	10 kΩ	carb.	0.1 W	4822 100 10035
620	Potm. trimmer	22 kΩ	carb.	0.1 W	4822 100 10051
625	Resistor	10 MΩ	5 %	0.25 W	4822 110 72214
636	Potm. trimmer	220 Ω	carb.	0.1 W	4822 100 10019
650, 655	Res. metal film	39 Ω	5 %	0.5 W	4822 116 52193
684	Potm. trimmer	100 Ω	carb.	0.1 W	4822 100 10075
706, 709	Potm. trimmer	22 kΩ	carb.	0.1 W	4822 100 10051
721	Potm. trimmer	47 Ω	carb.	0.1 W	4822 100 10476

COILS / UNIT 1, PLL / VCO

801-803	Damping lead	FXC3B	5322 526 10366
805	Damping lead	FXC3B	5322 526 10366
804, 807	Coil	4.7 µH	5322 158 10628
808	Coil	15 µH	5322 158 14004

Pos. no.	Description		Ordering code

UNIT 2, CPU

INTEGRATED CIRCUITS / UNIT 2, CPU

301-304	Integr. circuit	MC3441AP	5322 209 85464
305	Integr. circuit	HEF4093BP	5322 209 14927
306	Integr. circuit	HEF4738VP	5322 209 14509
307	Integr. circuit	HEF40244BP	5322 209 10489
308	Integr. circuit	HEF40373BP	5322 209 10491
309, 310	Integr. circuit	HEF4014BP	4822 209 10296
311	Integr. circuit	HEF4094BP	5322 209 10421
312	Integr. circuit	HEF4514BP	5322 209 14051
313	Integr. circuit	N74LS175N	5322 209 84999
314	Integr. circuit	N74LS363N	5322 209 81776
315^{*1}	I. C. P27128	(PROM, software version)	
317	Integr. circuit	PC74HCUO4P	5322 209 11323
318	Integr. circuit	P8031U processor	5322 209 82034
319	Integr. circuit	N74LS390N	5322 209 86362
320	Integr. circuit	HEF4049BP	4822 209 10306
321	Integr. circuit	HEF4094BP	5322 209 10421
322	Integr. circuit	LM741CN	4822 209 80617
323	Integr. circuit	DAC–08EN	5322 209 11254
324, 325	Integr. circuit	PCD8571P DIL8	4822 209 83571

TRANSISTOR / UNIT 2, CPU

351	Transistor	BC337–16	4822 130 41095

DIODE / UNIT 2, CPU

401-404	Diode	BAW62	4822 130 30613

CAPACITORS / UNIT 2, CPU

501	Cap. solid alu.	3.3 µF		16 V	4822 124 20947
502	Cap. ceramic	220 pF	10 %	100 V	4822 122 30094
503, 505	Cap. ceramic	22 pF	2 %	100 V	5322 122 32143
504	Cap. ceramic	27 pF	2 %	100 V	4822 122 30045
506-511	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
512	Cap. electrolyt.	220 µF		16 V	4822 124 40196
513-515	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
517	Cap. ceramic	100 nF	10 %	50 V	5322 122 32941

Pos. no.	Description				Ordering code

RESISTORS / UNIT 2, CPU

601, 602	Res. network	8 x 4.7 kΩ	5 %	0.125 W	5322 116 90132
604, 605	Res. network	8 x 4.7 kΩ	5 %	0.125 W	5322 116 90132
609	Res. network	8 x 4.7 kΩ	5 %	0.125 W	5322 116 90132

CRYSTAL / UNIT 2, CPU

801	Crystal	10 MHz	5322 242 71724

MISCELLANEOUS / UNIT 2, CPU

802	Lithium cell	3V/160mAh	5322 138 10144

UNIT 2, DFS

INTEGRATED CIRCUITS / UNIT 2, DFS

Pos. no.	Description		Ordering code
301	Integr. circuit	PC74HCUO4P	5322 209 11323
302, 306	Integr. circuit	N74LS132N	5322 209 85201
303	Integr. circuit	SN74LS109AN	5322 209 85974
304, 305	Integr. circuit	N74LS02N	5322 209 85312
307-311	Integr. circuit	HEF4094BP	5322 209 10421
312-321	Integr. circuit	N74LS283N	5322 209 86052
322, 323	Integr. circuit	N74LS273N	5322 209 85792
324	Integr. circuit	N74LS174N	5322 209 81632
325, 326	Integr. circuit	N74LS273N	5322 209 85792
327	Integr. circuit	N74LS86N	5322 209 84997
328	Integr. circuit	N74LS153N	5322 209 85488
329	Integr. circuit	SN74LS151N	5322 209 86452
330	Integr. circuit	N74LS00N	5322 209 84823
331, 332	Integr. circuit	N74LS86N	5322 209 84997
333	Integr. circuit	N74LS174N	5322 209 81632
334	Integr. circuit	N74LS175N	5322 209 84999
335	Integr. circuit	N82S115N (sine ROM)	5322 209 82603
336, 337	Integr. circuit	N74LS157N	5322 209 81521
338	Integr. circuit	N74LS174N	5322 209 81632
339	Integr. circuit	N74LS175N	5322 209 84999
340, 341	Integr. circuit	N74LS86N	5322 209 84997
342	Integr. circuit	N74LS273N	5322 209 85792
343	Integr. circuit	N74LS175N	5322 209 84999
344, 345	Integr. circuit	N74S04N	5322 209 84475
346	Integr. circuit	MC1458N	4822 209 81349
347	Integr. circuit	SN74LS151N	5322 209 86452
348	Integr. circuit	HEF4050BP	4822 209 10261

TRANSISTORS / UNIT 2, DFS

Pos. no.	Description		Ordering code
379, 402	Transistor	BC548B	4822 130 40937
401	Transistor	BC558B	4822 130 44197
403	Transistor	BC558C	5322 130 60068
404, 407	Transistor	BC558B	4822 130 44197
405, 406	Transistor	BC558C	5322 130 60068
408, 409	Transistor	BC558C	5322 130 60068
410, 413	Transistor	BC558B	4822 130 44197
411, 412	Transistor	BC558C	5322 130 60068
414, 415	Transistor	BC558C	5322 130 60068
416, 419	Transistor	BC558B	4822 130 44197
417, 418	Transistor	BC558C	5322 130 60068
420, 421	Transistor	BC558C	5322 130 60068
422, 425	Transistor	BC558B	4822 130 44197
423, 424	Transistor	BC558C	5322 130 60068
426, 427	Transistor	BC558C	5322 130 60068
428, 430	Transistor	BC558B	4822 130 44197
429	Transistor	BC558C	5322 130 60068
431	Transistor	BC548B	4822 130 40937

Pos. no.	Description		Ordering code

DIODES / UNIT 2, DFS

451-460	Diode	BAW62	4822 130 30613
461, 463	Diode, ref.	BZX79B4V3	4822 130 31554
462, 464	Diode	BAW62	4822 130 30613
465, 467	Diode, ref.	BZW79B4V3	4822 130 31554
466, 468	Diode	BAW62	4822 130 30613
469, 471	Diode, ref.	BZX79B4V3	4822 130 31554
470, 472	Diode	BAW62	4822 130 30613
473, 475	Diode, ref.	BZX79B4V3	4822 130 31554
474, 476	Diode	BAW62	4822 130 30613
477	Diode, ref.	BZX79B4V3	4822 130 31554
478	Diode, ref.	BZV46X2V0	4822 130 31248

CAPACITORS / UNIT 2, DFS

501, 507	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
502	Cap. ceramic	22 pF	2 %	100 V	5322 122 32143
503	Cap. ceramic	56 pF	2 %	100 V	4822 122 32027
504	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
505	Cap. trimmer	2.5-27 pF		100 V	5322 125 54083
506	Cap. solid alu.	1 µF		25 V	4822 124 20944
511-517	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
518	Cap. electrolyt.	220 µF		16 V	4822 124 40196
519	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
520, 521	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
522	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
523, 524	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
525, 526	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
527	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
528	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
529, 530	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
531	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
532	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
533, 534	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
535	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
536	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
537, 538	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
539	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
540	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
541, 542	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
543	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
544, 548	Cap. ceramic	33 pF	2 %	100 V	5322 122 32072
545, 546	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
547	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
549	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
550, 551	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
553	Cap. ceramic	82 pF	2 %	100 V	4822 122 31237
554	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
555, 556	Cap. ceramic	4.7 nF	10 %	100 V	4822 122 31125
558	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103

Pos. no.	Description				Ordering code
559	Cap. ceramic	22 pF	2 %	100 V	5322 122 32143
560, 562	Cap. ceramic	180 pF	2 %	100 V	5322 122 31907
561	Cap. ceramic	10 pF	2 %	100 V	4822 122 32185
563	Cap. solid alu.	6.8 µF		25 V	5322 124 14081
564, 566	Cap. solid alu.	10 µF		16 V	4822 124 21314
565, 567	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103

RESISTORS / UNIT 2, DFS

637	Res. metal film	619 Ω	0.1 %	0.25 W	5322 116 80212
639, 646	Res. metal film	1.87 kΩ	0.1 %	0.25 W	5322 116 80215
642, 649	Res. metal film	4.64 kΩ	0.1 %	0.25 W	5322 116 80216
643, 650	Res. metal film	1.69 kΩ	0.1 %	0.25 W	5322 116 80214
644, 651	Res. metal film	619 Ω	0.1 %	0.25 W	5322 116 80212
653, 660	Res. metal film	1.87 kΩ	0.1 %	0.25 W	5322 116 80215
656, 663	Res. metal film	4.64 kΩ	0.1 %	0.25 W	5322 116 80216
657, 664	Res. metal film	1.69 kΩ	0.1 %	0.25 W	5322 116 80214
658, 665	Res. metal film	619 Ω	0.1 %	0.25 W	5322 116 80212
667	Res. metal film	1.87 kΩ	0.1 %	0.25 W	5322 116 80215
670	Res. metal film	4.64 kΩ	0.1 %	0.25 W	5322 116 80216
671	Res. metal film	1.69 kΩ	0.1 %	0.25 W	5322 116 80214
672	Res. metal film	619 Ω	0.1 %	0.25 W	5322 116 80212
673	Res. metal film	11.5 kΩ	0.1 %	0.25 W	5322 116 51742
674	Res. metal film	9.53 kΩ	0.1 %	0.25 W	5322 116 80207
676	Potm. trimmer	470 Ω	carb.	0.1 W	4822 100 10038
679	Res. metal film	1.87 kΩ	0.1 %	0.25 W	5322 116 80215
682, 683	Res. metal film	4.64 kΩ	0.1 %	0.25 W	5322 116 80216
684, 685	Res. metal film	1.69 kΩ	0.1 %	0.25 W	5322 116 80214
686	Res. metal film	11.5 kΩ	0.1 %	0.25 W	5322 116 51742
687	Res. metal film	9.53 kΩ	0.1 %	0.25 W	5322 116 80207
689	Potm. trimmer	1 kΩ	carb.	0.1 W	4822 100 10037
693	Potm. trimmer	4.7 kΩ	CERMET	0.5 W	5322 101 10509

CRYSTAL / UNIT 2, DFS

810	Crystal	8.59 MHz	5322 242 74407

COILS / UNIT 2, DFS

802	Wide band choke	5322 158 10271
803	Choke	5322 158 20458
804	Choke	5322 158 20459

Pos. no.	Description				Ordering code
UNIT 3, KEYBOARD DISPLAY					
INTEGRATED CIRCUITS / UNIT 3					
351	Integr. circuit	HEF4049BP			4822 209 10306
352	Integr. circuit	MM5450N			4822 209 10199
353	Integr. circuit	SAA3007			5322 209 72061
TRANSISTOR / UNIT 3					
301	Transistor	BD646			4822 130 41212
DIODE / UNIT 3					
409, 410	Diode	BAW62			4822 130 30613
LEDs, DISPLAYS / UNIT 3					
401-404	LED	CQY54A			4822 130 31128
405-408	Display	LTM86 . . .			5322 130 90375
CAPACITORS / UNIT 3					
501	Cap. electrolyt.	220 µH		16 V	4822 124 40196
502-504	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
505, 506	Cap. ceramic	100 pF	2 %	100 V	4822 122 31316
507, 508	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
SWITCHES / UNIT 3					
801	Key switch	M75120001			5322 276 14338
802-817	Key switch	M75120051			5322 276 14418
819, 820	Key switch	M75120001			5322 276 14338
821-828	Key switch	M75120051			5322 276 14418
829, 834	Key switch	M75120001			5322 276 14338
830-833	Key switch	M75120051			5322 276 14418
835-840	Key switch	M75120051			5322 276 14418
841	Key switch	M75120001			5322 276 14338
842	Key switch	M75120051			5322 276 14418
843-858	Key switch	M75120001			5322 276 14338
MISCELLANEOUS / UNIT 3					
860	Cer. resonator	455 kHz			5322 242 71606

Pos. no.	Description		Ordering code

UNIT 3, KEYBOARD DISPLAY

INTEGRATED CIRCUITS / UNIT 3

351	Integr. circuit	HEF4049BP	4822 209 10306
352	Integr. circuit	MM5450N	4822 209 10199
353	Integr. circuit	SAA3007	5322 209 72061

TRANSISTOR / UNIT 3

301	Transistor	BD646	4822 130 41212

DIODE / UNIT 3

409, 410	Diode	BAW62	4822 130 30613

LEDs, DISPLAYS / UNIT 3

401-404	LED	CQY54A	4822 130 31128
405-408	Display	LTM86 . . .	5322 130 90375

CAPACITORS / UNIT 3

501	Cap. electrolyt.	220 µH		16 V	4822 124 40196
502-504	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103
505, 506	Cap. ceramic	100 pF	2 %	100 V	4822 122 31316
507, 508	Cap. ceramic	22 nF	+ 20/– 90 %	40 V	4822 122 30103

SWITCHES / UNIT 3

801	Key switch	M75120001	5322 276 14338
802-817	Key switch	M75120051	5322 276 14418
819, 820	Key switch	M75120001	5322 276 14338
821-828	Key switch	M75120051	5322 276 14418
829, 834	Key switch	M75120001	5322 276 14338
830-833	Key switch	M75120051	5322 276 14418
835-840	Key switch	M75120051	5322 276 14418
841	Key switch	M75120001	5322 276 14338
842	Key switch	M75120051	5322 276 14418
843-858	Key switch	M75120001	5322 276 14338

MISCELLANEOUS / UNIT 3

860	Cer. resonator	455 kHz	5322 242 71606

LACQUERED METAL FILM RESISTORS MR25

style	resistance range	tol. ±%	series	temperature coefficient ±ppm/°C	limiting voltage (r.m.s.) V	service code no. 5322 116 5.... followed by
MR 25	4,99 Ω – 301 kΩ	1	E96	50 *	250	

* For resistance values lower than 49,9 Ω: 100 ppm/°C.

4,99	0568	16,5	4109	54,9	4445	182	4493	604	4528
5,11	4192	16,9	0627	56,2	4446	187	4494	619	4529
5,23	4113	17,4	4432	57,6	4447	191	4495	634	4531
5,36	4239	17,8	0418	59	4448	196	0676	649	4532
5,49	4102	18,2	4083	60,4	4449	200	4496	665	4533
5,62	4128	18,7	0895	61,9	4451	205	0669	681	4534
5,76	4413	19,1	4104	63,4	4375	210	4036	698	4037
5,90	1064	19,6	0473	64,9	4453	215	0457	715	0571
6,04	4114	20	1048	66,5	4454	221	4002	732	4535
6,19	1049	20,5	0678	68,1	4455	226	4497	750	4536
6,34	0862	21	4433	69,8	4456	232	4498	768	4537
6,49	4112	21,5	0677	71,5	4457	237	0679	787	4538
6,65	4414	22,1	0983	73,2	4458	243	0437	806	4539
6,81	4013	22,6	0491	75	4459	249	4499	825	4541
6,98	4103	23,2	4434	76,8	0494	255	4501	845	4542
7,15	4415	23,7	4014	78,7	0578	261	4502	866	4543
7,32	4416	24,3	4435	80,6	4461	267	4503	887	4544
7,50	4417	24,9	0903	82,5	4462	274	4504	909	4545
7,68	4418	25,5	4436	84,5	4463	280	4505	931	4546
7,87	4046	26,1	0876	86,6	4464	287	4506	953	4547
8,06	4419	26,7	4067	88,7	4465	294	4507	976	4548
8,25	4099	27,4	0493	90,9	4466	301	4508	1K	4549
8,45	4421	28	0623	93,1	4467	309	4509	1K02	4551
8,66	1051	28,7	4068	95,3	0569	316	4511	1K05	4552
8,87	4101	29,4	4084	97,6	4468	324	4512	1K07	4553
9,09	0863	30,1	0904	100	4469	332	4513	1K1	4554
9,31	4422	30,9	4437	102	4471	340	4514	1K13	4555
9,53	4258	31,6	4034	105	4472	348	4515	1K15	0415
9,76	4423	32,4	4105	107	4473	357	0603	1K18	4556
10	0452	33,2	0527	110	4474	365	4516	1K21	4557
10,2	4111	34	4438	113	4475	374	4517	1K24	4559
10,5	4071	34,8	4027	115	4476	383	4518	1K27	0555
10,7	4424	35,7	4439	118	4477	392	4006	1K3	0526
11	4059	36,5	0409	121	4426	402	4519	1K33	4561
11,3	4425	37,4	4158	124	4478	412	4521	1K37	0628
11,5	0838	38,3	0954	127	4479	422	0459	1K4	4562
11,8	0738	39,2	4087	130	4481	432	4522	1K43	4563
12,1	4069	40,2	0926	133	4482	442	0592	1K47	0635
12,4	4427	41,2	4108	137	4483	453	4523	1K5	4564
12,7	4261	42,2	1052	140	4484	464	0536	1K54	0586
13	4082	43,2	0519	143	4485	475	4007	1K58	0622
13,3	1047	44,2	0818	147	0766	487	0508	1K62	4565
13,7	4428	45,3	0795	150	4486	499	4524	1K65	4566
14	0839	46,4	0492	154	0506	511	4525	1K69	4567
14,3	4429	47,5	0952	158	4487	523	4526	1K74	0629
14,7	0412	48,7	0511	162	0417	536	0621	1K78	5015
15	0902	49,9	4441	165	4488	549	0732	1K82	4568
15,4	0925	51,1	4442	169	4489	562	4009	1K87	0728
15,8	0861	52,3	4443	174	4491	576	4527	1K91	4569
16,2	4431	53,6	4444	178	4492	590	0561	1K96	4571

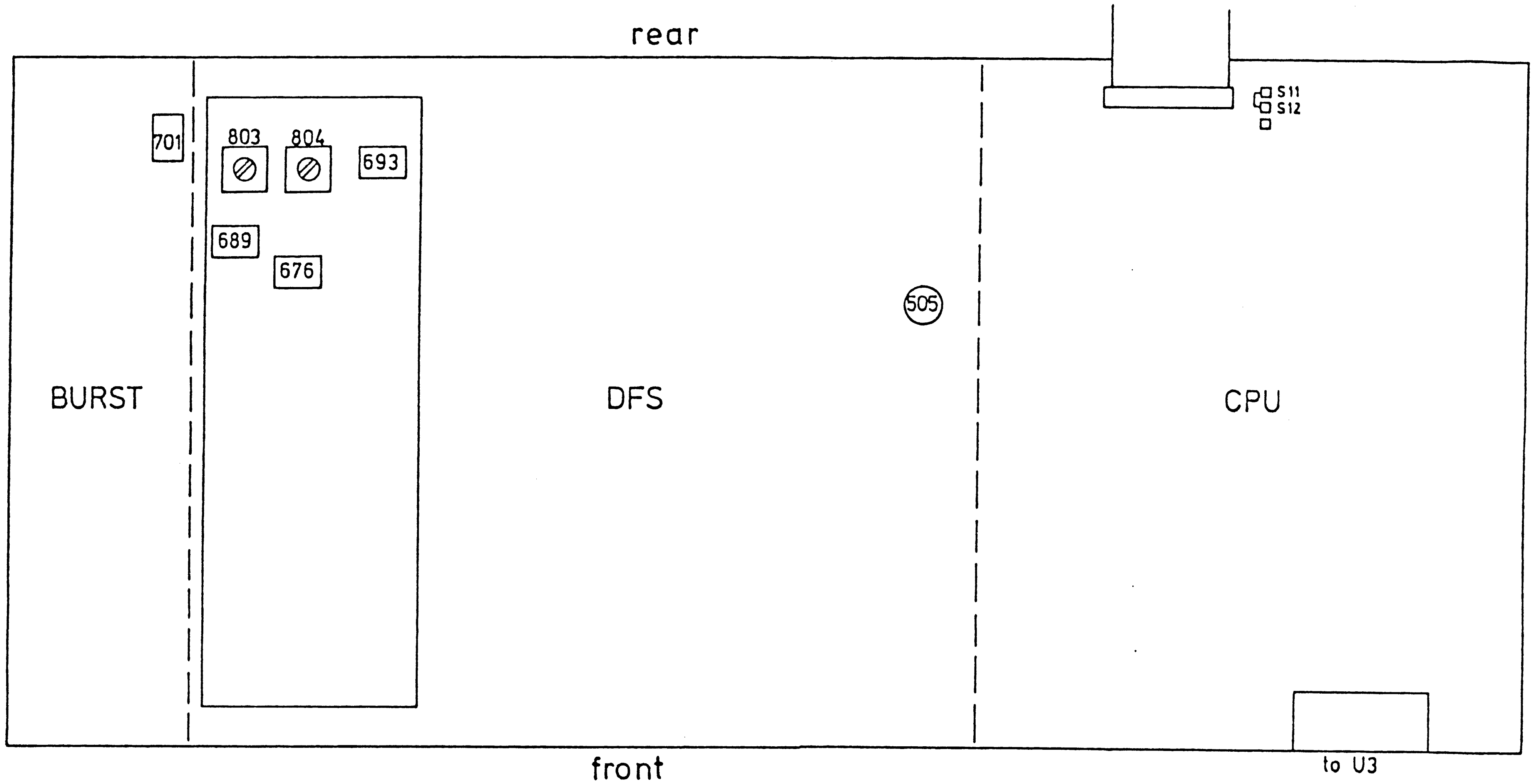
2K	4572	6K65	4604	22K1	4003	73K2	0666	243K	4733
2K05	0664	6K81	4012	22K6	0481	75K	4686	249K	4734
2K1	4573	6K98	4605	23K2	4645	76K8	4687	255K	4735
2K15	0767	7K15	4606	23K7	4646	78K7	0533	261K	4736
2K21	4574	7K32	4607	24K3	4647	80K6	4688	267K	4737
2K26	0675	7K5	4608	24K9	4648	82K5	4689	274K	4738
2K32	4575	7K68	4609	25K5	4649	84K5	4691	280K	4739
2K37	4576	7K87	0458	26K1	4651	86K6	4692	287K	4741
2K43	4004	8K06	4611	26K1	4652	88K7	4693	294K	4742
2K49	0581	8K25	4558	27K4	0559	90K9	4694	301K	4743
2K55	4577	8K45	4612	28K	0667	93K1	4297	316 K	5268
2K61	0671	8K66	4613	28K7	4653	95K3	0567	332 K	1184*
2K67	4578	8K87	4614	29K4	4654	97K6	4695	348 K	5499
2K74	0636	9K09	4615	30K1	4655	100K	4696	365 K	5641
2K8	4579	9K31	4616	30K9	4656	102K	4697	374 K	5457
2K87	0414	9K53	4617	31K6	4657	105K	4698	383 K	5335
2K94	4581	9K76	4618	32K4	4658	107K	4699	402 K	5283
3K01	0524	10K	4619	33K2	0482	110K	4701	412 K	5424
3K09	4582	10K2	4621	34K	4659	113K	4702	422 K	5247
3K16	0579	10K5	0731	34K8	4661	115K	4279	442 K	5458
3K24	4583	10K7	4622	35K7	4662	118K	4703	464 K	5207
3K32	4005	11K	4623	36K5	0726	121K	4704	475 K	1275
3K4	4584	11K3	0668	37K4	4663	124K	4705	499 K	5468
3K48	4585	11K5	4624	38K3	0483	127K	4706	511 K	5258
3K57	4586	11K8	4625	39K2	4664	130K	4707	536 K	4758
3K65	4587	12K1	0572	40K2	4665	133K	4708	562 K	1169
3K74	4588	12K4	4626	41K2	4666	137K	4709	590 K	5567
3K83	4589	12K7	0443	42K2	0474	140K	4259	619 K	5315
3K92	4591	13K	0522	43K2	4667	143K	4711	649 K	5331
4K02	4592	13K3	4627	44K2	4668	147K	4712	681 K	5284
4K12	4593	13K7	4628	45K3	4669	150K	4713	750 K	5532
4K22	0729	14K	4629	46K4	0557	154K	4714	806 K	1369
4K32	4594	14K3	4631	47K5	4671	158K	4715	825 K	1398
4K42	0556	14K7	4632	48K7	0442	162K	4716	866 K	1395
4K53	0631	15K	4001	49K9	0674	165K	4717	909 K	5533
4K64	0484	15K4	0479	51K1	0672	169K	4718	953 K	1368
4K75	4008	15K8	4633	52K3	4673	174K	4719	1MAO	5535
4K87	0509	16K2	0593	53K6	4674	178K	4721		
4K99	0523	16K5	4634	54K9	4675	182K	4722		
5K11	4595	16K9	4635	56K2	4676	187K	4723		
5K23	4596	17K4	4636	57K6	4677	191K	4724		
5K36	4597	17K8	4637	59K	4678	196K	4725		
5K49	4598	18K2	4638	60K4	4679	200K	4726		
5K62	4011	18K7	0558	61K9	0872	205K	4727		
5K76	4599	19K1	4639	63K4	4681	210K	4208		
5K9	0583	19K6	4641	64K9	0514	215K	4728		
6K04	4601	20K	4642	66K5	4682	221K	4038		
6K19	0608	20K5	4643	68K1	4683	226K	4729		
6K34	4602	21K	4644	69K8	4684	232K	4731		
6K49	4603	21K5	0451	71K5	4685	237K	4732		

* 4822 116 5 . . . .

Figures

FIGURES 28 – 44

Fig. 28 Unit 2, adjusting elements
Fig. 29 Unit 1, adjusting elements
Fig. 30 Block diagram
Fig. 31 Front view
Fig. 32 Rear view
Fig. 33 Overall circuitdiagram
Fig. 34 Unit 1, component lay-out
Fig. 35 Power supply
Fig. 36 Unit 1, pulse generator
Fig. 37 Unit 1, PLL/VCO
Fig. 38 Unit 1, output amplifier
Fig. 39 Unit 1, modulator
Fig. 40 Unit 2, component lay-out
Fig. 41 Unit 2, CPU
Fig. 42 Unit 3, (keyboard/display) component lay-out
Fig. 43 Unit 3, keyboard display
Fig. 44 Unit 2, digital frequency synthesis (DFS)


rear
701
803
804
693
S11
S12
689
676
505
BURST
DFS
CPU
front
to U3

component side

Fig. 28
Unit 2
adjusting elements

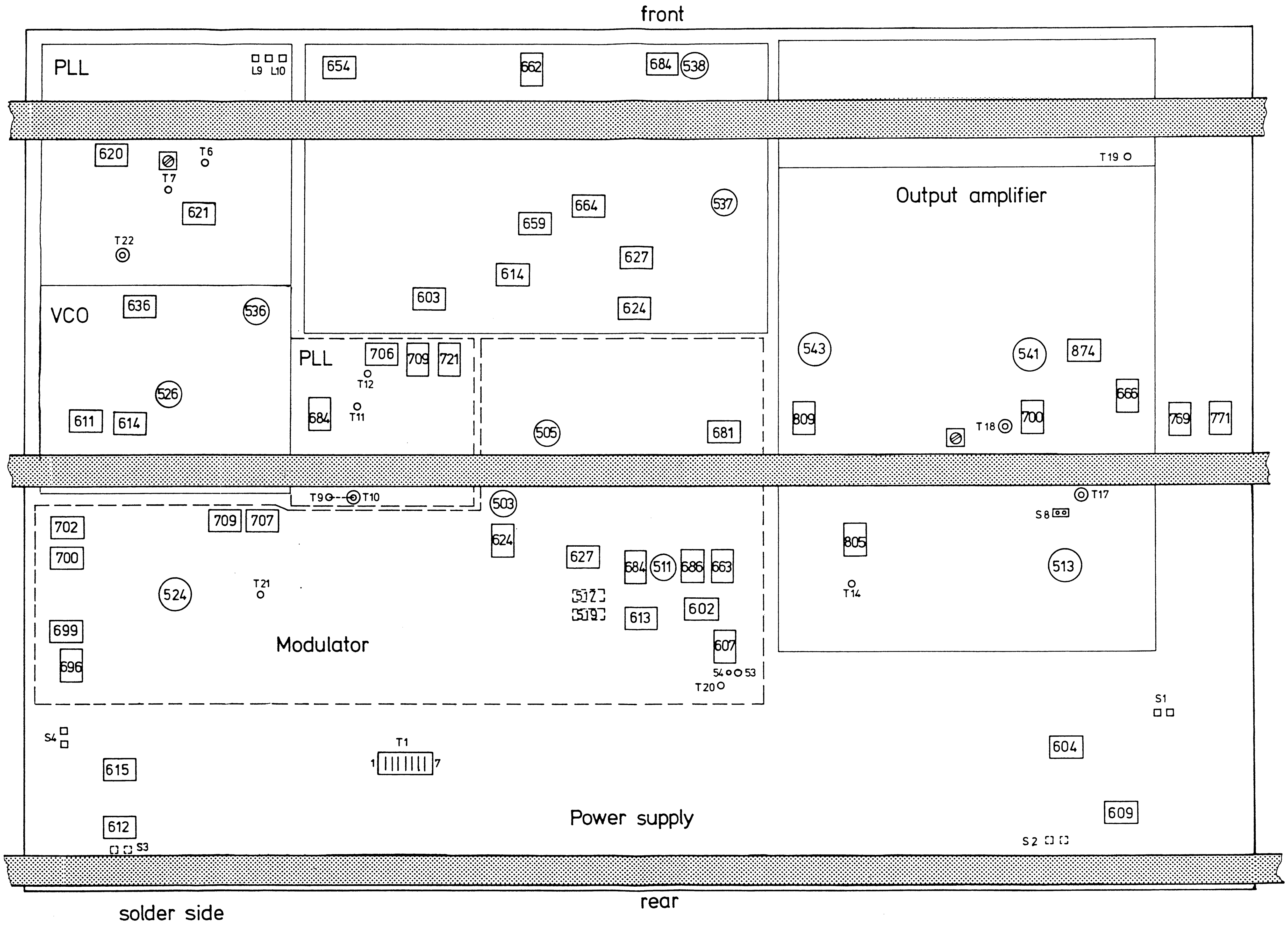

front
PLL
L9
L10
654
662
684
538
620
T6
T7
621
T22
664
659
537
627
614
603
624
Output amplifier
T19
VCO
636
536
526
611
614
PLL
706
709
721
T12
684
T11
505
681
543
541
874
665
809
T18
700
769
771
T9
T10
503
T17
S8
702
700
709
707
624
627
684
511
686
663
805
513
524
T21
517
519
613
602
T14
699
Modulator
696
607
54
53
T20
S1
S4
T1
1
7
615
604
612
Power supply
609
S3
S2
rear
solder side

Fig. 29
Unit 1
adjusting elements

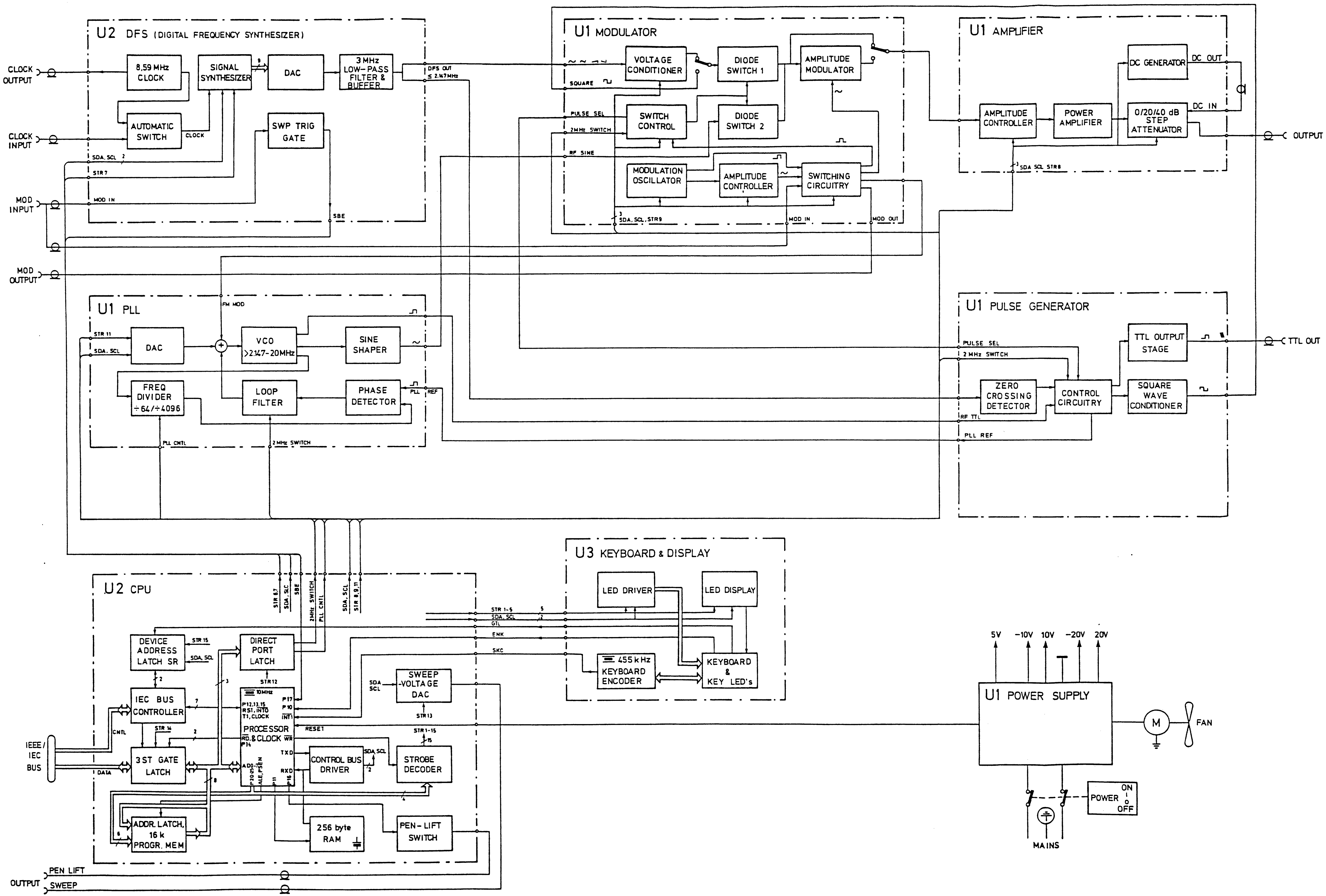

U2 DFS (DIGITAL FREQUENCY SYNTHESIZER)
CLOCK OUTPUT
CLOCK INPUT
MOD INPUT
MOD OUTPUT
8,59 MHz CLOCK
SIGNAL SYNTHESIZER
9
DAC
3 MHz LOW-PASS FILTER & BUFFER
DFS OUT ≤ 2.147MHz
AUTOMATIC SWITCH
CLOCK
SWP TRIG GATE
SDA, SCL 2
STR 7
MOD IN
SBE
U1 MODULATOR
VOLTAGE CONDITIONER
SQUARE
DIODE SWITCH 1
AMPLITUDE MODULATOR
PULSE SEL
2MHz SWITCH
SWITCH CONTROL
DIODE SWITCH 2
RF SINE
MODULATION OSCILLATOR
AMPLITUDE CONTROLLER
SWITCHING CIRCUITRY
3 SDA, SCL, STR9
MOD IN
MOD OUT
U1 AMPLIFIER
DC GENERATOR
DC OUT
DC IN
AMPLITUDE CONTROLLER
POWER AMPLIFIER
0/20/40 dB STEP ATTENUATOR
OUTPUT
3 SDA SCL STR8
U1 PLL
FM MOD
STR 11
SDA, SCL
DAC
VCO >2.147-20MHz
SINE SHAPER
FREQ DIVIDER ÷64/÷4096
LOOP FILTER
PHASE DETECTOR
PLL REF
PLL CNTL
2MHz SWITCH
U1 PULSE GENERATOR
PULSE SEL
2 MHz SWITCH
TTL OUTPUT STAGE
TTL OUT
ZERO CROSSING DETECTOR
CONTROL CIRCUITRY
SQUARE WAVE CONDITIONER
RF TTL
PLL REF
U3 KEYBOARD & DISPLAY
LED DRIVER
LED DISPLAY
455 kHz KEYBOARD ENCODER
KEYBOARD & KEY LED's
STR 1-5
5
SDA, SCL
2
GTL
ENK
SKC
U2 CPU
STR 8,7
SDA, SLC
SBE
2MHz SWITCH
PLL CNTL
SDA, SCL
STR 8,9,11
DEVICE ADDRESS LATCH SR
STR 15
SDA, SCL
DIRECT PORT LATCH
STR12
2
IEC BUS CONTROLLER
7
3
10MHz
P12,13,15 RS1, INT0 T1, CLOCK
P17
P10
INT1
PROCESSOR & CLOCK
RD, WR
P14
RESET
SWEEP -VOLTAGE DAC
SDA SCL
STR 13
IEEE/IEC BUS
CNTL
STR 14
2
3ST GATE LATCH
DATA
TXD
RXD
AD0-7
P20-23
ALE, PSEN
P11
P16
CONTROL BUS DRIVER
SDA SCL
2
STR 1-15
15
STROBE DECODER
4
8
6
ADDR. LATCH, 16 k PROGR. MEM
256 byte RAM
PEN-LIFT SWITCH
PEN LIFT
OUTPUT
SWEEP
5V
-10V
10V
-20V
20V
U1 POWER SUPPLY
M
FAN
POWER ON OFF
MAINS

Fig. 30 Block diagram

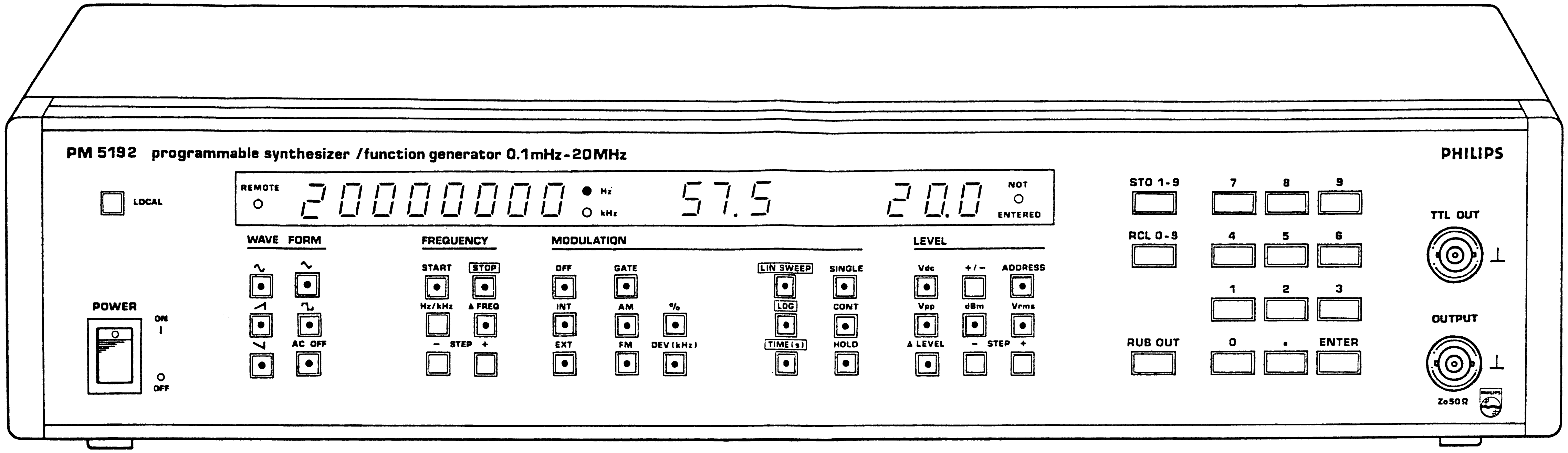

PM 5192 programmable synthesizer /function generator 0.1mHz-20MHz
PHILIPS
LOCAL
REMOTE
20000000
Hz
kHz
57.5
20.0
NOT
ENTERED
WAVE FORM
AC OFF
FREQUENCY
START
STOP
Hz/kHz
Δ FREQ
– STEP +
MODULATION
OFF
GATE
INT
AM
%
EXT
FM
DEV (kHz)
LIN SWEEP
SINGLE
LOG
CONT
TIME (s)
HOLD
LEVEL
Vdc
+/–
ADDRESS
Vpp
dBm
Vrms
Δ LEVEL
– STEP +
POWER
ON
I
O
OFF
STO 1-9
RCL 0-9
RUB OUT
7
8
9
4
5
6
1
2
3
0
.
ENTER
TTL OUT
OUTPUT
Zo50Ω

Fig. 31 Front view

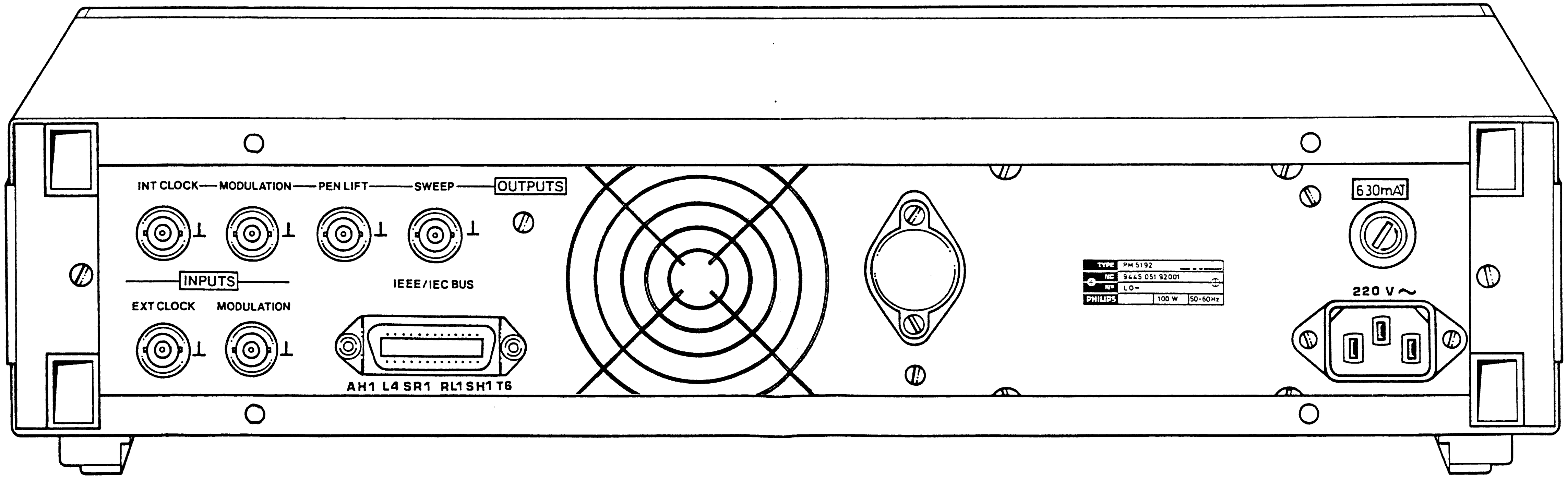
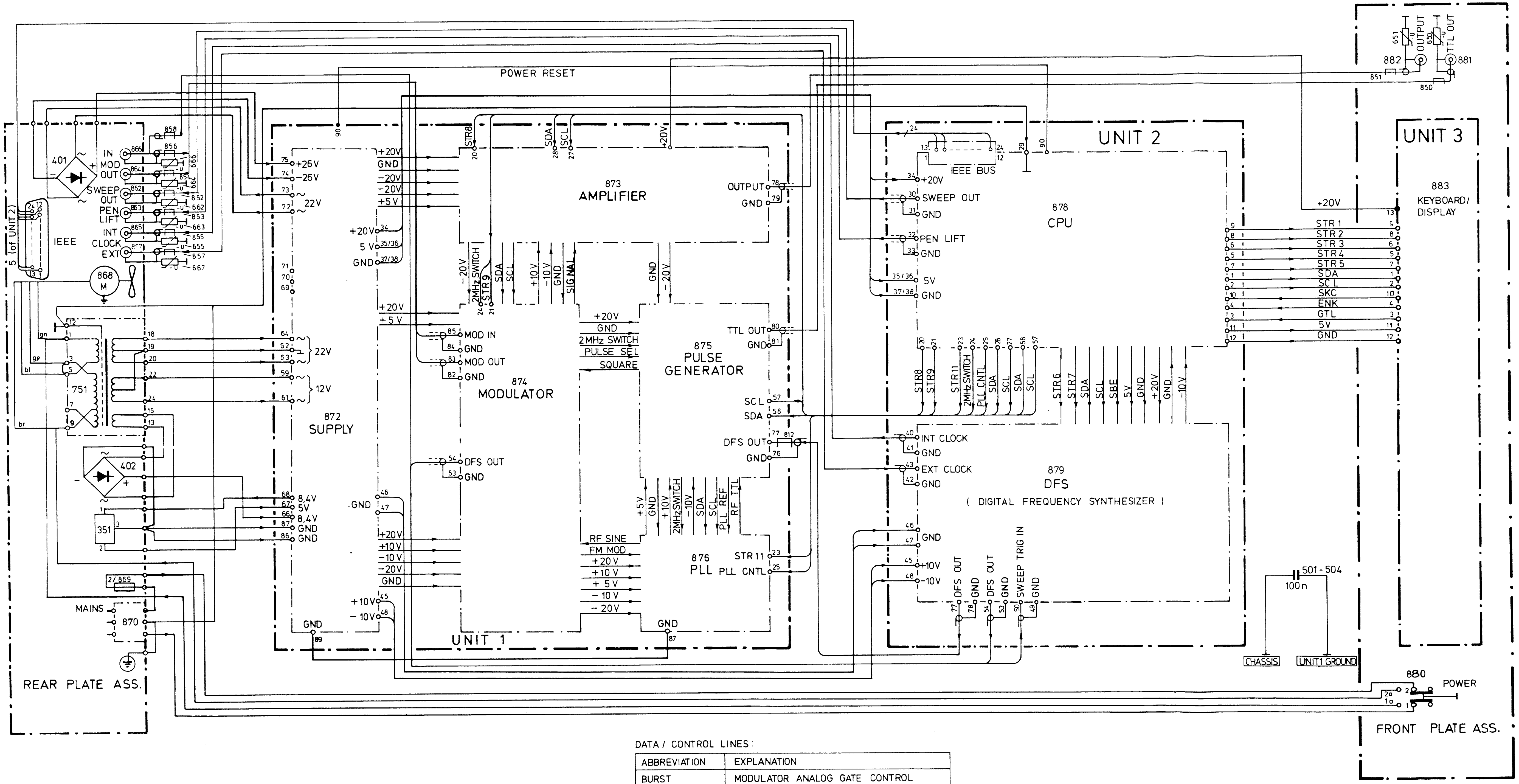

INT CLOCK
MODULATION
PEN LIFT
SWEEP
OUTPUTS
INPUTS
EXT CLOCK
MODULATION
IEEE/IEC BUS
AH1 L4 SR1 RL1 SH1 T6
630mAT
220 V ~
PM 5192
9445 051 92001
100 W
50-60Hz
PHILIPS

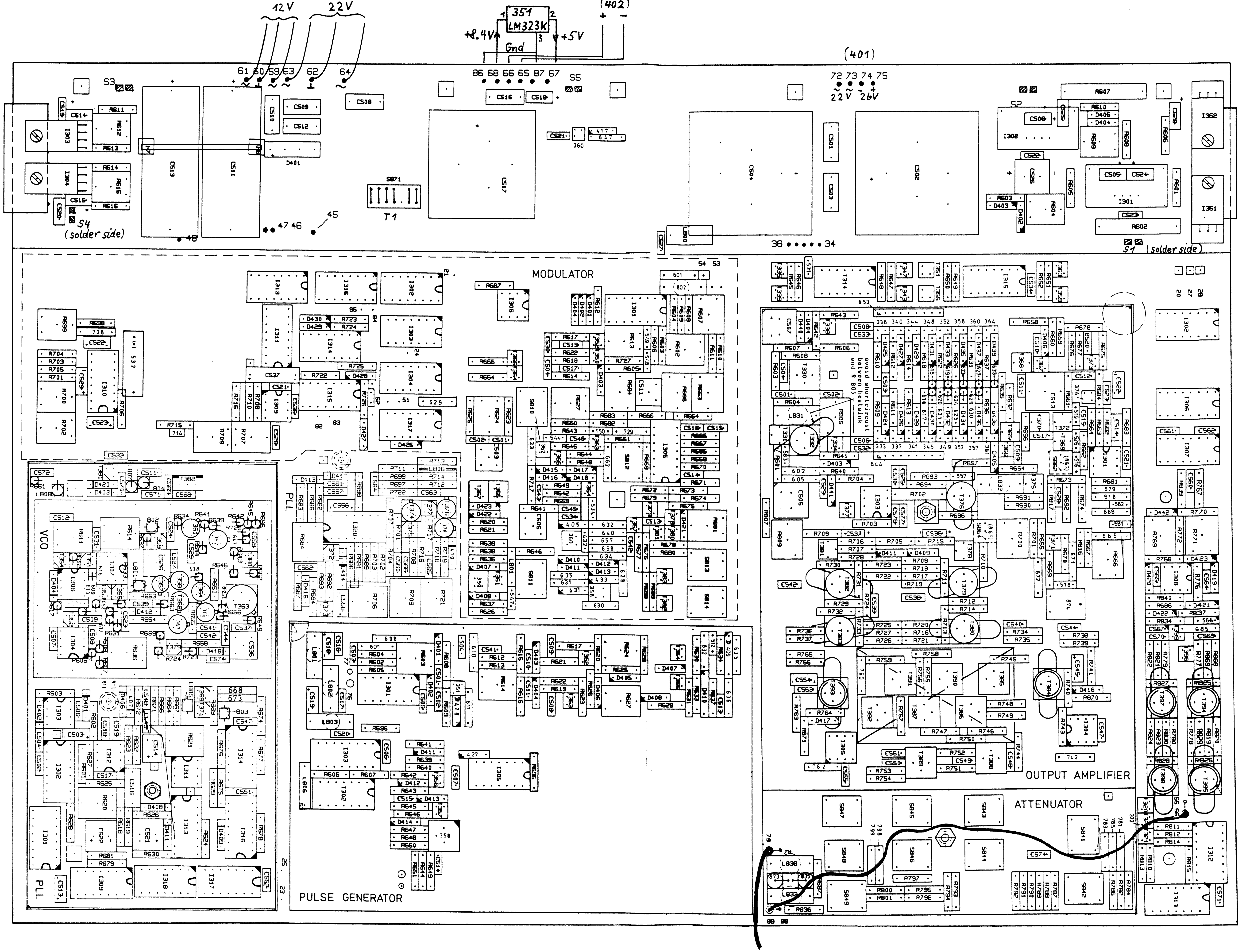
Fig. 32 Rear view


POWER RESET
UNIT 1
UNIT 2
UNIT 3
REAR PLATE ASS.
FRONT PLATE ASS.
401
402
751
351
868 M
870
2/869
MAINS
IEEE
5 (of UNIT 2)
IN MOD OUT
SWEEP OUT
PEN LIFT
INT CLOCK EXT
872 SUPPLY
+26V
-26V
22V
12V
8,4V
5V
GND
873 AMPLIFIER
OUTPUT
874 MODULATOR
MOD IN
MOD OUT
DFS OUT
875 PULSE GENERATOR
TTL OUT
SCL
SDA
876 PLL
STR 11
PLL CNTL
RF SINE
FM MOD
2MHz SWITCH
PULSE SEL
SQUARE
SIGNAL
PLL REF
RF TTL
878 CPU
IEEE BUS
SWEEP OUT
PEN LIFT
INT CLOCK
EXT CLOCK
879 DFS
(DIGITAL FREQUENCY SYNTHESIZER)
SWEEP TRIG IN
STR 1
STR 2
STR 3
STR 4
STR 5
SDA
SCL
SKC
ENK
GTL
5V
GND
+20V
883 KEYBOARD/ DISPLAY
882 OUTPUT
881 TTL OUT
651
650
851
850
880 POWER
501-504 100n
CHASSIS
UNIT1 GROUND

DATA / CONTROL LINES :

ABBREVIATION	EXPLANATION
BURST	MODULATOR ANALOG GATE CONTROL
2 MHz SWITCH	2.16 MHz SWITCH OVER
SDA	SERIAL DATA LINE
SCL	SERIAL CLOCK LINE
PULSE SELECT	SQUARE WAVE AND PULSE SELECT
STR	STROBE
PLL CNTL	PLL INTEGRATOR TIME CONSTANT CONTROL
SBE	SINGLE BURST END INDICATION
SKC	SERIAL KEYBOARD CODE
ENK	ENABLE KEYBOARD
GTL	GO TO LOCAL

Fig. 33 Overall circuitdiagram


12 V
22 V
351
LM323K
+8.4V
+5V
Gnd
(402)
+
−
(401)
61 60 59 63 62 64
86 68 66 65 87 67
72 73 74 75
22 V 26V
S3
S5
S2
S4 (solder side)
S1 (solder side)
T1
47 46
45
48
38 34
MODULATOR
VCO
PLL
PULSE GENERATOR
OUTPUT AMPLIFIER
ATTENUATOR
avoid shortcircuit between heatsink and R 805

Output signal to BNC socket BB2

Fig. 34 Unit 1, component lay-out

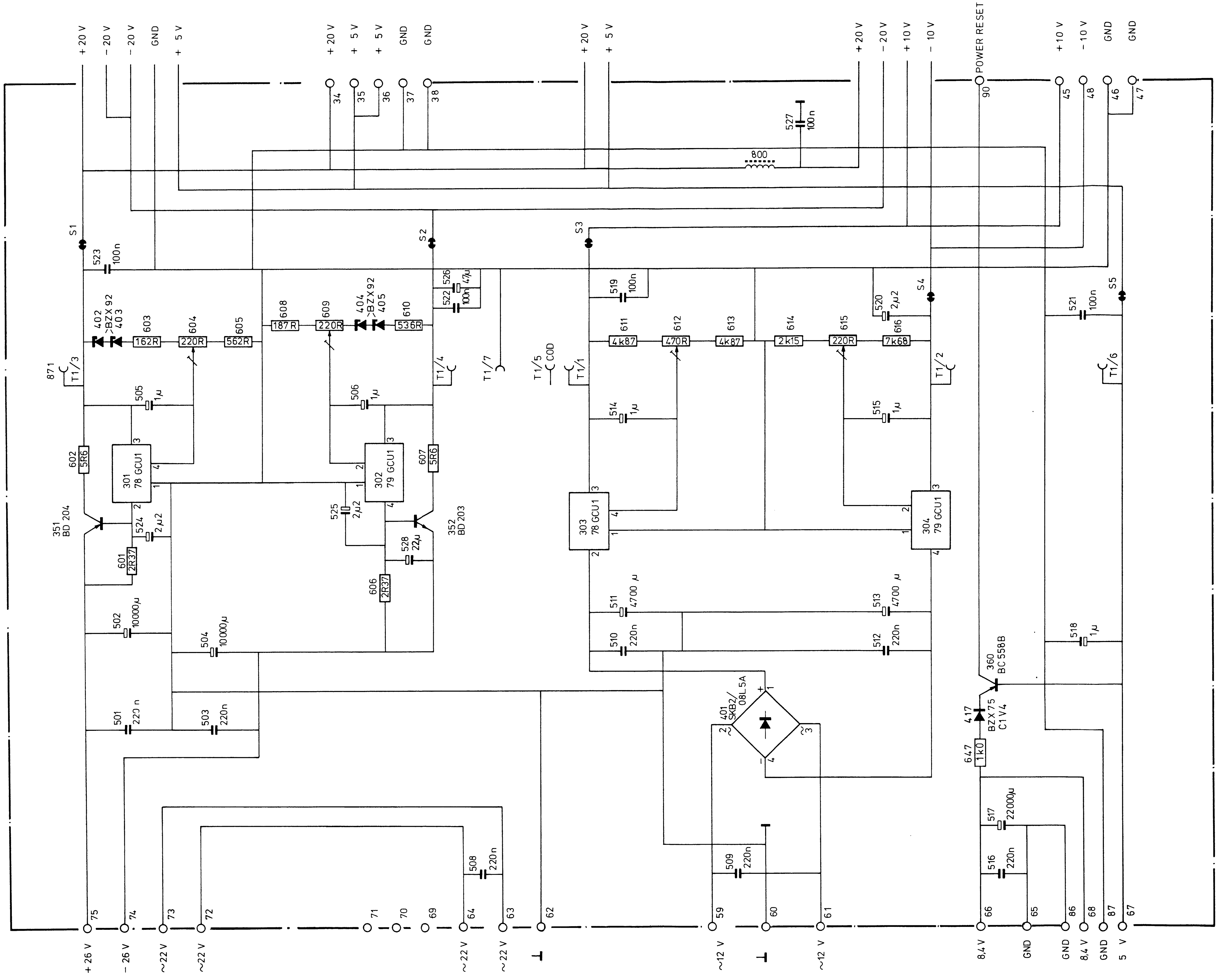

+20 V
-20 V
-20 V
GND
+5 V
+20 V
+5 V
+5 V
GND
GND
34
35
36
37
38
+20 V
+5 V
+20 V
-20 V
+10 V
-10 V
POWER RESET
90
+10 V
-10 V
GND
GND
45
48
46
47
527
100n
800
S1
S2
S3
S4
S5
523
100n
402
BZX92
403
603
162R
604
220R
605
562R
608
187R
609
220R
404
BZX92
405
610
536R
526
47µ
522
100n
519
100n
611
4k87
612
470R
613
4k87
614
2k15
615
220R
520
2µ2
616
7k68
521
100n
871
T1/3
T1/4
T1/7
T1/5
COD
T1/1
T1/2
T1/6
505
1µ
506
1µ
514
1µ
515
1µ
602
5R6
301
78 GCU1
302
79 GCU1
607
5R6
303
78 GCU1
304
79 GCU1
351
BD 204
352
BD 203
524
2µ2
525
2µ2
528
22µ
601
2R37
606
2R37
511
4700 µ
513
4700 µ
510
220n
512
220n
502
10000µ
504
10000µ
518
1µ
360
BC 558B
401
SKB2/
08L 5A
417
BZX 75
C1V4
647
1k0
501
222 n
503
220n
517
22000µ
516
220n
508
220n
509
220n
75
74
73
72
71
70
69
64
63
62
59
60
61
66
65
86
68
87
67
+26 V
-26 V
~22 V
~22 V
~22 V
~22 V
~12 V
~12 V
8,4 V
GND
GND
8,4 V
GND
5 V

Fig. 35 Unit 1, power supply

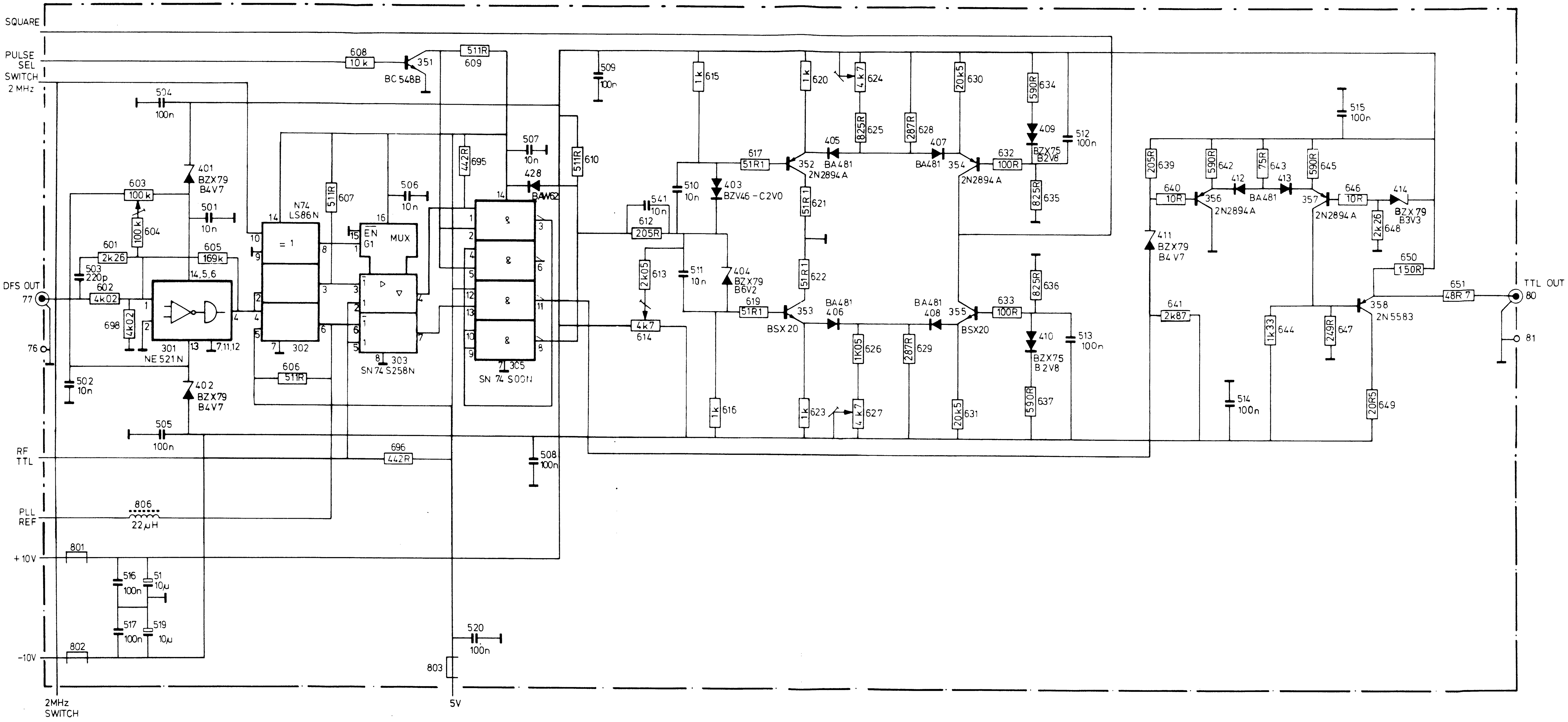
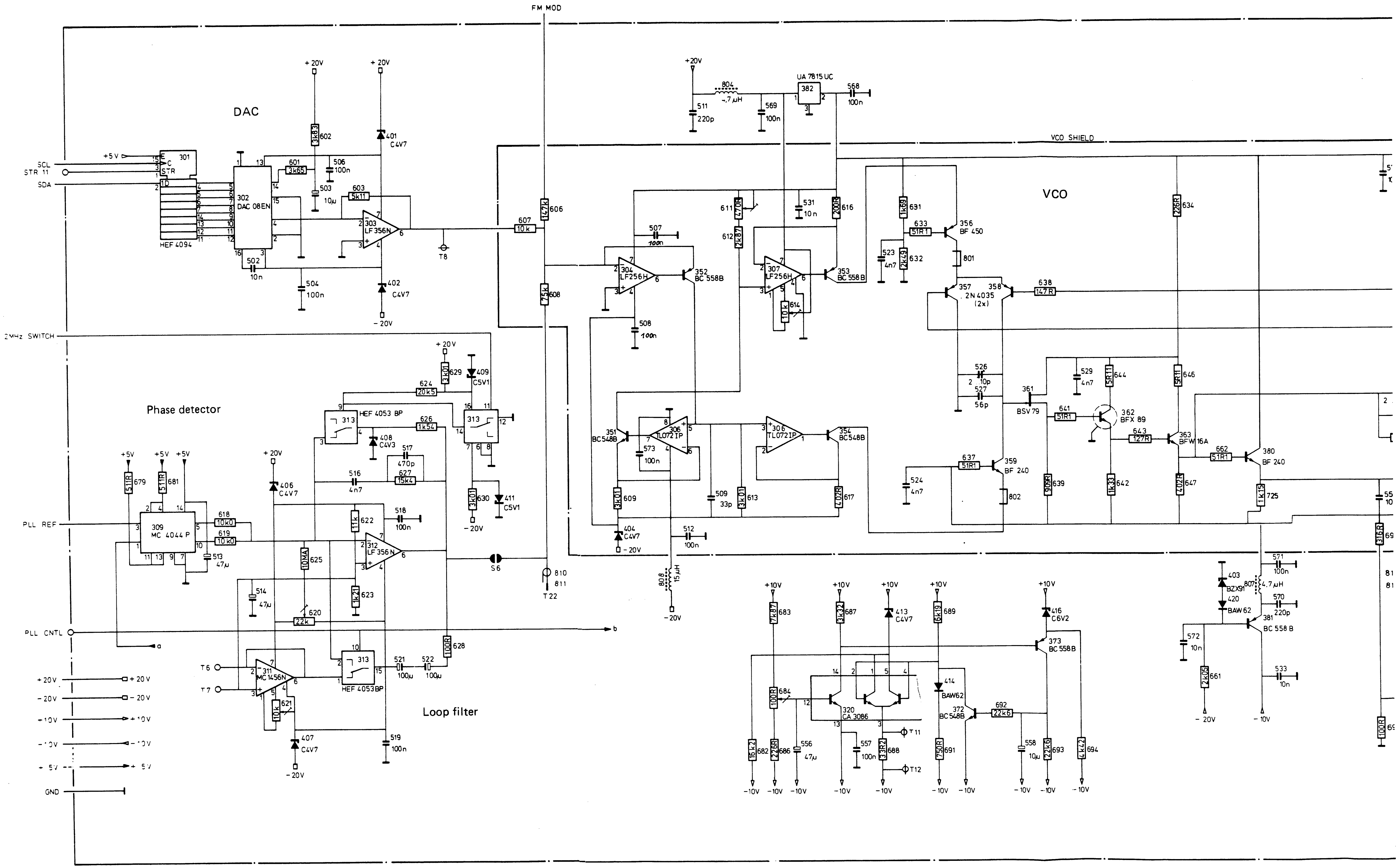
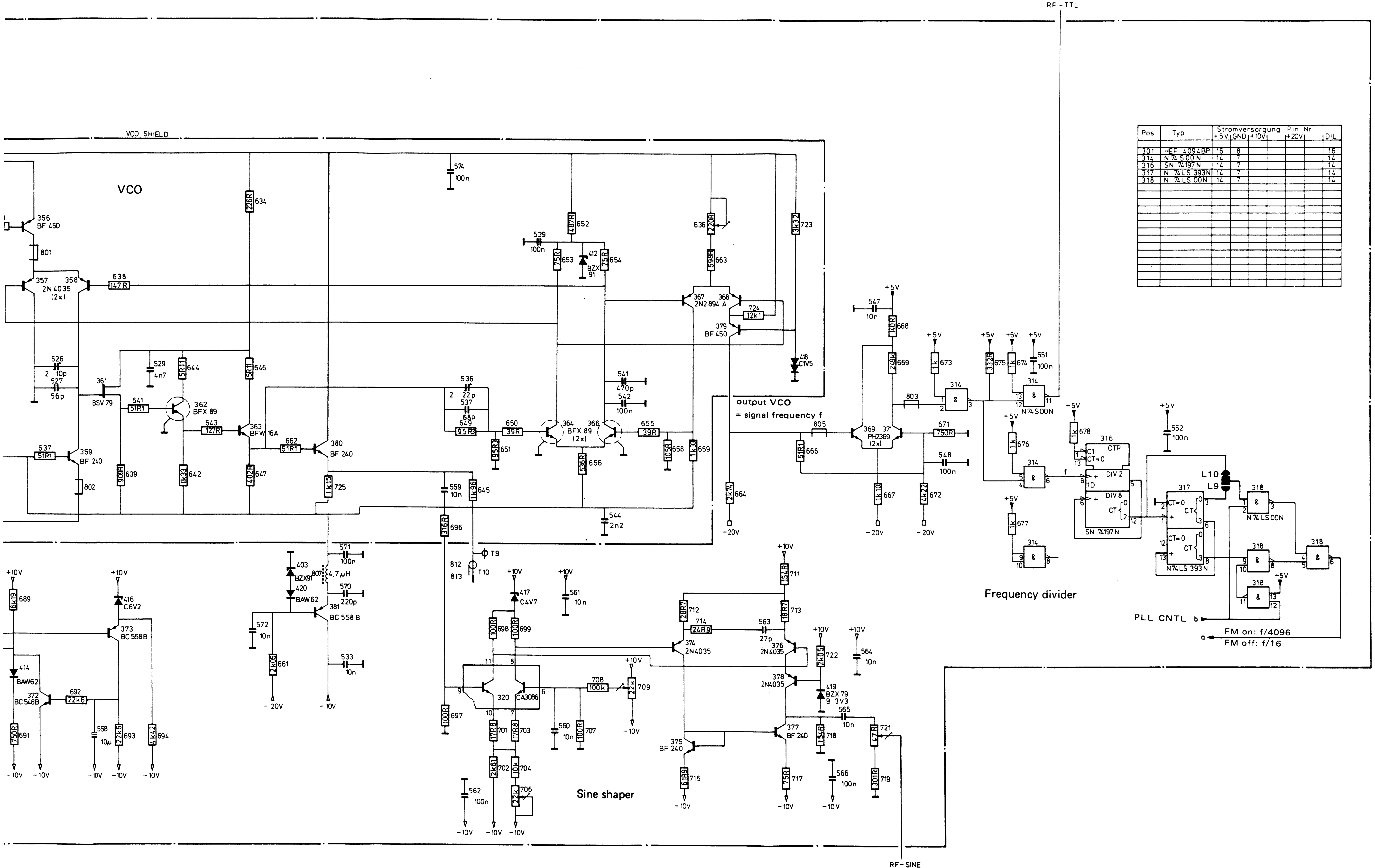

SQUARE
PULSE SEL
SWITCH 2 MHz
DFS OUT 77
76
RF TTL
PLL REF
+10V
-10V
2MHz SWITCH
5V
TTL OUT 80
81
608 10 k
351 BC 548B
511R 609
504 100n
401 BZX79 B4V7
603 100 k
604 100 k
501 10n
601 2k26
605 169k
503 220p
602 4k02
698 4k02
14,5,6
301 NE 521 N
13
7,11,12
402 BZX79 B4V7
502 10n
505 100n
N74 LS86N
= 1
302
606 511R
511R 607
506 10n
EN G1 MUX
303 SN 74 S258N
442R 695
507 10n
428 BAW62
&
305 SN 74 S00N
696 442R
806 22 µH
508 100n
801
802
516 100n
51 10µ
517 100n
519 10µ
520 100n
803
509 100n
511R 610
1k 615
541 10n
612 205R
510 10n
403 BZV46 - C2V0
2k05 613
511 10n
404 BZX79 B6V2
4k7 614
1k 616
617 51R1
352 2N2894 A
51R1 621
51R1 622
619 51R1
353 BSX 20
1k 620
4k7 624
825R 625
405 BA481
406 BA481
1K05 626
1k 623
4k7 627
287R 628
407 BA481
287R 629
408 BA481
20k5 630
354 2N2894 A
355 BSX20
20k5 631
632 100R
633 100R
590R 634
409 BZX75 B2V8
825R 635
512 100n
825R 636
410 BZX75 B2V8
590R 637
513 100n
205R 639
640 10R
356 2N2894A
411 BZX79 B4V7
641 2k87
590R 642
412 BA481
75R 643
413
590R 645
515 100n
357 2N2894 A
646 10R
414 BZX 79 B3V3
2k26 648
650 150R
651 48R 7
358 2N 5583
1k33 644
249R 647
514 100n
20R5 649

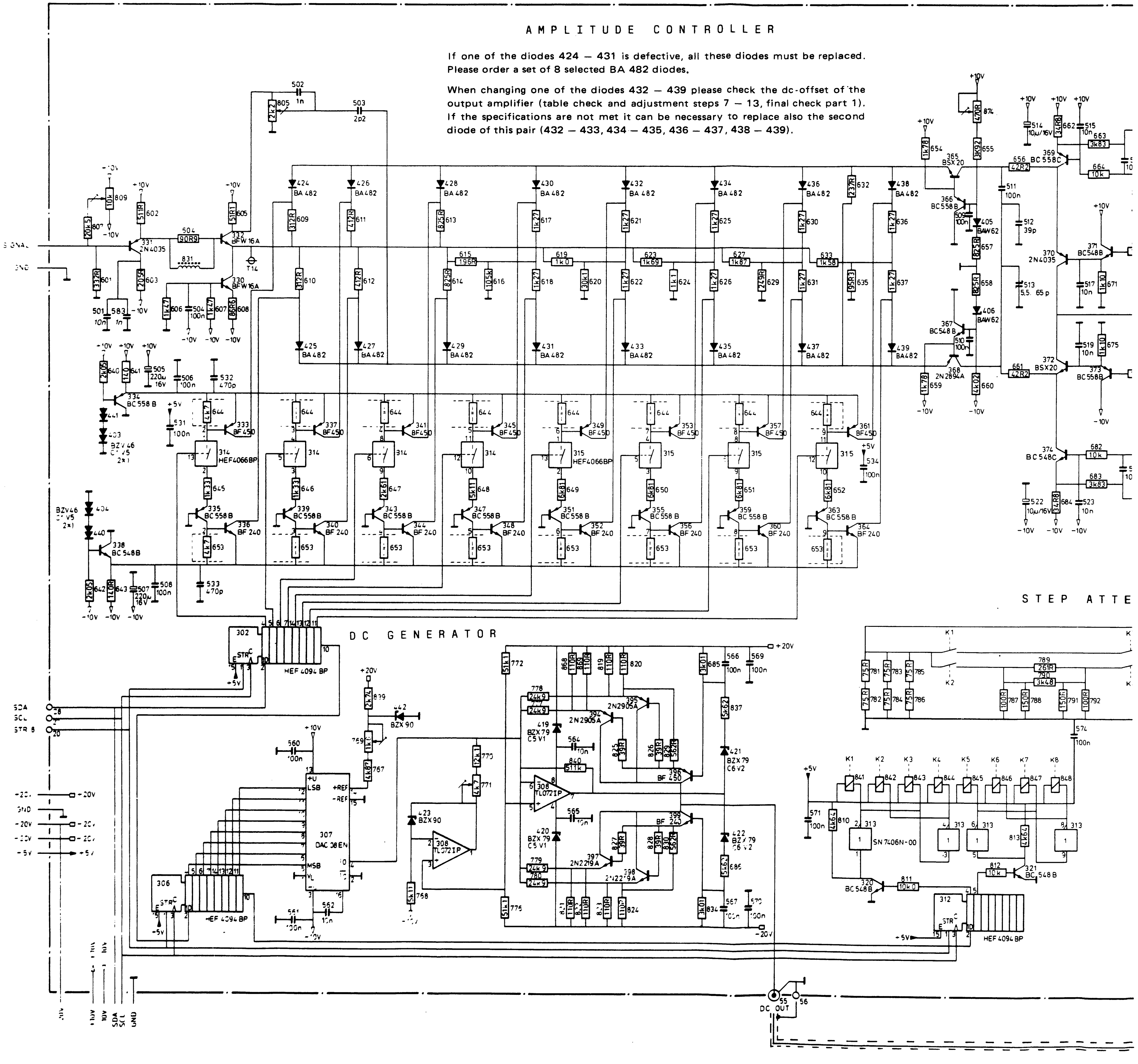
Fig. 36 Unit 1, pulse generator


FM MOD
DAC
VCO SHIELD
VCO
Phase detector
Loop filter
SCL
STR 11
SDA
2MHz SWITCH
PLL REF
PLL CNTL
+20V
-20V
+10V
-10V
+5V
GND
301 HEF 4094
302 DAC 08EN
303 LF 356N
304 LF256H
306 TL072IP
307 LF256H
309 MC 4044 P
311 MC1456N
312 LF 356N
313 HEF 4053 BP
320 CA 3086
351 BC548B
352 BC 558B
353 BC 558B
354 BC548B
356 BF 450
357 358 2N 4035 (2x)
359 BF 240
361 BSV 79
362 BFX 89
363 BFW 16A
372 BC548B
373 BC 558B
380 BF 240
381 BC 558 B
382 UA 7815 UC
401 C4V7
402 C4V7
403 BZX91
404 C4V7
406 C4V7
407 C4V7
408 C4V3
409 C5V1
411 C5V1
413 C4V7
414 BAW62
416 C6V2
420 BAW 62
804 4,7μH
807 4,7μH
808 15μH
T6
T7
T8
T11
T12
T22
S6
810
811
a
b


RF-TTL
VCO SHIELD
VCO
356 BF 450
801
357 358 2N4035 (2x)
638 147R
634 226R
574 100n
652 487R
539 100n
653 75R
412 BZX 91
654 75R
636 220R
663 698R
723 3k32
367 368 2N2894 A
724 12k1
379 BF 450
418 C1V5
526 2 . 10p
527 56p
529 4n7
644 5R11
646 5R11
361 BSV 79
641 51R1
362 BFX 89
643 127R
363 BFW 16A
662 51R1
380 BF 240
536 2 . 22p
537 68p
649 95R3
650 39R
651 95R3
364 366 BFX 89 (2x)
541 470p
542 100n
655 39R
658 105R
659 1k33
656 536R
637 51R1
359 BF 240
802
639 909R
642 1k33
647 402R
725 1k15
559 10n
645 1k96
544 2n2
664 2k74
-20V
output VCO
= signal frequency f
547 10n
+5V
668 140R
669 249k
803
805
666 51R1
369 371 PH2369 (2x)
671 750R
548 100n
667 1k10
672 4k22
673 1k
675 332R
674 1k
551 100n
314 &
N74S00N
676 1k
677 1k
678 1k
316 CTR C1 CT=0 DIV 2 1D DIV 8 CT
SN 74197N
552 100n
L10 L9
317 CT=0 CT
N74LS 393N
318 &
N74LS00N
Frequency divider
PLL CNTL
FM on: f/4096
FM off: f/16
696 316R
571 100n
403 BZX91
807 4,7μH
420 BAW62
570 220p
381 BC 558 B
572 10n
661 2k05
533 10n
- 20V
- 10V
812 813
T9 T10
+10V
689 6k19
416 C6V2
373 BC 558B
414 BAW62
372 BC548B
692 22k6
558 10μ
691 750R
693 22k6
694 4k42
-10V
417 C4V7
561 10n
698 100R
699 100R
320 CA3086
708 100k
709 22k
697 100R
701 17R8
703 17R8
702 2k61
704 10k
706 22k
560 10n
707 100R
562 100n
Sine shaper
711 154R
712 28R7
713 18R7
714 24R9
563 27p
374 2N4035
376 2N4035
378 2N4035
722 2k05
564 10n
419 BZX 79 B 3V3
565 10n
375 BF 240
377 BF 240
715 61R9
718 154R
717 75R
566 100n
721 47R
719 301R
RF-SINE

Pos	Typ	Stromversorgung Pin Nr +5V	GND	+10V		+20V		DIL
301	HEF 4094BP	16	8					16
314	N 74 S00 N	14	7					14
316	SN 74197 N	14	7					14
317	N 74LS 393N	14	7					14
318	N 74LS 00N	14	7					14

Fig. 37 Unit 1, PLL / VCO



AMPLITUDE CONTROLLER
DC GENERATOR
STEP ATTE
SIGNAL
GND
SDA
SCL
STR B
-20V
GND
+20V
-10V
+10V
+5V
331 2N4035
332 BFW 16A
330 BFW 16A
T14
334 BC 558 B
338 BC 548B
BZV46 C1V5 2x1
424 BA 482
426 BA 482
428 BA 482
430 BA 482
432 BA 482
434 BA 482
436 BA 482
438 BA 482
425 BA 482
427 BA 482
429 BA 482
431 BA 482
433 BA 482
435 BA 482
437 BA 482
439 BA 482
333 BF 450
337 BF 450
341 BF 450
345 BF 450
349 BF 450
353 BF 450
357 BF 450
361 BF 450
314 HEF 4066BP
315 HEF 4066BP
335 BC 558 B
339 BC 558 B
343 BC 558 B
347 BC 558 B
351 BC 558 B
355 BC 558 B
359 BC 558 B
363 BC 558 B
336 BF 240
340 BF 240
344 BF 240
348 BF 240
352 BF 240
356 BF 240
360 BF 240
364 BF 240
365 BSX 20
366 BC558B
367 BC548 B
368 2N2894A
369 BC 558C
370 2N4035
371 BC548B
372 BSX20
373 BC558B
374 BC 548C
405 BAW62
406 BAW62
302 HEF 4094 BP
306 HEF 4094 BP
307 DAC 08EN
LSB
MSB
+REF
-REF
308 TL072IP
419 BZX79 C5V1
420 BZX 79 C5 V1
421 BZX 79 C6V2
422 BZX 79 C6 V2
423 BZX90
442 BZX 90
394 2N2905A
395 2N2905A
396 BF 450
397 2N2219A
398 2N2219A
399 BF 240
55 DC OUT
56
K1
K2
K3
K4
K5
K6
K7
K8
313 SN 7406N-00
320 BC 548B
321 BC 548 B
312 HEF 4094 BP
STR

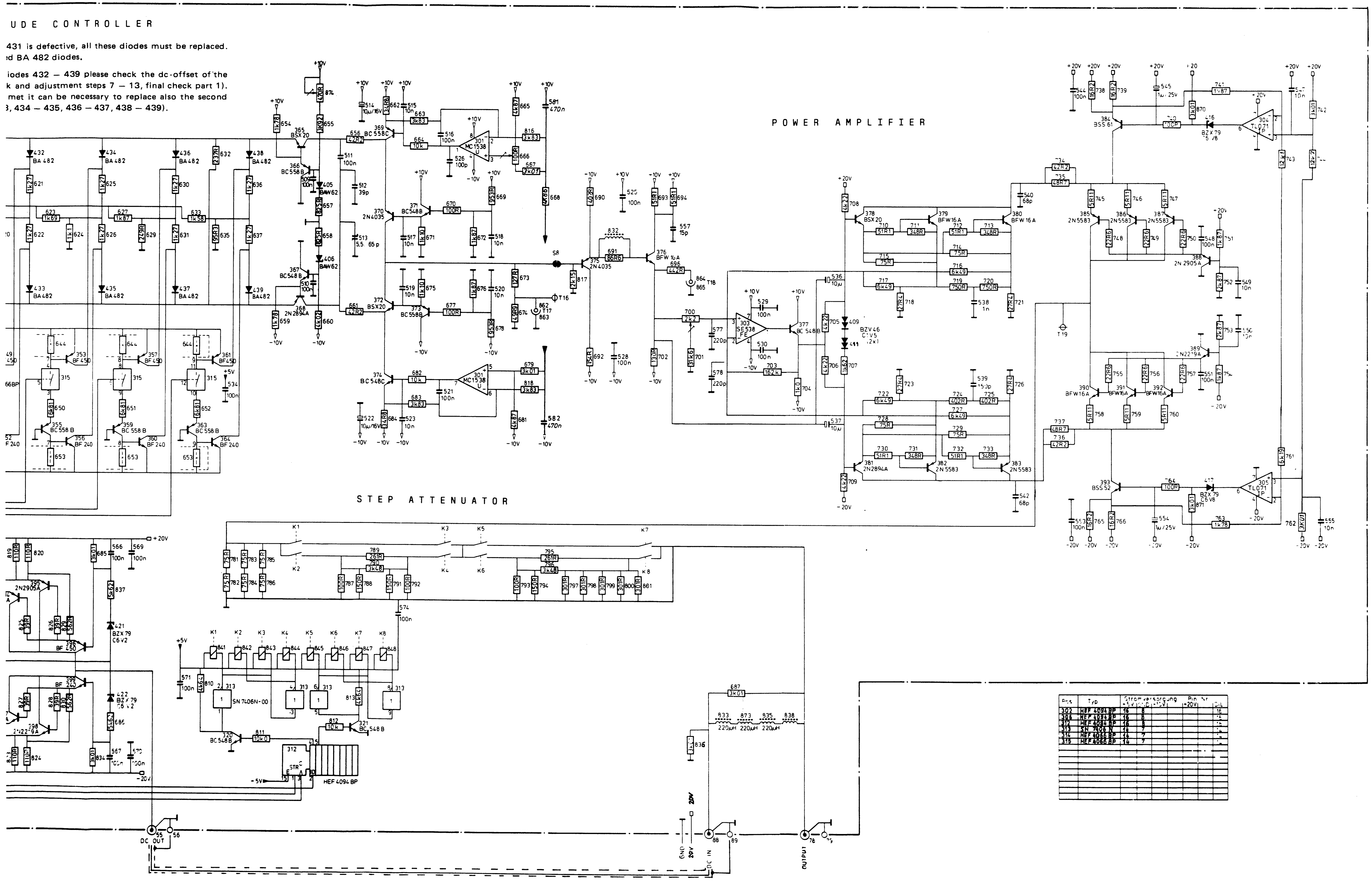
If one of the diodes 424 – 431 is defective, all these diodes must be replaced. Please order a set of 8 selected BA 482 diodes.

When changing one of the diodes 432 – 439 please check the dc-offset of the output amplifier (table check and adjustment steps 7 – 13, final check part 1). If the specifications are not met it can be necessary to replace also the second diode of this pair (432 – 433, 434 – 435, 436 – 437, 438 – 439).

UDE CONTROLLER

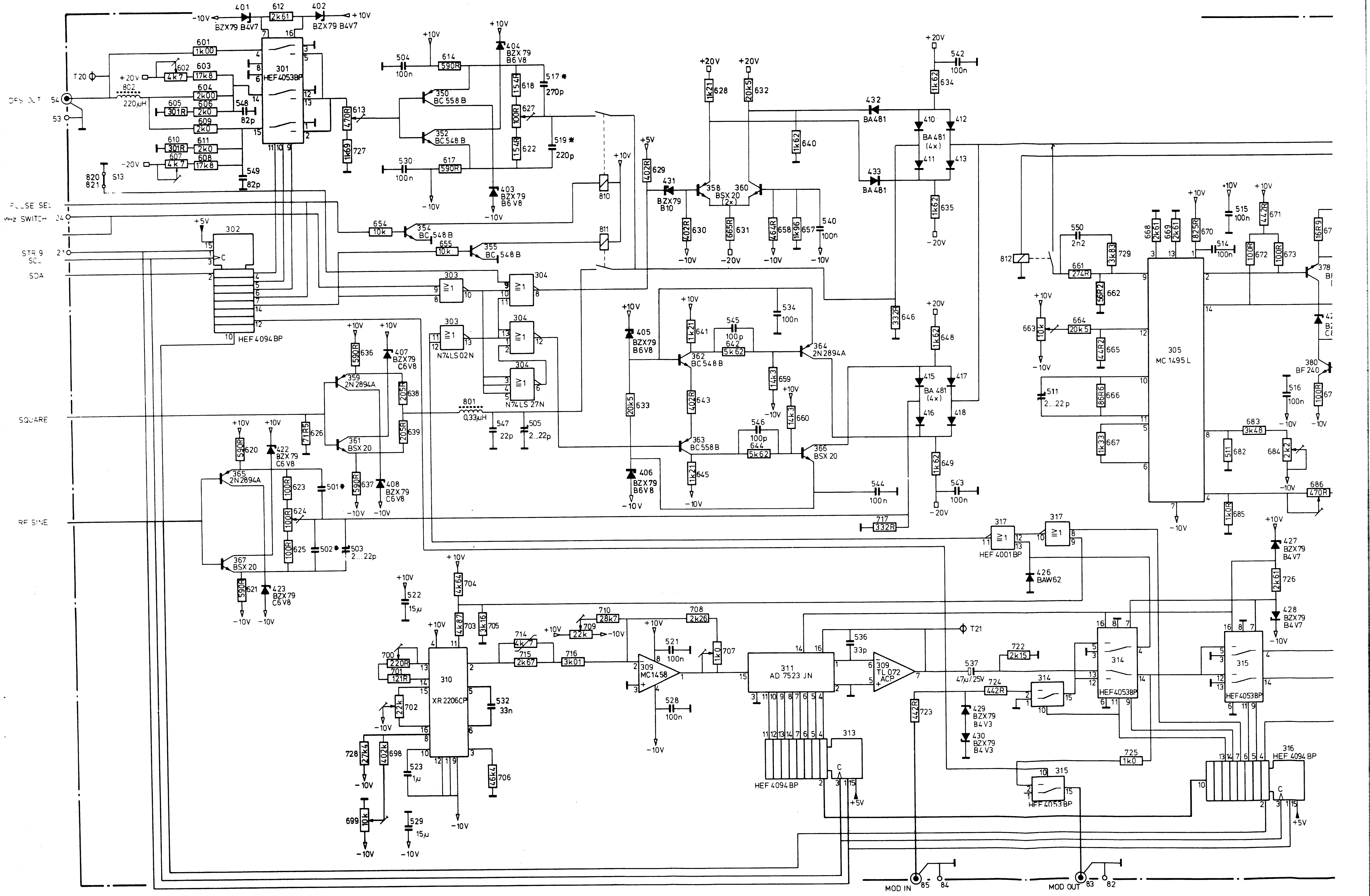
431 is defective, all these diodes must be replaced.
ed BA 482 diodes.

iodes 432 – 439 please check the dc-offset of the
k and adjustment steps 7 – 13, final check part 1).
met it can be necessary to replace also the second
3, 434 – 435, 436 – 437, 438 – 439).

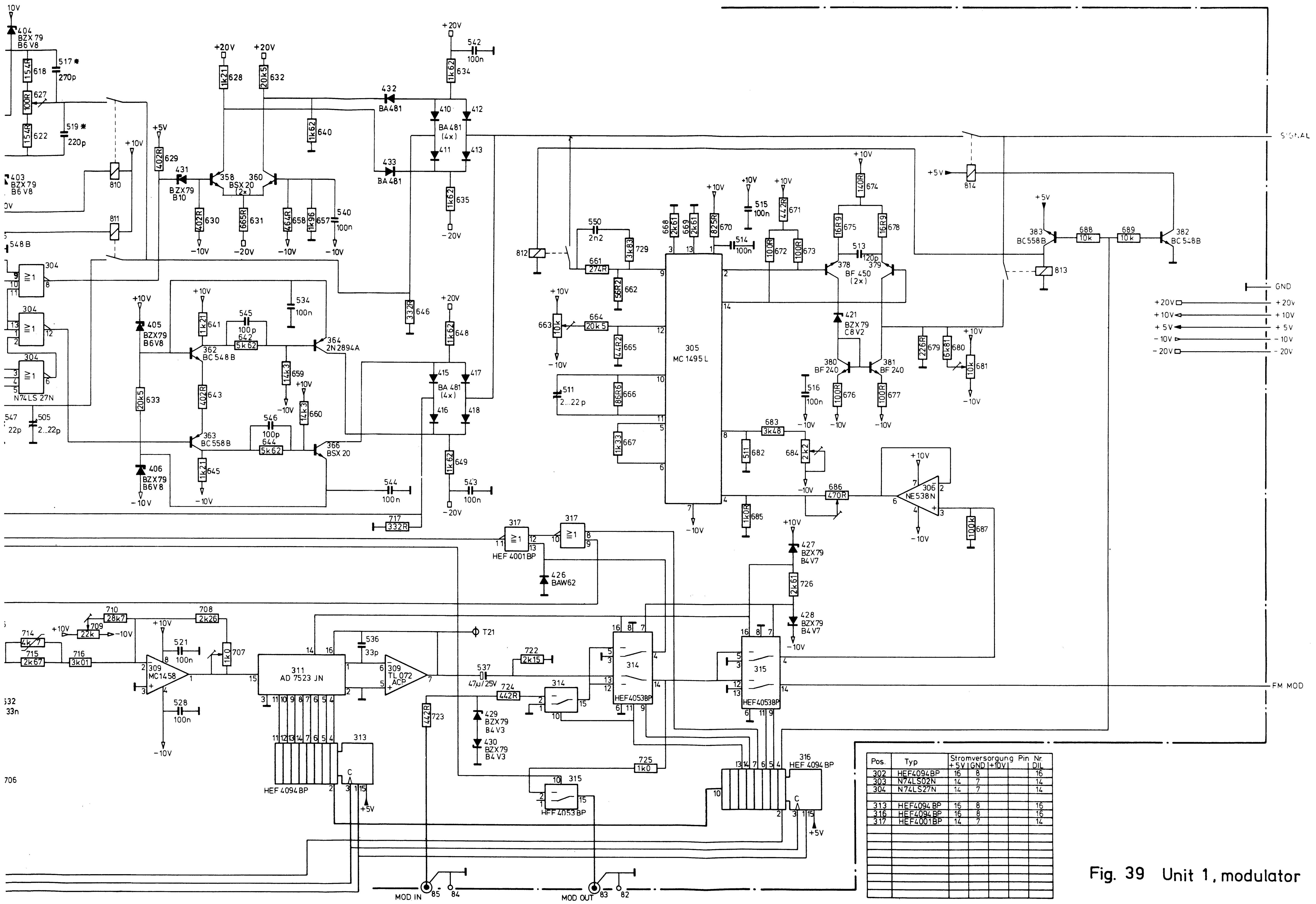

POWER AMPLIFIER
STEP ATTENUATOR
MC1538
SE538
TL071
SN 7406N-00
HEF 4094 BP
BA 482
BAW62
BSX20
BC558B
BC548B
2N4035
BFW16A
2N5583
2N2894A
2N2905A
BSS61
BSS52
BZX79 C6V8
BZV46 C1V5
BF450
BF240
K1
K2
K3
K4
K5
K6
K7
K8
S8
T16
T17
T18
T19
+5V
+10V
-10V
+20V
-20V
-5V
220µH
DC OUT
GND
20V
DC IN
OUTPUT
55
56
88
89
78
79

Pos	Typ	Stromversorgung Pin Nr +5V	GND	+10V		+20V		DIL
302	HEF 4094 BP	16	8					16
306	HEF 4094 BP	16	8					16
312	HEF 4094 BP	16	8					16
313	SN 7406 N	14	7					14
314	HEF 4066 BP	14	7					14
315	HEF 4066 BP	14	7					14

Fig. 38 Unit 1, output amplifier



DFS OUT 54
53
T20
PULSE SEL
MHz SWITCH 24
STR 9
SCL 21
SDA
SQUARE
RF SINE
820
821
S13
301 HEF4053BP
302
HEF 4094 BP
303
304
N74LS 02 N
N74LS 27 N
305 MC 1495 L
309 MC1458
309 TL 072 ACP
310 XR 2206CP
311 AD 7523 JN
313 HEF 4094 BP
314 HEF4053BP
315 HEF4053BP
316 HEF 4094 BP
317 HEF 4001 BP
350 BC 558 B
352 BC 548 B
354 BC 548 B
355 BC 548 B
358 360 BSX 20 (2x)
359 2N 2894A
361 BSX 20
362 BC 548 B
363 BC 558 B
364 2N 2894A
365 2N 2894A
366 BSX 20
367 BSX 20
378
380 BF 240
401 BZX79 B4V7
402 BZX79 B4V7
403 BZX79 B6V8
404 BZX 79 B6 V8
405 BZX79 B6V8
406 BZX79 B6V8
407 BZX79 C6V8
408 BZX 79 C6 V8
410 411 412 413 BA 481 (4x)
415 416 417 418 BA 481 (4x)
422 BZX 79 C6 V8
423 BZX79 C6 V8
426 BAW62
427 BZX79 B4 V7
428 BZX79 B4 V7
429 BZX79 B4 V3
430 BZX79 B4 V3
431 BZX79 B10
432 BA481
433 BA481
801 0,33μH
802 220μH
810
811
812
T21
MOD IN 85 84
MOD OUT 83 82
+10V
-10V
+20V
-20V
+5V


SIGNAL
GND
+20V
+10V
+5V
−10V
−20V
FM MOD
MOD IN
85
84
MOD OUT
83
82
T21
305 MC 1495 L
306 NE538N
309 MC1458
309 TL 072 ACP
311 AD 7523 JN
313 HEF 4094 BP
314 HEF4053BP
315 HEF4053BP
316 HEF 4094 BP
317 HEF 4001 BP
304 N74LS 27N
358 360 BSX 20 (2×)
362 BC 548 B
363 BC 558 B
364 2N 2894A
366 BSX 20
378 379 BF 450 (2×)
380 BF 240
381 BF 240
382 BC 548B
383 BC 558B
410 412 411 413 BA 481 (4×)
415 417 416 418 BA 481 (4×)
404 BZX 79 B6V8
405 BZX79 B6V8
406 BZX79 B6V8
421 BZX 79 C8V2
426 BAW62
427 BZX79 B4V7
428 BZX79 B4V7
429 BZX79 B4V3
430 BZX79 B4V3
431 BZX79 B10
432 BA481
433 BA481
810
811
812
813
814

Pos.	Typ	Stromversorgung Pin Nr. +5V	GND	+10V		DIL
302	HEF4094BP	16	8			16
303	N74LS02N	14	7			14
304	N74LS27N	14	7			14
313	HEF4094 BP	16	8			16
316	HEF4094 BP	16	8			16
317	HEF4001BP	14	7			14

Fig. 39 Unit 1, modulator

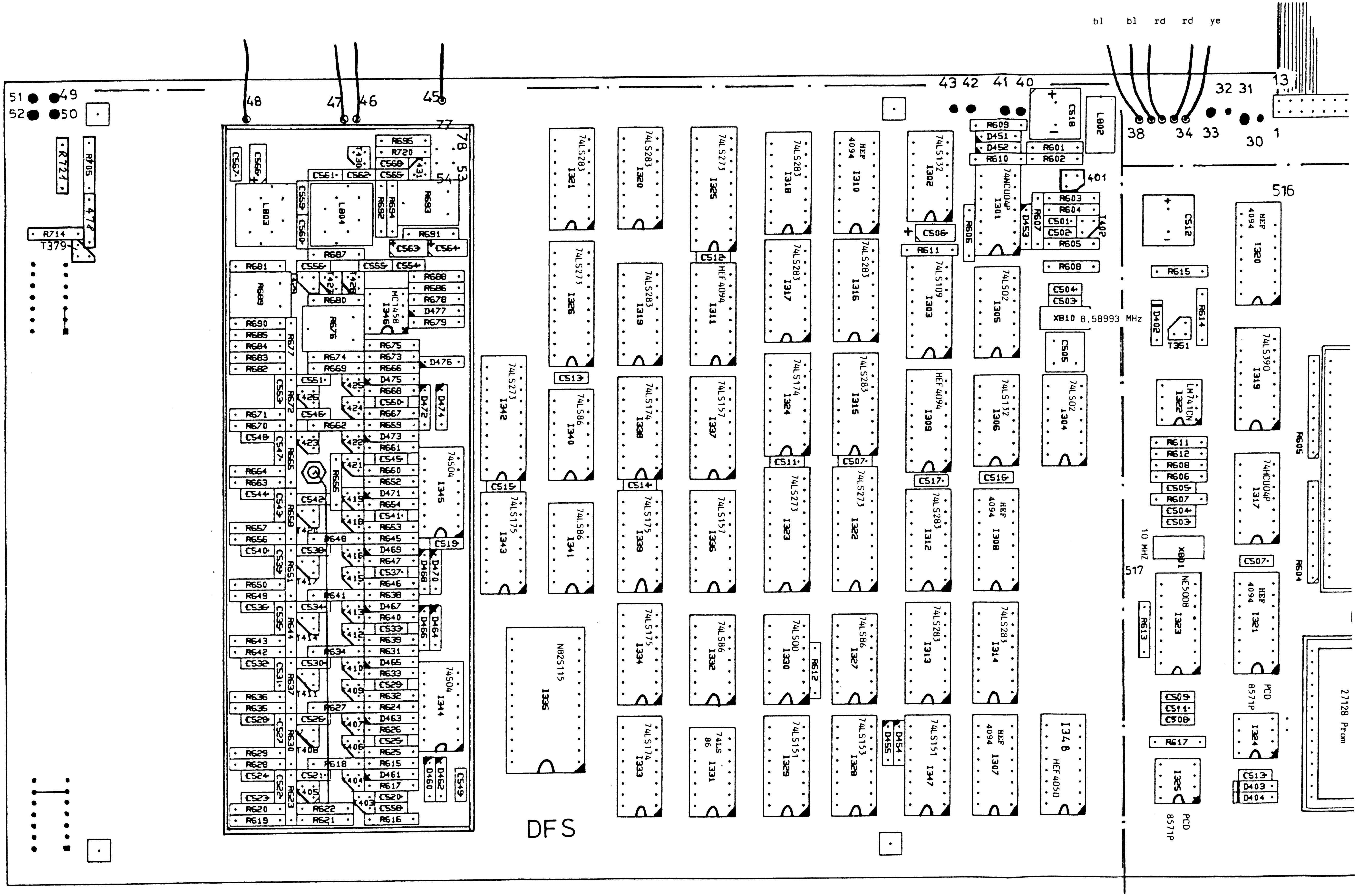

bl
bl
rd
rd
ye
51
49
52
50
48
47
46
45
43
42
41
40
38
34
33
32
31
30
13
1
77
78
53
54
401
516
517
DFS
8.58993 MHz
X810
10 MHz
X801
27128 Prom
PCD 8571P
N82S115
I336
HEF 4094
74LS283
74LS273
74LS174
74LS175
74LS157
74LS86
74LS132
74LS109
74LS02
74LS00
74LS151
74LS153
74LS390
74HCU04P
74S04
MC1458
LM741CN
NE5008
HEF4050
L801
L802
L803
L804
R721
R705
R714
T379
T351

Fig.

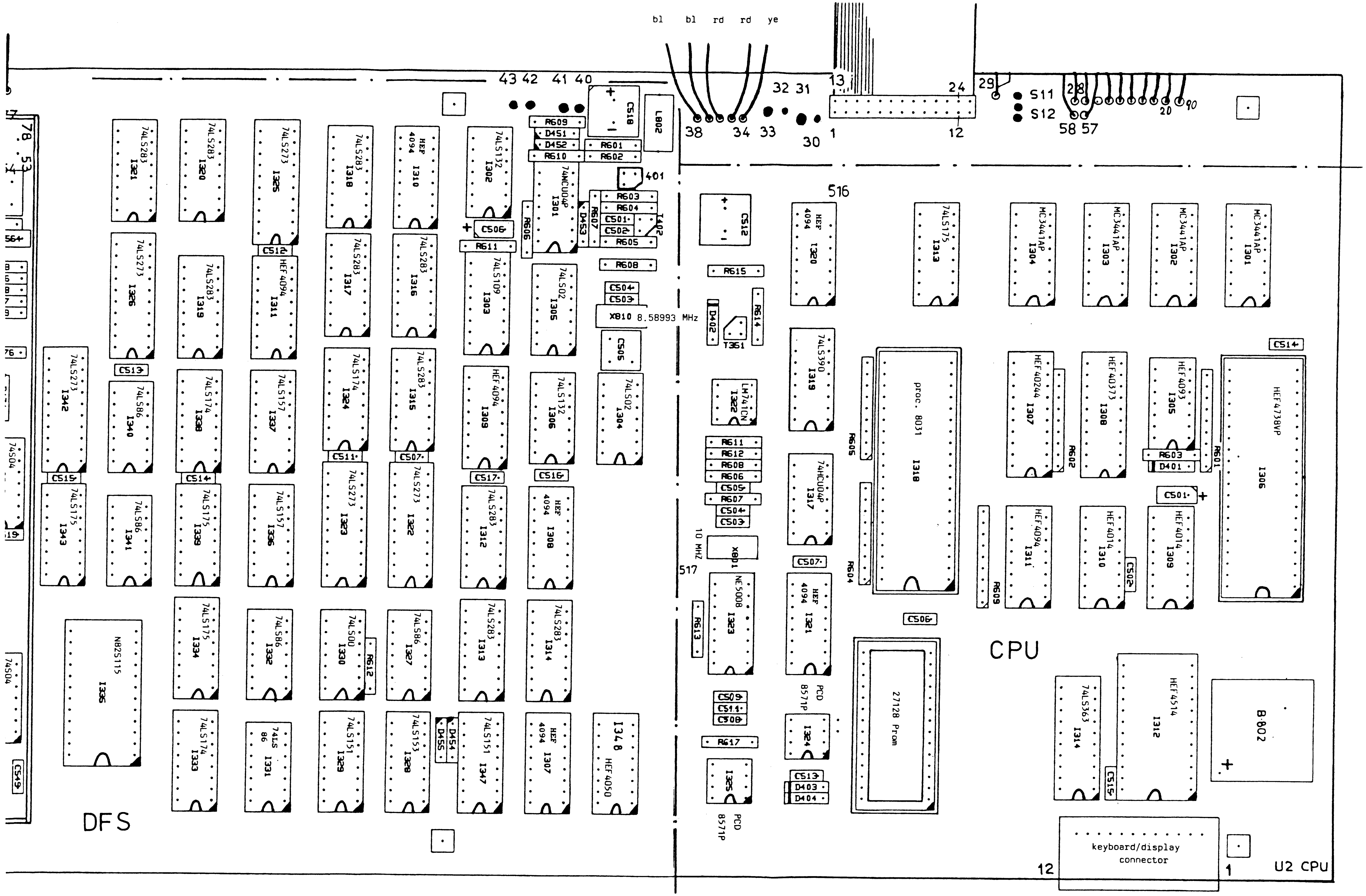
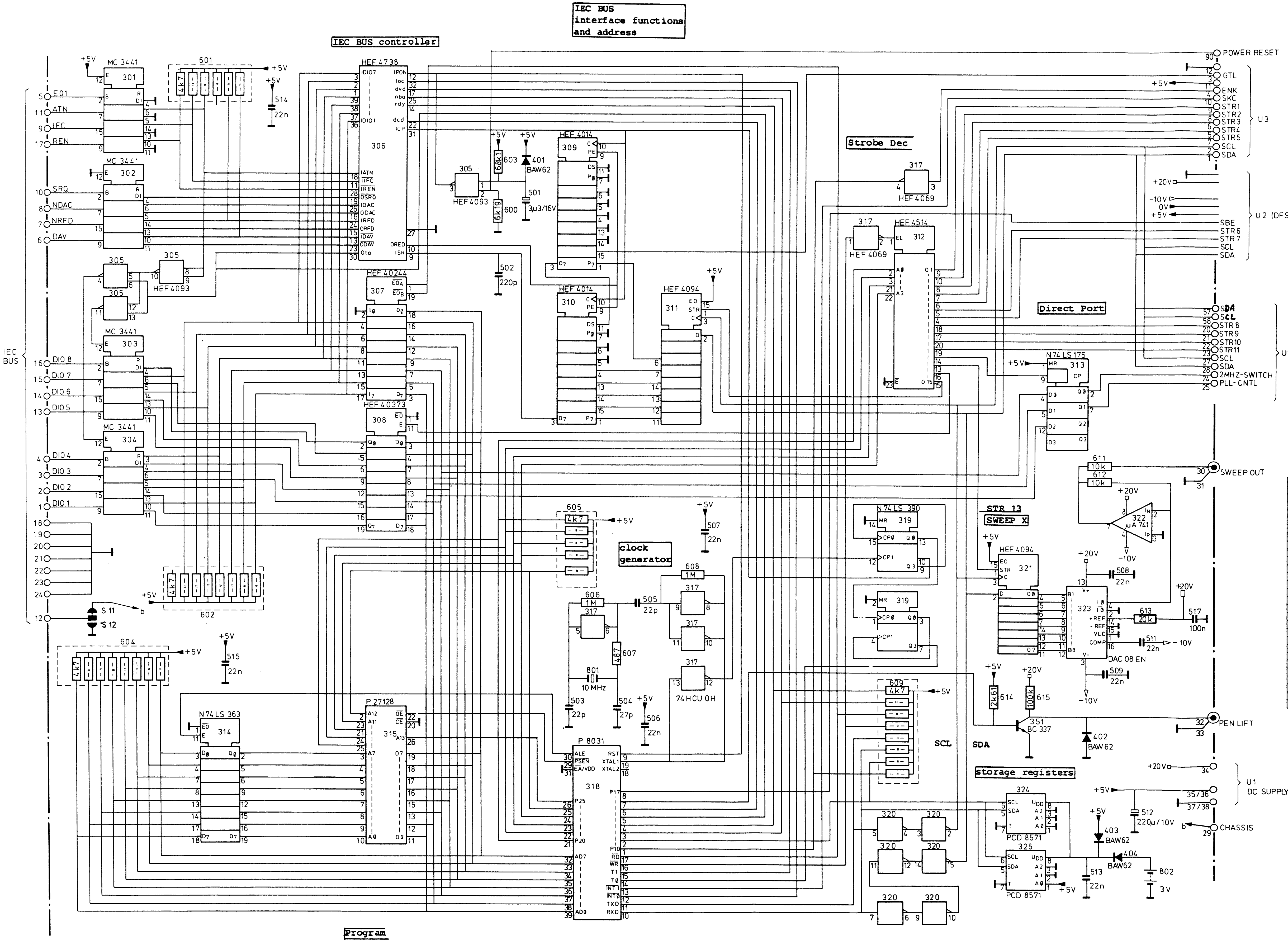

bl
bl
rd
rd
ye
43 42 41 40
32 31
13
24
29
S11
S12
28
20
90
38
34 33
30
1
12
58 57
C518
L802
R609
D451
D452
R610
R601
R602
401
R603
R604
R607
D453
C501
C502
R605
T402
R606
C506
R611
R608
C504
C503
X810 8.58993 MHz
C505
D402
R614
T351
R615
C512
516
74LS283 I321
74LS283 I320
74LS273 I325
74LS283 I318
HEF 4094 I310
74LS132 I302
74HCU04P I301
74LS273 I326
74LS283 I319
C512
HEF4094 I311
74LS283 I317
74LS283 I316
74LS109 I303
74LS02 I305
C513
74LS273 I342
74LS86 I340
74LS174 I338
74LS157 I337
74LS174 I324
74LS283 I315
HEF4094 I309
74LS132 I306
74LS02 I304
C515
C514
C511
C507
C517
C516
74LS175 I343
74LS86 I341
74LS175 I339
74LS157 I336
74LS273 I323
74LS273 I322
74LS283 I312
HEF 4094 I308
N82S115 I335
74LS175 I334
74LS86 I332
74LS00 I330
R612
74LS86 I327
74LS283 I313
74LS283 I314
74LS174 I333
74LS 86 I331
74LS151 I329
74LS153 I328
D454
D455
74LS151 I347
HEF 4094 I307
I348 HEF4050
DFS
78
53
564
76
74S04
519
74S04
C549
LM741CN I322
R611
R612
R608
R606
C505
R607
C504
C503
10 MHz
X801
517
NE5008 I323
R613
C509
C511
C508
R617
I325
PCD 8571P
HEF 4094 I320
74LS175 I313
MC3441AP I304
MC3441AP I303
MC3441AP I302
MC3441AP I301
C514
74LS390 I319
proc. 8031 I318
R605
HEF40244 I307
R602
HEF40373 I308
HEF4093 I305
R603
D401
R601
HEF4738VP I306
C501
74HCU04P I317
R604
C507
HEF 4094 I321
PCD 8571P
I324
C513
D403
D404
C506
27128 Prom
HEF4094 I311
R609
HEF4014 I310
C502
HEF4014 I309
CPU
74LS363 I314
C515
HEF4514 I312
B802
keyboard/display connector
12
1
U2 CPU

Fig. 40 Unit 2, component lay-out


IEC BUS controller
IEC BUS interface functions and address
Strobe Dec
Direct Port
clock generator
STR 13
SWEEP X
storage registers
Program
IEC BUS
MC 3441
HEF 4738
HEF 40244
HEF 40373
HEF 4093
HEF 4014
HEF 4094
HEF 4069
HEF 4514
N74LS 175
N74LS 390
N74LS 363
P 27128
P 8031
74HCU 04
µA 741
DAC 08 EN
PCD 8571
BC 337
BAW62
10 MHz
POWER RESET
GTL
ENK
SKC
STR1
STR2
STR3
STR4
STR5
SCL
SDA
U3
U2 (DFS)
SBE
STR6
STR7
STR8
STR9
STR10
STR11
2MHZ-SWITCH
PLL-CNTL
U1
SWEEP OUT
PEN LIFT
U1 DC SUPPLY
CHASSIS
E01
ATN
IFC
REN
SRQ
NDAC
NRFD
DAV
DIO 8
DIO 7
DIO 6
DIO 5
DIO 4
DIO 3
DIO 2
DIO 1
S 11
S 12

Pos.	Typ	Stromversorg +5V	PIN GND	DIL
301	MC 3441	16	8	16
302	" "	16	8	16
303	" "	16	8	16
304	" "	16	8	16
305	HEF 4093	14	7	14
306	" 4738	40	20	40
307	" 40244	20	10	20
308	" 40373	20	10	20
309	" 4014	16	8	16
310	" "	16	8	16
311	" 4094	16	8	16
312	" 4514	24	12	24
313	N74LS 175	16	8	16
314	N74LS 363	20	10	20
315	P 27128	28	14	28
317	74HCU04	14	7	14
318	P 8031	40	20	40
319	N74LS 390	16	8	16
320	HEF 4049	1	8	16
321	" 4094	16	8	16
324	PCD 8571		4	8
325	" "		4	8

Fig. 41 Unit 2, CPU

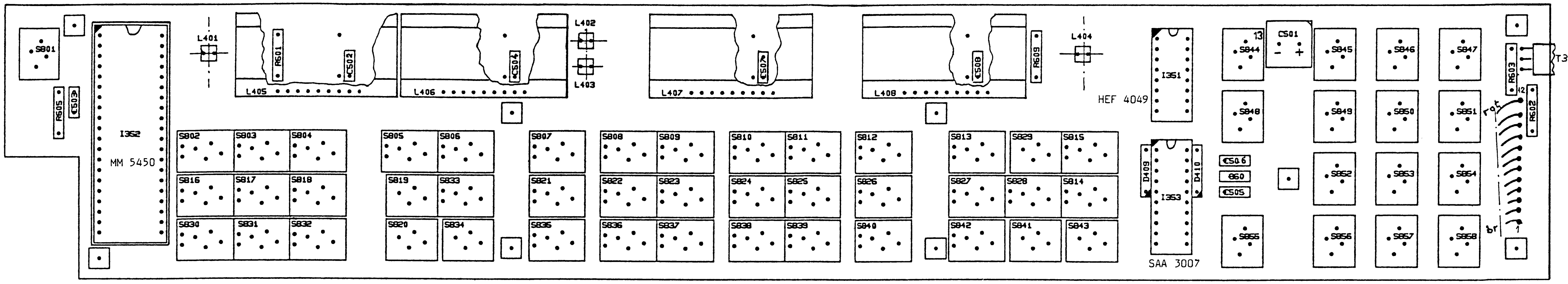
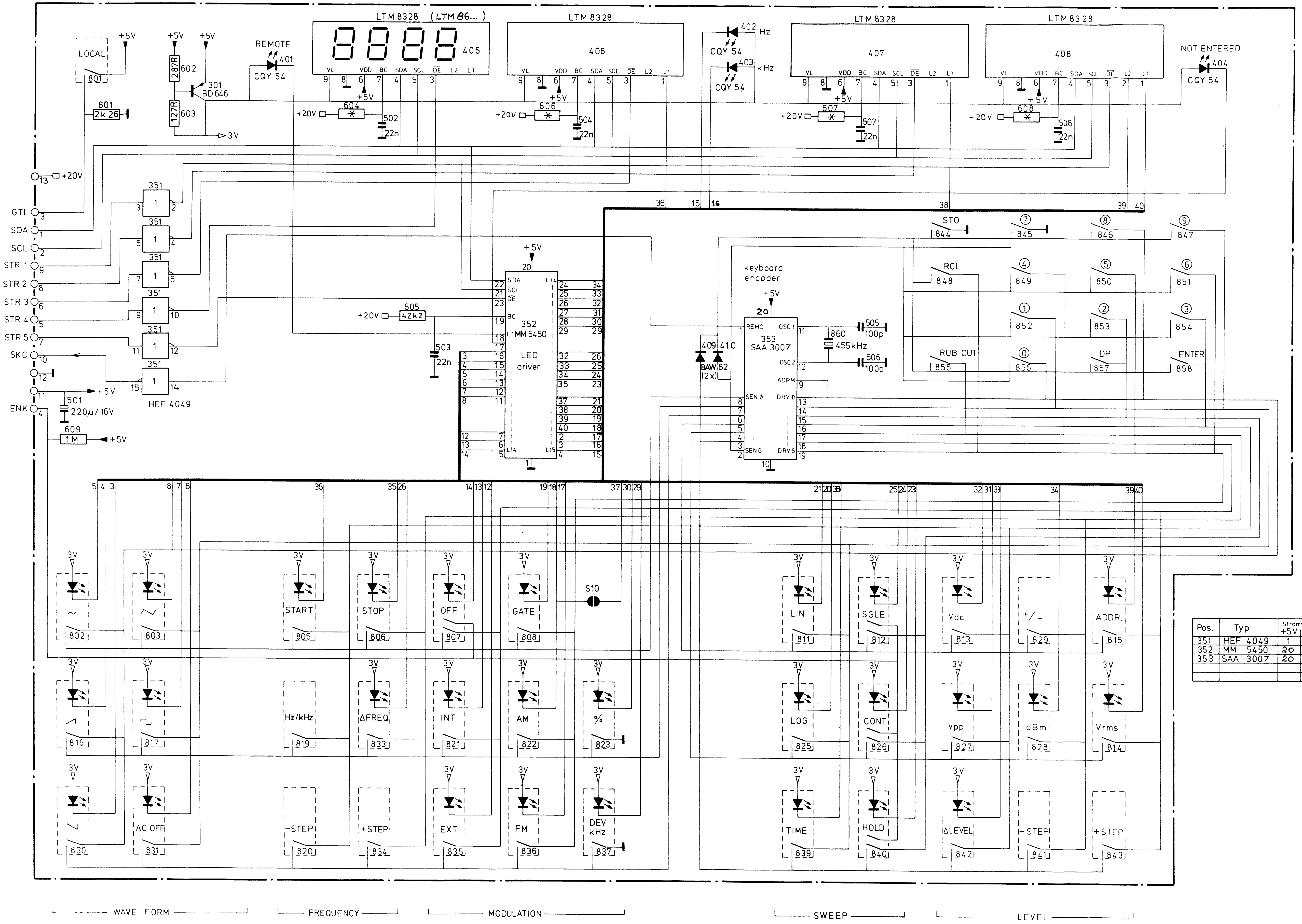
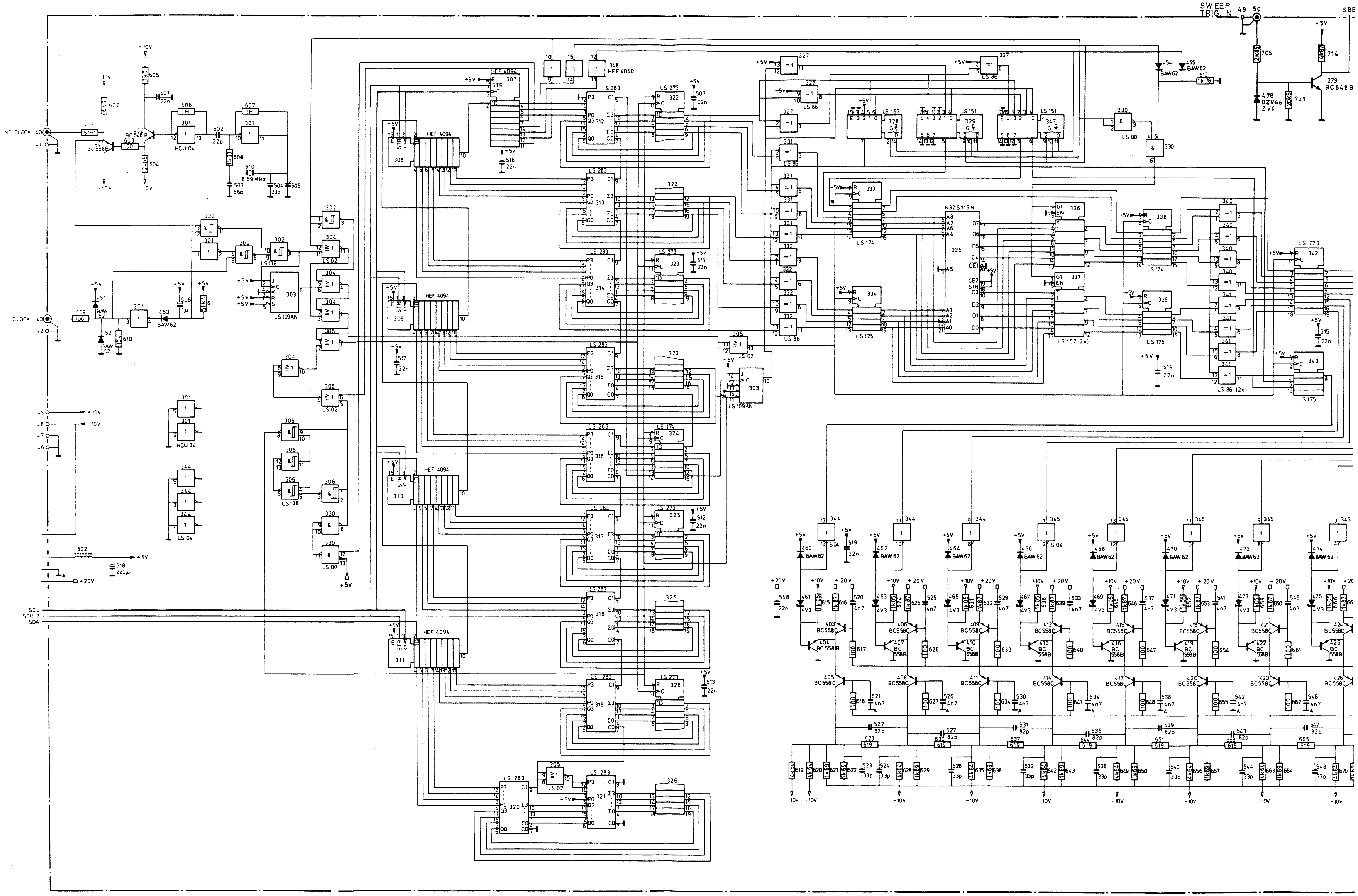

S801
R605
C503
I352
MM 5450
L401
R601
C502
L405
C504
L406
L402
L403
C507
L407
C508
L408
R609
L404
I351
HEF 4049
D409
I353
D410
SAA 3007
C506
860
C505
13
C501
- +
S844
S845
S846
S847
R603
T301
12
R602
rot
br
1
S848
S849
S850
S851
S852
S853
S854
S855
S856
S857
S858
S802
S803
S804
S805
S806
S807
S808
S809
S810
S811
S812
S813
S829
S815
S816
S817
S818
S819
S833
S821
S822
S823
S824
S825
S826
S827
S828
S814
S830
S831
S832
S820
S834
S835
S836
S837
S838
S839
S840
S842
S841
S843

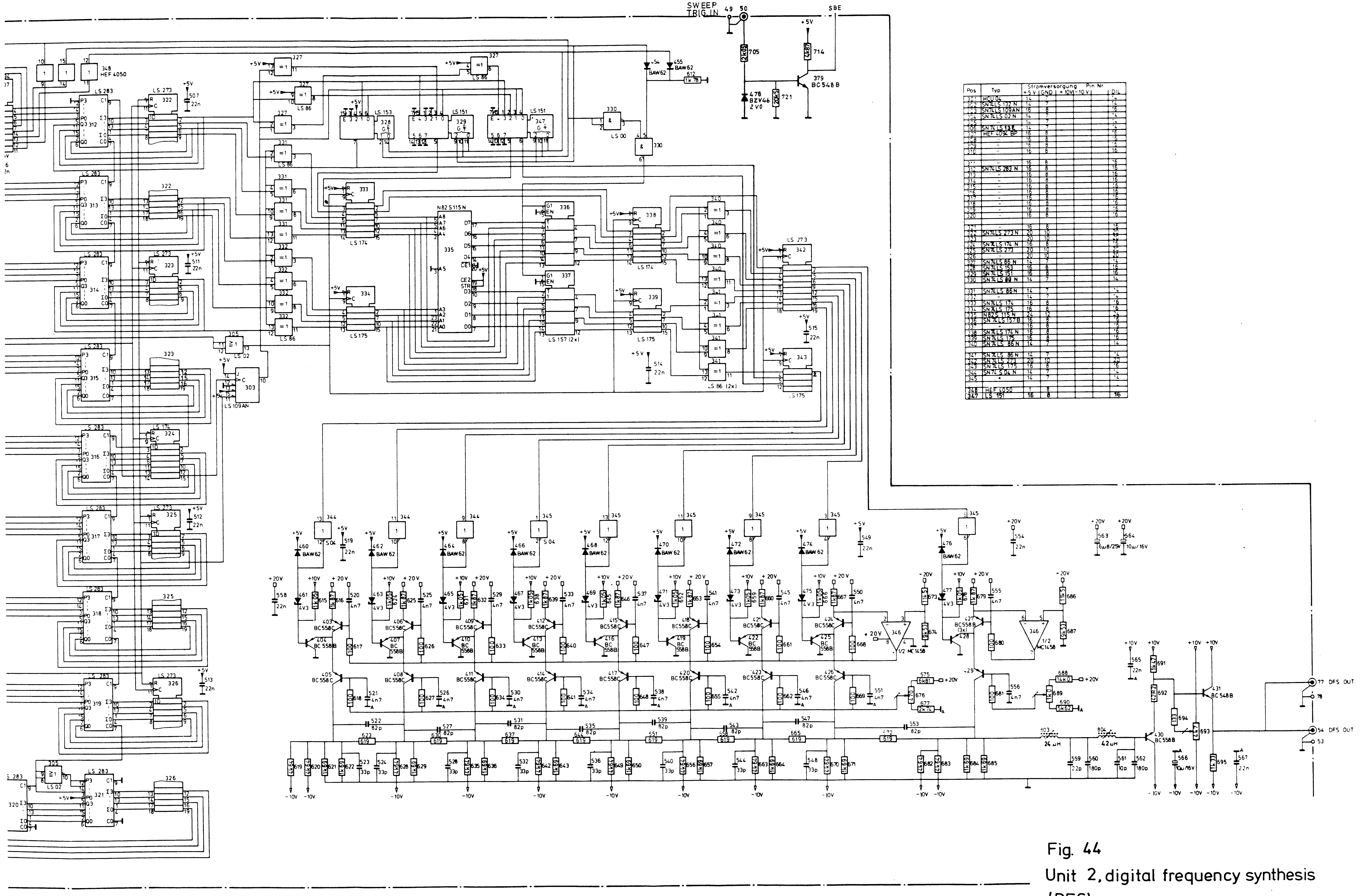
Fig. 42 Unit 3, Keyboard/display component lay-out


LTM 8328 (LTM 86...)
405
LTM 8328
406
LTM 8328
407
LTM 8328
408
VL VDD BC SDA SCL DE L2 L1
LOCAL
801
601
2k 26
+5V
287R
602
127R
603
301
BD 646
3V
REMOTE
401
CQY 54
402 Hz
403 kHz
CQY 54
NOT ENTERED
404
CQY 54
+20V
604
605
606
607
608
42k2
502
503
504
507
508
22n
13
GTL
SDA
SCL
STR 1
STR 2
STR 3
STR 4
STR 5
SKC
ENK
351
HEF 4049
501
220µ/16V
609
1M
352
MM 5450
LED driver
SDA SCL DE BC L1 L14 L15 L34
keyboard encoder
353
SAA 3007
REMO OSC 1 OSC 2 ADRM SEN 0 SEN 6 DRV 0 DRV 6
860
455kHz
505
506
100p
409
410
BAW 62 (2x)
STO 844
845
846
847
RCL 848
849
850
851
852
853
854
RUB OUT 855
856
DP 857
ENTER 858
802
803
START 805
STOP 806
OFF 807
GATE 808
S10
LIN 811
SGLE 812
Vdc 813
+/− 829
ADDR. 815
816
817
Hz/kHz 819
ΔFREQ 833
INT 821
AM 822
% 823
LOG 825
CONT 826
Vpp 827
dBm 828
Vrms 814
830
AC OFF 831
−STEP 820
+STEP 834
EXT 835
FM 836
DEV kHz 837
TIME 839
HOLD 840
ΔLEVEL 842
−STEP 841
+STEP 843
WAVE FORM
FREQUENCY
MODULATION
SWEEP
LEVEL

Pos.	Typ	Stromversg +5V	Stromversg GND	PIN DIL
351	HEF 4049	1	8	16
352	MM 5450	20	1	40
353	SAA 3007	20	10	20

Fig. 43 Unit 3, keyboard display


SWEEP TRIG IN
49
50
SBE
INT CLOCK 40
CLOCK 43
SCL
STR 7
SDA
+5V
+10V
-10V
+20V
HCU 04
LS 132
LS 109AN
LS 02
LS 00
LS 04
HEF 4094
HEF 4050
LS 283
LS 273
LS 174
LS 175
LS 86
LS 153
LS 151
LS 157 (2x)
LS 86 (2x)
N82 S 115 N
BC 558 B
BC 548 B
BC 558C
BZV46 2V0
BAW 62
8.59 MHz
220µ
22n
4n7
82p
33p
301
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
347
348
379


SWEEP TRIG IN
49
50
SBE
+5V
705
2k05
714
4k87
379
BC 548 B
478
BZV46
2V0
721
20k5
BAW62
612
1k78
HEF 4050
LS 283
LS 273
LS 174
LS 175
LS 86
LS 153
LS 151
LS 00
LS 02
LS 109AN
N82 S 115 N
LS 157 (2x)
LS 86 (2x)
S 04
BC558C
BC 558B
1/2 MC1458
BC548B
BC558B
24 µH
42 µH
77 DFS OUT
78
54 DFS OUT
53
+20V
+10V
-10V

Pos	Typ	Stromversorgung Pin Nr +5 V	GND	+10V	-10 V		DIL
301	HCU 04	14	7				14
302	SN74LS 132 N	14	7				14
303	SN74LS 109AN	16	8				16
304	SN 74LS 02 N	14	7				14
305	"	14	7				14
306	SN 74 LS 132	14	7				14
307	HEF 4094 BP	16	8				16
308	"	16	8				16
309	"	16	8				16
310	"	16	8				16
311	"	16	8				16
312	SN74LS 283 N	16	8				16
313	"	16	8				16
314	"	16	8				16
315	"	16	8				16
316	"	16	8				16
317	"	16	8				16
318	"	16	8				16
319	"	16	8				16
320	"	16	8				16
321	"	16	8				16
322	SN74LS 273 N	20	10				20
323	"	20	10				20
324	SN74LS 174 N	16	8				16
325	SN74LS 273	20	10				20
326	"	20	10				20
327	SN74LS 86 N	14	7				14
328	SN 74LS 153	16	8				16
329	SN 74 LS 151	16	8				16
330	SN74LS 00 N	14	7				14
331	SN74LS 86N	14	7				14
332	"	14	7				14
333	SN74LS 174	16	8				16
334	SN74LS 175	16	8				16
335	N82S 115 N	24	12				24
336	SN 74LS 157B	16	8				16
337	"	16	8				16
338	SN74LS 174 N	16	8				16
339	SN74LS 175	16	8				16
340	SN74LS 86 N	14	7				14
341	SN 74LS 86 N	14	7				14
342	SN74LS 273	20	10				20
343	SN 74LS 175	16	8				16
344	SN 74 S 04 N	14	7				14
345	"	14	7				14
348	HEF 4050	1	8				
347	LS 151	16	8				16

Fig. 44

Unit 2, digital frequency synthesis (DFS)

CODING SYSTEM OF FAILURE REPORTING FOR QUALITY ASSESSMENT OF T & M INSTRUMENTS

(excl. potentiometric recorders)

The information contents of the coded failure description is necessary for our computerized processing of quality data.

Since the reporting of repair and maintenance routines must be complete and exact, we give you an example of a correctly filled-out PHILIPS SERVICE Job sheet.

① *Country*	② *Day Month Year*	③ *Typenumber /Version*	④ *Factory/Serial no.*
3 2	1 5 0 4 7 5	O P M 3 2 6 0 0 2	D O 0 0 7 8 3

CODED FAILURE DESCRIPTION ⑥

⑤ *Nature of call*	*Location*	*Component/sequence no.*	*Category*
☐ Installation		T S 0 6 0 7	5
☐ Pre sale repair		R 0 0 6 3 1	2
☐ Preventive maintenance	0 0 2 1	9 9 0 0 0 1	4
☒ Corrective maintenance			
☐ Other			

⑦ *Job completed* ☒

Working time ⑧ 1 2 Hrs

Detailed description of the information to be entered in the various boxes:

①Country: 3 2 = Switzerland

②Day Month Year 1 5 0 4 7 5 = 15 April 1975

③Type number/Version O P M 3 2 6 0 0 2 = Oscilloscope PM 3260, version 02 (in later oscilloscopes this number is placed in front of the serial no)

④Factory/Serial number D O 0 0 7 8 3 = DO 783 These data are mentioned on the type plate of the instrument

⑤Nature of call: Enter a cross in the relevant box

⑥Coded failure description

Location

These four boxes are used to isolate the problem area. Write the code of the part in which the fault occurs, e.g. unit no or mechanical item no of this part (refer to 'PARTS LISTS' in the manual).

Example: 0001 for Unit 1
000A for Unit A
0075 for item 75

If units are not numbered, do not fill in the four boxes; see Example Job sheet.

Component/sequence no.

These six boxes are intended to pinpoint the faulty component.

A. Enter the component designation as used in the circuit diagram. If the designation is alfa-numeric, the letters must be written (starting from the left) in the two left-hand boxes and the figures must be written (in such a way that the last digit occupies the right-most box) in the four right-hand boxes.

B. Parts not identified in the circuit diagram:

990000 Unknown/Not applicable
990001 Cabinet or rack (text plate, emblem, grip, rail, graticule, etc.)
990002 Knob (incl. dial knob, cap, etc.)
990003 Probe (only if attached to instrument)
990004 Leads and associated plugs
990005 Holder (valve, transistor, fuse, board, etc.)
990006 Complete unit (p.w. board, h.t. unit, etc.)
990007 Accessory (only those without type number)
990008 Documentation (manual, supplement, etc.)
990009 Foreign object
990099 Miscellaneous

Category

0 Unknown, not applicable (fault not present, intermittent or disappeared)
1 Software error
2 Readjustment
3 Electrical repair (wiring, solder joint, etc.)
4 Mechanical repair (polishing, filing, remachining, etc.)
5 Replacement (of transistor, resistor, etc.)
6 Cleaning and/or lubrication
7 Operator error
8 Missing items (on pre-sale test)
9 Environmental requirements are not met

⑦Job completed: Enter a cross when the job has been completed.

⑧Working time: Enter the total number of working hours spent in connection with the job (excluding travelling, waiting time, etc.), using the last box for tenths of hours.

1 2 = 1,2 working hours (1 h 12 min.)